

SAT and SMT Solvers in Practice

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`http://www.cs.cmu.edu/~mheule/15816-f20/`
`https://github.com/marijnheule/sat-examples.git`

Automated Reasoning and Satisfiability
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DIMACS: SAT solver input format

The DIMACS format for SAT solvers has three types of lines:

- **header:** `p cnf n m` in which `n` denotes the highest variables index and `m` the number of clauses
- **clauses:** a sequence of integers ending with “0”
- **comments:** any line starting with “c ”

	c	example
$(a \vee b \vee \bar{c}) \wedge$	<code>p cnf 4 7</code>	
$(\bar{a} \vee \bar{b} \vee c) \wedge$	<code>1 2 -3 0</code>	
$(b \vee c \vee \bar{d}) \wedge$	<code>-1 -2 3 0</code>	
$(\bar{b} \vee \bar{c} \vee d) \wedge$	<code>2 3 -4 0</code>	
$(a \vee c \vee d) \wedge$	<code>-2 -3 4 0</code>	
$(\bar{a} \vee \bar{c} \vee \bar{d}) \wedge$	<code>1 3 4 0</code>	
$(\bar{a} \vee b \vee d)$	<code>-1 -3 -4 0</code>	
	<code>-1 2 4 0</code>	

DIMACS: SAT solver output format

The solution line of a SAT solver starts with “s ”:

- s SATISFIABLE: The formula is satisfiable
- s UNSATISFIABLE: The formula is unsatisfiable
- s UNKNOWN: The solver cannot determine satisfiability

In case the formula is satisfiable, the solver emits a certificate:

- lines starting with “v ”
- a list of integers ending with 0
- e.g. v -1 2 4 0

In case the formula is unsatisfiable, then most solvers support emitting a **proof of unsatisfiability** to a separate file

CaDiCaL: download and install

Most SAT solvers are implemented in C/C++

CaDiCaL is one of the strongest SAT solvers. As the name suggests it is based on CDCL. Recommended for Linux and macOS users.

obtain CaDiCaL:

- `git clone https://github.com/arminbiere/cadical.git`
- `cd cadical`
- `./configure; make`

to run: `./build/cadical formula.cnf`

SAT4J: download and install

SAT4J is a SAT solver in Java. It is also based on CDCL.
Recommended for windows users.

obtain SAT4J:

- `git clone`
`https://github.com/marijnheule/sat-examples.git`
- `cd sat-examples`

to run: `java -jar org.sat4j.core-2.3.1.jar formula.cnf`

UBCSAT: download and install

UBCSAT is a collection of local search SAT solvers.

obtain UBCSAT:

- download and unzip
`http://ubcsat.dtopkins.com/downloads/ubcsat-beta-12-b18.tar.gz`
- `cd ubcsat-beta-12-b18`
- `make clean; make`

to run: `./ubcsat -alg ddfw -i formula.cnf`

there are many LS algorithms to choose from (`-alg`)

Many SAT solvers

Many SAT solvers have been developed

Lots of them participate in the annual SAT competition

- All code of participants in open source
- Each solver is run on hundreds of benchmarks
- Large timeout 5000 seconds

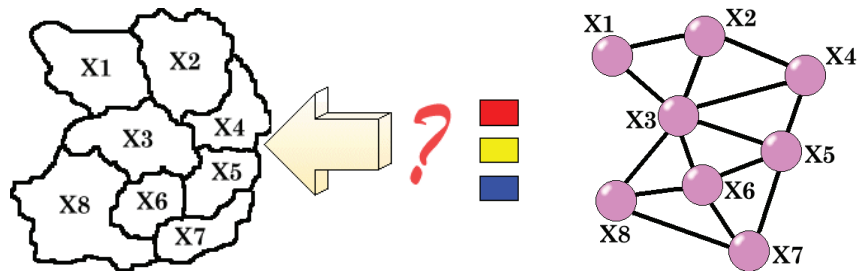
For details and downloading more solvers visit

<http://satcompetition.org/>

Demo: SAT Solving

Graph coloring

Given a graph $G(V, E)$, can the vertices be colored with k colors such that for each edge $(v, w) \in E$, the vertices v and w are colored differently.



Graph coloring encoding

Variables	Range	Meaning
$x_{v,i}$	$i \in \{1, \dots, c\}$ $v \in \{1, \dots, V \}$	node v has color i

Clauses	Range	Meaning
$(x_{v,1} \vee x_{v,2} \vee \dots \vee x_{v,c})$	$v \in \{1, \dots, V \}$	v is colored
$(\bar{x}_{v,s} \vee \bar{x}_{v,t})$	$s \in \{1, \dots, c-1\}$ $t \in \{s+1, \dots, c\}$	v has at most one color
$(\bar{x}_{v,i} \vee \bar{x}_{w,i})$	$(v, w) \in E$	v and w have a different color

Graph coloring encoding code

```
#include <stdio.h>
#include <stdlib.h>

int main (int argc, char** argv) {
    FILE* graph = fopen (argv[1], "r");
    int i, j, a, b, nVertex, nEdge, nColor = atoi (argv[2]);
    fscanf (graph, " p edge %i %i ", &nVertex, &nEdge);

    printf ("p cnf %i %i\n", nVertex * nColor, nVertex + nEdge * nColor);

    for (i = 0; i < nVertex; i++) {
        for (j = 1; j <= nColor; j++)
            printf ("%i ", i * nColor + j);
        printf ("\n"); }

    while (1) {
        int tmp = fscanf (graph, " e %i %i ", &a, &b);
        if (tmp == 0 || tmp == EOF) break;
        for (j = 1; j <= nColor; j++)
            printf ("-%i -%i 0\n", (a-1) * nColor + j, (b-1) * nColor + j);
    }
}
```

Demo: Encode, Decode

Unsatisfiable cores

An **unsatisfiable core** of an unsatisfiable formula F is a subset of F that is unsatisfiable.

An **minimal unsatisfiable core** of an unsatisfiable formula such that the removal of any clause makes the formula satisfiable.

Extracting a minimal unsatisfiable core from a formula has **many applications**, but the computational costs could be high.

- maxSAT
- diagnosis
- formal verification

Proofs

A **proof of unsatisfiability** is a certificate that a given formula is unsatisfiable.

Various proof producing methods exists (another lecture).

Proof checking tools cannot only validate a proof but also produce **additional information** about the formula:

- unsatisfiable core
- optimized proof

DRAT-trim is a tool that validates proofs and produces such information

Demo: Core Extraction

Tools for making SAT-based modeling easier

PySAT is a Python toolkit that makes it easier for users to call SAT solvers and build encodings using Python:

- <https://pysathq.github.io/>
- SAT solver is still written in C, C++
- Interface includes several encodings for linear constraints:
 - At-most-one constraints
 - Cardinality constraints
 - AIGER circuits to CNF
 - ...
- Well documented
- Active development

Demo: PySAT

SMT-LIB: SMT solver input format (I)

<http://smtlib.cs.uiowa.edu/>

Language has similarities with functional languages and it is more readable than CNF. Theories:

- Arrays,
- Bitvectors,
- Boolean predicates,
- Floating point,
- Ints,
- Reals

SMT-LIB: SMT solver input format (II)

<https://rise4fun.com/Z3/mpltR>

```
; Basic Boolean example
(set-logic QF_UF)
(declare-const p Bool)
(assert (and p (not p)))
(check-sat) ; returns 'unsat'
(exit)
```

SMT-LIB: SMT solver input format (III)

<https://rise4fun.com/Z3/DG4p1I>

```
; Integer arithmetic
(set-logic QF_LIA)
(declare-const x Int)
(declare-const y Int)
(assert (= (- x y) (+ x (- y) 1)))
(check-sat) ; returns 'unsat'
(exit)
```

SMT Solvers

- Z3 (Microsoft):
`https://github.com/Z3Prover/z3/wiki`
- CVC4 (Stanford):
`http://cvc4.cs.stanford.edu/web/`
- Yices (SRI): `http://yices.csl.sri.com/`
- Boolector (JKU Austria):
`https://boolector.github.io/`

SMT Solvers

We recommend the use of **Z3**:

- Tutorials:

 - <https://rise4fun.com/z3/tutorial>

 - <https://theory.stanford.edu/~nikolaj/programmingz3.html>

- APIs for Python, C++, Java

- MIT License: <https://github.com/Z3Prover/z3>

- Most used and cited SMT solver (>5,000 citations)

Demo: SMT solving

<https://rise4fun.com/z3/tutorial>

Proving program equivalence in SMT

```
1 int power3(int in)
2 {
3     int i, out_a;
4     out_a = in;
5     for (i = 0; i < 2; i++)
6         out_a = out_a * in;
7     return out_a;
8 }
```

```
1 int power3_new(int in)
2 {
3     int out_b;
4
5     out_b = (in * in) * in;
6
7     return out_b;
8 }
```

$$\varphi_a \equiv (\text{out0_a} = \text{in0_a}) \wedge (\text{out1_a} = \text{out0_a} \times \text{in0_a}) \wedge \\ (\text{out2_a} = \text{out1_a} \times \text{in0_a})$$

$$\varphi_b \equiv \text{out0_b} = (\text{in0_b} \times \text{in0_b}) \times \text{in0_b}$$

To show these programs are equivalent, we must show the following formula is valid: $\text{in0_a} = \text{in0_b} \wedge \varphi_a \wedge \varphi_b \implies \text{out2_a} = \text{out0_b}$

Demo: Program equivalence with SMT solving

Integers as mathematical integers:

<https://rise4fun.com/Z3/BLQp1>

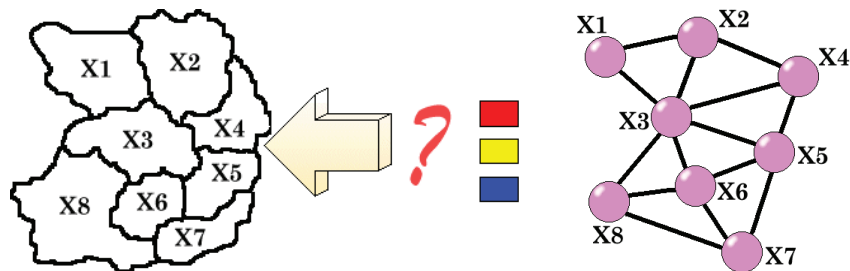
Integers as bit vectors:

<https://rise4fun.com/Z3/ibsw3>

Using uninterpreted functions:

<https://rise4fun.com/Z3/V7Sf>

Graph coloring encoding in SMT



Variables:

- Integer variables x_i for each node

Constraints:

- $1 \leq x_i \leq c$
- $x_i \neq x_j$ for $(x_i, x_j) \in E$

Graph coloring encoding code

```
from z3 import *
import sys

with open(sys.argv[1]) as f:
    content = f.readlines()

nodes=int(content[0].split()[2])
edges=int(content[0].split()[3])
s = Solver()
variables = []

for id in range(1,nodes+1):
    variables.append(Int('x'+str(id)))
    # each node can be assigned a value between 1 and k
    s.add(And(1 <= variables[id-1], variables[id-1] <= int(sys.argv[2])))

for line in content:
    if line[0]=='p':
        continue
    else:
        edge=line.split()
        # if two nodes are connected then they have different colors
        s.add((variables[int(edge[1])-1])!=(variables[int(edge[2])-1]))

s.check() # check if sat/unsat
print(s.model()) # print model
```

Demo: Encoding in SMT

Unsatisfiable cores in SMT

<https://rise4fun.com/Z3/VHDA>

```
; Getting unsatisfiable cores
(set-option :produce-unsat-cores true)
(set-logic QF_UF)
(declare-const p Bool) (declare-const q Bool) (declare-const r Bool)
(declare-const s Bool) (declare-const t Bool)
(assert (! ( $\Rightarrow$  p q) :named PQ))
(assert (! ( $\Rightarrow$  q r) :named QR))
(assert (! ( $\Rightarrow$  r s) :named RS))
(assert (! ( $\Rightarrow$  s t) :named ST))
(assert (! (not ( $\Rightarrow$  q s)) :named NQS))
(check-sat)
; unsat
(get-unsat-core)
; (QR RS NQS)
(exit)
```

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