Geometric Characterization of Series-Parallel Variable Resistor Networks*

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can produce results that are overly pessimistic, computing a larger range than is actually achievable, while at other times they produce results that are overly optimistic, computing a smaller range. In fact, existing programs can even fail to compute the correct result for fixed resistance networks.

sources of potentially differing current are connected in series. differing voltage are connected in parallel or where two current only under conditions where two voltage sources of potentially ically realizable series-parallel network. In particular, it fails infinite ones. The method derives exact results for any physbitrary, nonnegative resistance values are allowed, including elements: resistors, voltage sources, and current sources. Ar-The method handles networks of independent, variable linear bounds on the operating conditions of series-parallel networks. This paper describes an efficient method for computing exact efficient algorithms that at times err on the side of pessimism. algorithms that either work under restricted conditions, or for cient algorithm for this task exists. Instead, we must look for resistors is NP-complete [5]. Thus, it is unlikely that an effirange of voltages in an arbitrary network of variable, linear In earlier work, we have shown that computing the precise

2. Summary of Method

Our method operates by taking a geometric view of the set of possible network operating points. The possible Thevenin or Norton equivalent circuits for the network are viewed as points in a half plane. Thevenin equivalents having finite resistance are represented by points of the form $\langle R, V \rangle$, while by points of the form $\langle G, I \rangle$. We also introduce a class of infinite "Omega" points to represent infinite resistances and conductances. That is, the Thevenin equivalent of a current source is given by Omega point $\langle I \rangle \rangle$, while the Norton equivalent of a voltage source is given by Omega point $\langle V \rangle \rangle$. We extend conventional Euclidean geometry to include Omega points in a steady of the form $\langle S \rangle$ include the Norton equivalent of the Norton equivalent of a voltage source is given by Omega point $\langle V \rangle \rangle$. We extend conventional Euclidean geometry to include Omega points in a straightforward way.

Our main result is to show that the Thevenin or Norton equivalent of a series-parallel network containing k variable elements can be represented as a convex polygon of degree (i.e., number of vertices) less than or equal to 2k. Furthermore, if the network contains a total of n elements, this polygon can be comwork contains a total of n elements, this polygon can be com-

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Abstract—The range of operating conditions for a seriesparallel network of variable linear resistors, voltage sources, and current sources can be represented as a convex polygon in a Thevenin or Norton half plane. For a network with kvariable elements, these polygons have at most 2k vertices. By introducing a class of infinite points, we can also represent circuits with potentially infinite Thevenin resistance or Norton conductance.

1. Introduction

In analyzing a circuit under a range of operating conditions or parametric variations, three approaches are commonly followed. First, one can characterize the nominal behavior and express the effect of variations as sensitivities [2, 3]. Such an approach is appropriate only when the variations are small. Second, one can employ Monte Carlo methods to statistically characterize the effects of variations. Finally, one can develop bounding techniques that succinctly characterize the potential range of behaviors [8]. Bounding approaches have the advantage that they capture the full range of behaviors with a single computation, and that they do not overlook any extreme, although statistically improbable, cases.

This paper considers methods to bound the range of behaviors of variable resistor networks. This problem arises when modeling MOS circuits by linear switch-level simulation [6]. In while node, transistors are modeled as switched, linear resistors, while node voltages are approximated by logic values $\{0, 1, X\}$, where X indicates an unknown or potentially nondigital voltage. When a transistor gate node has value X, the transistor is assumed to have an arbitrary resistance greater than or equal to its value when fully on. The simulator must then compute the ranges of possible steady state voltages on the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations of the resistances to determine the nodes for all possible variations

Most linear switch-level simulators use simplistic methods to compute the possible voltage ranges [1, 6]. At times they

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Figure 2: Two Element Circuits and their Representations

Y axis in the Norton plane.

puted in time O(nk). Given such a polygon, one can easily determine the ranges of possible steady state voltages, currents, resistances, or conductances.

3. Thevenin and Norton Representations of Circuits

the Omega axis in the Thevenin plane or a segment along the the duals of those for a voltage source—either a segment along ductance. The representations of a current source (circuit B) are -non notion sin infinite Notion conplane as a line segment along the Omega axis having endpoints sistance is 0. The same source is represented in the Norton points (0, V_{min}) and (0, V_{max}) indicating that its Thevenin re-Thevenin plane as a line segment along the Y axis having end voltage interval $[V_{max}, V_{max}]$ (circuit A) is represented in the from that for real points. A voltage source varying over the that the vertical scale for Omega points will generally differ respond to infinite values of resistance or conductance. Note axis to the right of all real points; conceptually these points corthe right. We show the set of Omega points along a separately plane and conductance in the Norton) extends indefinitely far to Observe in this figure that the X axis (resistance in the Thevenin illustrates the representations of the allowed circuit elements. a circuit as the Thevenin and Norton half planes. Figure 1 We will refer to the two coordinate systems for representing

(i.e., an open circuit), the Thevenin representation would still be a segment, but the right hand endpoint would be the Omega point $\langle 0 \rangle$ and the segment would contain all real points $\langle R, 0 \rangle$ for R greater or equal to R_{min} . Similarly, for a resistor with $R_{min} = 0$ (i.e., a perfect conductor), the Norton representation would be a segment with right hand endpoint $\langle 0 \rangle \rangle$. Figure 2 illustrates the Thevenin and Norton representations of circuits consisting of a single variable cource and a sinfrom 0 to ∞). Observe that the Thevenin representation of voltage source ranging over the interval $[V_{min}, V_{max}]$ plus sevoltage source ranging over the interval $[V_{min}, V_{max}]$ plus serites resistor (circuit D) is a rectangle—the source voltage and voltage source ranging over the interval $[V_{min}, V_{max}]$ plus sevoltage source ranging over the interval $[V_{min}, V_{max}]$ plus sevoltage source ranging over the interval $[V_{min}, V_{max}]$ plus sevoltage source ranging over the interval $[V_{min}, V_{max}]$ plus sevoltage source ranging over the interval $[V_{min}, V_{max}]$ plus sevoltage source ranging over the interval $[V_{min}, V_{max}]$ plus se-

 $\infty = {}_{x n m} \mathcal{R}$ drive rotation of $(1/\mathcal{R}_{min}, 0)$. For a resistor with $\mathcal{R}_{n a x}$, $(0, {}_{x n m} \mathcal{R}/1)$

and $\langle R_{max}, 0 \rangle$, while in the Norton plane it has endpoints

In the Thevenin plane this segment has endpoints $\langle \Omega_{min}, 0 \rangle$

val $[R_{min}, R_{max}]$ (circuit C) is represented in both Thevenin and Norton planes as a horizontal line segment along the X axis.

A resistor varying over the (finite, nonzero) resistance inter-

tance is infinite, the circuit behaves as a variable voltage source,

Thevenin representation is that of an open circuit. The Norton representation of this circuit is more subtle. When the conduc-

indicating that when the resistance becomes infinite, the

the Thevenin resistance are independent, plus the Omega point



Figure 3: Parallel Combination of Networks



Figure 4: Contour Representation of a Polygon

- 2. For real point $\langle 0, y \rangle$: $\tau (\langle 0, y \rangle) = \langle \langle y \rangle \rangle$.
- 3. For omega point $\langle m \rangle : \tau (\langle m \rangle) = \langle 0, m \rangle$.

This operator has the properties that it preserves convexity and serves as its own inverse. In fact, the transform of a convex polygon is itself a convex polygon having as vertices the transformed form preserves vertical orderings of points, the transformed vertices of the upper (respectively, lower) contour become the upper (resp., lower) contour of the transformed polygon. The left to right ordering of the points in the two contours is reversed, however.

We combine polygons by pointwise addition, yielding either the Norton representation of two subnetworks connected in parallel or the Thevenin representation of two subnetworks connected in series. Addition of a real point with an Omega point yields the Omega point, corresponding to the property that the parallel combination of a voltage source with a circuit having finite conductance yields the voltage source, and similarly for the series combination of a current source with a circuit having finite resistance. Addition of two identical Omega points yields the same point. This corresponds to the

represented by a segment along the Omega axis. As the conductance becomes finite with tance is decreased, the Norton conductance becomes finite with the current range bounded by two lines with slopes V_{min} and V_{max} . As the conductance approaches zero, the Norton representation is that of an open circuit, i.e., the real point $\langle 0, 0 \rangle$. The dual case occurs for a variable current source in parallel with a variable resistance (circuit E).

Figure 3 illustrates the effect of combining several smaller networks of variable elements. Both networks $N_{\rm A}$ and $N_{\rm B}$ consist of voltage sources and series resistances. Hence their Thevenin representations are rectangular. However, when these networks are combined in parallel to form network N the overall form networks are combined in parallel to form network N the overall networks are combined in parallel to form network N the overall networks are combined in parallel to form network N the overall networks are combined in parallel to form network N the overall range of possible Thevenin equivalents is given by a hexagon. A method for deriving this polygon will be described in the next section. It involves transforming the two rectangles in the next section. It involves transforming the two rectangles in the ing the range of Norton equivalents for network N. This polygon ing the range of Norton equivalents for network N. This polygon is then transformed back to the Thevenin plane.

4. Computational Method

Each network element is represented as either a point (for a fixed element) or a line segment (for a variable element). Following the series-parallel structure of the network we construct polygonal representations of each subnetwork, converting to a Norton form for parallel connections and to a Thevenin form for series connections. As illustrated in Figure 4, a polygon is represented by its upper and lower contours, consisting of the set resented by its upper or lower boundary of the polygon. As this example illustrates, the final point in a contour may be an Omega point $\langle m \rangle \rangle$ (m = -0.25 in these cases). Such a point of the content and the preceding point of the contour may be an defines a polygon edge extending from the preceding point of the contour infinitely to the right and having slope $\langle m \rangle$).

We define a transform operation τ that converts between the Thevenin and Norton representations of a subnetwork as follows:

I. For real point
$$\langle x, y \rangle$$
 with $x > 0$: $\tau (\langle x, y \rangle) = \langle 1 \langle x, y, y \rangle$.



Figure 5: Contour Addition by Segment Merging

case where matching voltage (resp., current) sources are connected in parallel (resp., series). On the other hand, the sum of two distinct Omega points is undefined, corresponding to one of the error conditions described earlier.

preceding vertex defined by the next segment in the list. A remaining vertices are computed by adding the offset from the vertex the sum of the leftmost vertices of C_A and C_B. The segments in the sum. The upper contour $C_A + C_B$ has as leftmost an Omega point. The resulting list then becomes the set of We also eliminate any segments to the right of one containing segments as shown by the case labeled "merge" in the figure. line segments of matching slope, we combine these into single list in descending slope order. Where the two lists contain CB, we start by merging the two segment lists into a single may include an Omega point. To sum two contours $\mathbb{C}_{\widehat{A}}$ and order. As this figure illustrates, the final segment of a contour upper contour, the slopes of the segments will be in descreasing having the slope and length of an edge of the polygon. For an of line segments, as shown in the lower part of the figure, each The ordered list of vertices in a contour define an ordered set This process is illustrated in Figure 5 for two upper contours. easily be compute the upper and lower contours of their sum. Given the upper and lower contours of two polygons, one can

similar process is used for summing lower contours, except that the segment lists are in ascending slope order.

It can be shown that the sum of two convex polygons of degrees k_1 and k_2 is a convex polygon of degree less than or equal to $k_1 + k_2$. Computing this sum has complexity $O(k_1 + k_2)$. Thus, for a network of k variable elements, the Thevenin and Norton polygons will have degree at most 2k.

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We have analyzed a number of university and industrial MOS circuit designs to determine how often a series-parallel network solution technique could be employed [4]. Even assuming worst case conditions where all of the transistors are potentially conducting, we determined that over 90% of the node voltages could be computed by this means. Under more realistic operating conditions, we would expect the technique to be applicable for many of the remaining 10%.

For (two-port) networks that are not series-parallel, it can be shown by network tearing [7] that the range of Thevenin and Norton equivalents are also be given as polygons. However, these polygons may be concave and it appears they may have degree exponential in the number of variable elements.

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