Mutable Store

15-411/15-611 Compiler Design

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Today

- Pointers
- The Heap and pointers
- Arrays
- Length & bounds checking
- Elaboration of +=, etc.

Adding a pointer

Extend types

$$\tau ::= int \mid bool \mid \tau *$$

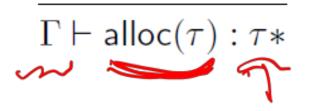
Extend expressions

alloc(\tau): allocate a heap cell to hold a value of τ e dereference a pointer to get value at e

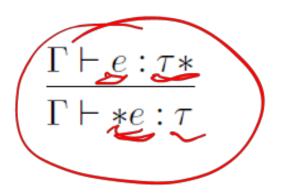
null: special null pointer

$$e ::= \dots \mid \mathsf{alloc}(\tau) \mid *e \mid \mathsf{null}$$

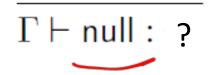
Typing rules



 A freshly allocated cell has type "pointer to τ"



• if e has type "pointer to τ ," then *e has type " τ " not quite



What type should null have?

- Desired behavior
 - allow any pointer to be compared to null
 - disallow pointer dereference of null





Equality for pointers?

- Can we compare τ^* and σ^* :
 - if τ = σ :
 - if $\tau \neq \sigma$:
 - What about int* p; ... if (p==null) ...
- null is given type of "any*"
- And, implicitly converted to τ^* as needed

- Desired behavior
 - allow any pointer to be compared to null
 - disallow pointer dereference to null
- Using the type "any*" along with subsumption
- Subsumption used for implicit coercion

$$\frac{\Gamma \vdash e : any *}{\Gamma \vdash null : any *}$$

Have to make sure introducing any* is safe

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$$\frac{\Gamma \vdash e : any *}{\Gamma \vdash null : any *}$$

• Can't allow *null

$$\frac{\Gamma \vdash e : \tau * \quad \Gamma \not\vdash e : any *}{\Gamma \vdash *e : \tau}$$

Typing rules (revised)

$$\Gamma \vdash \mathsf{alloc}(\tau) : \tau *$$

• A freshly allocated cell has type "pointer to τ "

$$\frac{\Gamma \vdash e : \tau * \quad \Gamma \not \vdash e : any *}{\Gamma \vdash *e : \tau}$$

if e has type "pointer to τ," and e isn't null, then *e has type "τ"

$$\Gamma \vdash \text{null} : any*$$

• null has the indefinite type

$$\frac{\Gamma \vdash e : any *}{\Gamma \vdash e : \tau *}$$

Implicit coercion

Representing the Heap

Evaluation of expression e in the context of

- a Heap,
- Stack, and
- binding environment.



$$H; S; \eta \vdash e \rhd K$$

• alloc(τ) returns an unused address in H (the heap) which can store a value of τ

What is an address?

- How do we represent addresses, i.e., the result of the alloc operation?
- 64-bits? infinite?
- What happens when we run out of memory? How do we model this in the dynamic semantics?

What is an address?

- How do we represent addresses, i.e., the result of the alloc operation?
- 64-bits? infinite?
- What happens when we run out of memory? How do we model this in the dynamic semantics?
- Assume infinite address space, i.e., an address is in \mathbb{N} .
- Out of heap memory will generate an exception: "exception(mem)"

Using H

- alloc(τ) returns an address of proper size (or raises an exception)
- H must keep track of next free address.

$$H: (\mathbb{N} \cup \{\text{next}\}) \rightarrow \text{Val}$$

• Extend all old rules with H; which they leave unchanged, e.g.,

$$H; S; \eta \vdash e_1 \oplus e_2 \rhd K \longrightarrow H; S; \eta \vdash e_1 \rhd (\blacksquare \oplus e_2, K)$$

Pointers

null evaluates to 0

$$H; S; \eta \vdash \text{null} \rhd K \longrightarrow H; S; \eta \vdash 0 \rhd K$$

- alloc(τ):
 - returns a fresh address(a,)
 - updates the next address in the heap
 - initializes the location to default for τ

$$H; S; \eta \vdash \text{alloc}(\tau) \triangleright K \rightarrow$$

$$H[a \mapsto \text{default}(\tau), \text{next} \mapsto \hat{a} + |\tau|]; S; \eta \vdash \hat{a} \triangleright K$$

$$a = H(\text{next})$$

$$H; S; \eta \vdash \text{alloc}(\tau) \rhd K \rightarrow$$

$$H[a \mapsto \text{default}(\tau), \text{next} \mapsto a + |\tau|]; S; \eta \vdash a \rhd K$$

$$a = H(\text{next})$$

- default(τ): 0 for int, false for bool, null for ptr
- $|\tau|$ for x86-64:
 - |int| = 4 |bool| = 4 $|\tau^*| = 8$

Accessing Memory

Dereferencing a pointer:

$$H; S; \eta \vdash *e \rhd K \longrightarrow H; S; \eta \vdash e \rhd (* , K)$$

Accessing Memory



Dereferencing a pointer:

$$H; S; \eta \vdash * e \rhd K \longrightarrow H; S; \eta \vdash e \rhd (* \nearrow K)$$

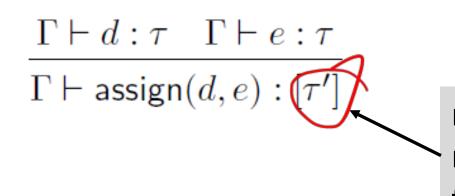
The interesting part:

$$H; S; \eta \vdash a \triangleright K \longrightarrow H; S; \eta \vdash H(a) \triangleright K \qquad a \neq 0$$
 $H; S; \eta \vdash a \triangleright K \longrightarrow \text{exception(mem)} \qquad a = 0$

- I-values and r-values
- I-values or destinations:

$$d := x | * d$$

Typing is the same for all destinations:



recall, $[\tau']$, is the return type of the function.

 Distinguish between variables, x, which live on the stack,

$$\begin{array}{lll} H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash \operatorname{assign}(x,e) \blacktriangleright K & \longrightarrow & H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash e \rhd (\operatorname{assign}(x,\underline{\ \ \ }) \hspace{0.1cm} , K) \\ H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash c \rhd (\operatorname{assign}(x,\underline{\ \ \ }) \hspace{0.1cm} , K) & \longrightarrow & H \hspace{0.1cm} ; S \hspace{0.1cm} ; \underline{\eta[x \mapsto c]} \rhd \operatorname{nop} \blacktriangleright K \end{array}$$

 Distinguish between variables, x, which live on the stack,

```
\begin{array}{lll} H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash \operatorname{assign}(x,e) \blacktriangleright K & \longrightarrow & H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash e \rhd (\operatorname{assign}(x,\underline{\ }) \hspace{0.1cm} , K) \\ H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash c \rhd (\operatorname{assign}(x,\underline{\ }) \hspace{0.1cm} , K) & \longrightarrow & H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta[x \mapsto c] \rhd \operatorname{nop} \blacktriangleright K \end{array}
```

 and other destinations which live in the heap.

```
\begin{array}{lll} H \ ; S \ ; \eta \vdash \mathsf{assign}(*d,e) \blacktriangleright K & \longrightarrow & H \ ; S \ ; \eta \vdash d \rhd (\mathsf{assign}(*\_,e) \ , K) \\ H \ ; S \ ; \eta \vdash a \rhd (\mathsf{assign}(*\_,e) \ , K) & \longrightarrow & H \ ; S \ ; \eta \vdash e \rhd (\mathsf{assign}(*a,\_) \ , K) \\ H \ ; S \ ; \eta \vdash c \rhd (\mathsf{assign}(*a,\_) \ , K) & \longrightarrow & H[a \mapsto c] \ S \ ; \eta \vdash \mathsf{nop} \blacktriangleright K & (a \neq 0) \\ H \ ; S \ ; \eta \vdash c \rhd (\mathsf{assign}(*a,\_) \ , K) & \longrightarrow & \mathsf{exception}(\mathsf{mem}) & (a = 0) \end{array}
```

left to right evaluation of address and r-value

$$\begin{array}{lll} H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash \operatorname{assign}(*d,e) \blacktriangleright K & \longrightarrow & H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash d \rhd (\operatorname{assign}(*_,e) \hspace{0.1cm} , K) \\ \\ H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash a \rhd (\operatorname{assign}(*_,e) \hspace{0.1cm} , K) & \longrightarrow & H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash e \rhd (\operatorname{assign}(*a,_) \hspace{0.1cm} , K) \end{array}$$

• Then making assignment (if $a \neq 0$)

left to right evaluation of address and rval

$$\begin{array}{lll} H \ ; S \ ; \eta \vdash a \operatorname{ssign}(*d,e) \blacktriangleright K & \longrightarrow & H \ ; S \ ; \eta \vdash d \rhd (\operatorname{assign}(*_{-},e) \ , K) \\ \\ H \ ; S \ ; \eta \vdash a \rhd (\operatorname{assign}(*_{-},e) \ , K) & \longrightarrow & H \ ; S \ ; \eta \vdash e \rhd (\operatorname{assign}(*a,_{-}) \ , K) \end{array}$$

• Then making assignment (if $a \neq 0$)

```
\begin{array}{lll} H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash c \rhd (\operatorname{assign}(*a,\_) \hspace{0.1cm} , K) & \longrightarrow & H[a \mapsto c] \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash \operatorname{nop} \blacktriangleright K & (a \neq 0) \\ H \hspace{0.1cm} ; S \hspace{0.1cm} ; \eta \vdash c \rhd (\operatorname{assign}(*a,\_) \hspace{0.1cm} , K) & \longrightarrow & \operatorname{exception}(\operatorname{mem}) & (a = 0) \end{array}
```

Proper evaluation order

```
• int* p = NULL;
                     H; S; 10 + assign (+P, 1/0);
 *p = 1/0;
                4:5/1-P; assign (+15,16);
                けらうかりは いるからりんのひり
• int**p = NULL;
 **p = 1/0;
              4) 5) 1 assim ( + 27) /) -
                      -47 ; acsna (+43) /1).
                     MP; *B; assyn (xxP)
```

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Arrays: static semantics

Extend types, expressions, and destinations

```
\begin{array}{lll} \tau & ::= & \dots \mid \underline{\tau[]} \\ e & ::= & \dots \mid \underline{\mathsf{alloc\_array}}(\tau,e) \mid e_1[e_2] \\ d & ::= & \dots \mid d[e] \end{array}
```

Need typing rules for alloc_array and e₁[e₂]

$$\frac{\Gamma \vdash e : \mathsf{int}}{\Gamma \vdash \mathsf{alloc_array}(\tau, e) : \tau[]} \qquad \frac{\Gamma \vdash e_1 : \tau[] \quad \Gamma \vdash e_2 : \mathsf{int}}{\Gamma \vdash e_1[e_2] : \tau}$$

Allocating the array

Allocating the array

$$H \; ; \; S \; ; \; \eta \vdash \mathsf{alloc_array}(\tau, e) \rhd K$$

$$\longrightarrow \qquad H \; ; \; S \; ; \; \eta \vdash e \rhd (\mathsf{alloc_array}(\tau, _) \; , K)$$

$$H \; ; \; S \; ; \; \eta \vdash n \rhd (\mathsf{alloc_array}(\tau, _) \; , K)$$

$$\longrightarrow \qquad H' \; ; \; S \; ; \; \eta \vdash a \rhd K \qquad (n \geq 0)$$

(n < 0)

Allocating the array



$$\begin{array}{ccc} H \; ; \; S \; ; \; \eta \vdash \mathsf{alloc_array}(\tau, e) \rhd K \\ &\longrightarrow & H \; ; \; S \; ; \; \eta \vdash e \rhd (\mathsf{alloc_array}(\tau, \underline{\ \ \ }) \; , K) \end{array}$$

$$H ; S ; \eta \vdash n \rhd (\mathsf{alloc_array}(\tau, _) , K)$$

$$H': S: \eta \vdash a \rhd K \qquad (n \geq 0)$$

$$a = H(\text{next})$$

$$H' = H[\underline{a + 0|\tau}] \mapsto \text{default}(\tau), \dots,$$

$$a + (n - 1)|\tau| \mapsto \text{default}(\tau), \text{next} \mapsto a + n|\tau|]$$

$$\longrightarrow$$
 exception(mem) $(n < 0)$

Accessing the Array

 left to right evaluation of base address of array and index

$$H; S; \eta \vdash e_1[e_2] \triangleright K \longrightarrow H; S; \eta \vdash e_1 \triangleright (_[e_2], K)$$

$$H; S; \eta \vdash a \triangleright (_[e_2], K) \longrightarrow H; S; \eta \vdash e_2 \triangleright (a[_], K)$$

Then, if in bounds, get the value

$$H \; ; S \; ; \eta \vdash i \rhd (a[_] \; , K) \qquad \longrightarrow \qquad H \; ; S \; ; \eta \vdash H(a+i|\tau|) \rhd K$$

$$a \neq 0, 0 \leq i < \mathsf{length}(a), a \; ; \tau[\;]$$

• Or, generate an exception

$$\longrightarrow$$
 exception(mem) $a = 0 \text{ or } i < 0 \text{ or } i \ge \text{length}(a)$

Accessing the Array

 left to right evaluation of base address of array and index

$$H; S; \eta \vdash e_1[e_2] \triangleright K \longrightarrow H; S; \eta \vdash e_1 \triangleright (\underline{-[e_2]}, K)$$

$$H; S; \eta \vdash a \triangleright (\underline{-[e_2]}, K) \longrightarrow H; S; \eta \vdash e_2 \triangleright (a[\underline{-]}, K)$$

• Then, if in bounds, get the value

$$H ; S ; \eta \vdash i \rhd (a[_] , K)$$
 \longrightarrow $H ; S ; \eta \vdash H(a+i \tau) \rhd K$
$$a \neq 0, 0 \leq i < \mathsf{length}(a), a : \tau[]$$

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 \longrightarrow exception(mem) $a = 0 \text{ or } i < 0 \text{ or } i \ge \text{length}(a)$

Accessing the Array

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• Then, if in bounds, get the value



$$H \; ; \; S \; ; \; \eta \vdash i \rhd (a[_] \; , K) \qquad \longrightarrow \qquad H \; ; \; S \; ; \; \eta \vdash H(\underline{a+i|\tau|}) \rhd K$$

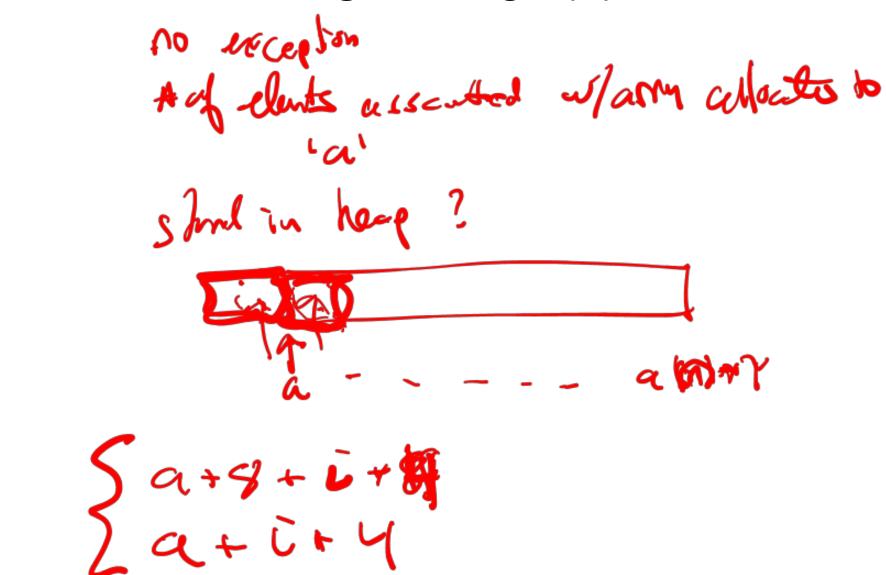
$$\boxed{a \neq 0, 0 \leq i < \mathsf{length}(a), a \; ; \; \tau[\;]}$$

Or, generate an exception

recall: alloc_array(τ ,e) \longrightarrow exception(mem) $a = 0 \text{ or } i < 0 \text{ or } i \ge \mathsf{length}(a)$

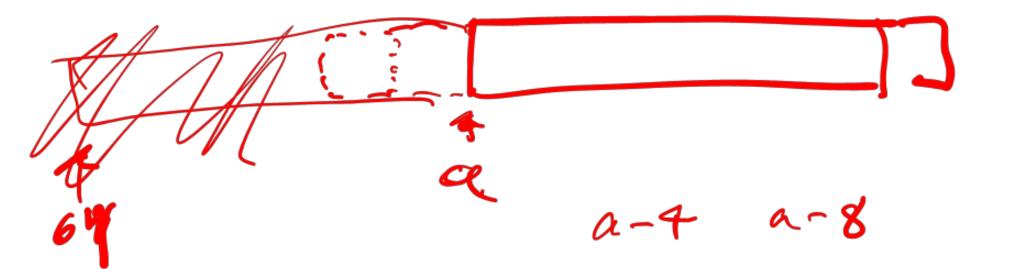
Bounds checking

Constraints in design of length(a)



Bounds checking

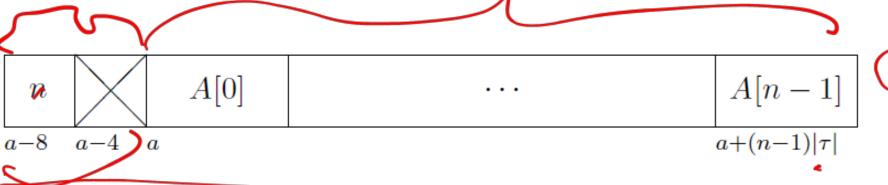
- Constraints in design of length(a)
- \longrightarrow Be able to find length of array given a
- O Minimize code size
 - Alignment (padding, etc.)
- Inter-operability



Bounds checking

• Must store length in heap.





Rationale for storing length at a-8?

Writing to the array

$$H; S; \eta \vdash \operatorname{assign}(d[e_2], e_3) \blacktriangleright K \longrightarrow H; S; \eta \vdash d \rhd (\operatorname{assign}([e_2], e_3), K)$$

$$H; S; \eta \vdash a \rhd (\operatorname{assign}([e_2], e_3), K) \longrightarrow H; S; \eta \vdash e_2 \rhd (\operatorname{assign}(a[], e_3), K)$$

$$H ; S ; \eta \vdash i \rhd (\operatorname{assign}(a[_], e_3), K) \longrightarrow H ; S ; \eta \vdash e_3 \rhd (\operatorname{assign}(a + i|\tau|, _), K)$$

$$a \neq 0, 0 \leq i < \operatorname{length}(a), a : \tau[]$$

$$\longrightarrow$$
 exception(mem)
 $a = 0 \text{ or } i < 0 \text{ or } i \ge \text{length}(a)$

$$H \; ; S \; ; \eta \vdash c \rhd (\mathsf{assign}(b, \underline{\ }) \; , K) \\ \hspace*{0.5in} \longrightarrow \hspace{0.5in} H[b \mapsto c] \; ; S \; ; \eta \vdash \mathsf{nop} \blacktriangleright K$$

one caveat

$$H ; S ; \eta \vdash \operatorname{assign}(d[e_2], e_3) \blacktriangleright K \longrightarrow H ; S ; \eta \vdash d \rhd (\operatorname{assign}(\underline{e_2}, e_3), K)$$

$$H \; ; S \; ; \eta \vdash a \rhd (\mathsf{assign}(\underline{\ }[e_2], e_3) \; , K) \qquad \longrightarrow \qquad H \; ; S \; ; \eta \vdash e_2 \rhd (\mathsf{assign}(a[\underline{\ }], e_3) \; , K)$$

$$H \; ; S \; ; \eta \vdash c \rhd (\mathsf{assign}(b, \underline{\ }) \; , K) \\ \hspace*{0.5in} \longrightarrow \hspace{0.5in} H[b \mapsto c] \; ; S \; ; \eta \vdash \mathsf{nop} \blacktriangleright K$$



7	n		A[0]	• • •	A[n-1]
a-	8	a-4 a			$a+(n-1) \tau $

Code Generation



• For access: $e_1[e_2]$ where $e_1:\tau[]$ and $|\tau|=k$

Hisjyt eilers 7K

fresh a

Code Generation

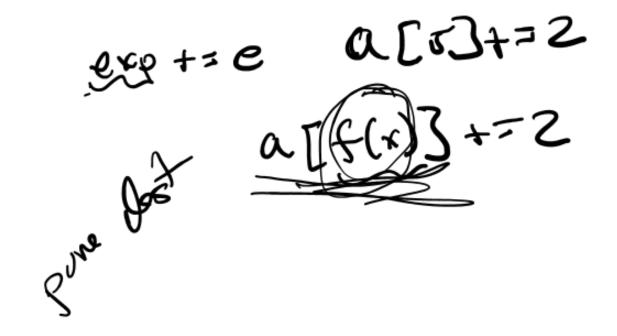
• For access: $e_1[e_2]$ where $e_1:\tau[]$ and $|\tau|=k$

$$\begin{array}{lll} \operatorname{cogen}(e_1,a) & (a\ \operatorname{new}) \\ \operatorname{cogen}(e_2,i) & (i\ \operatorname{new}) \\ a_1 \leftarrow a - 8 \\ t_2 \leftarrow M[a_1] \\ \text{if } (i < 0) \ \text{goto error} \\ \text{if } (i \geq t_2) \ \text{goto error} \\ a_3 \leftarrow i * \$k \\ a_4 \leftarrow a + a_3 \\ t_5 \leftarrow M[a_4] \\ \end{array}$$

not quite

Elaboration

• x = x + e is no longer always valid for x += e



Elaboration

- x = x + e is no longer always valid for x += e
- next time introduce structure and &