For: dwight
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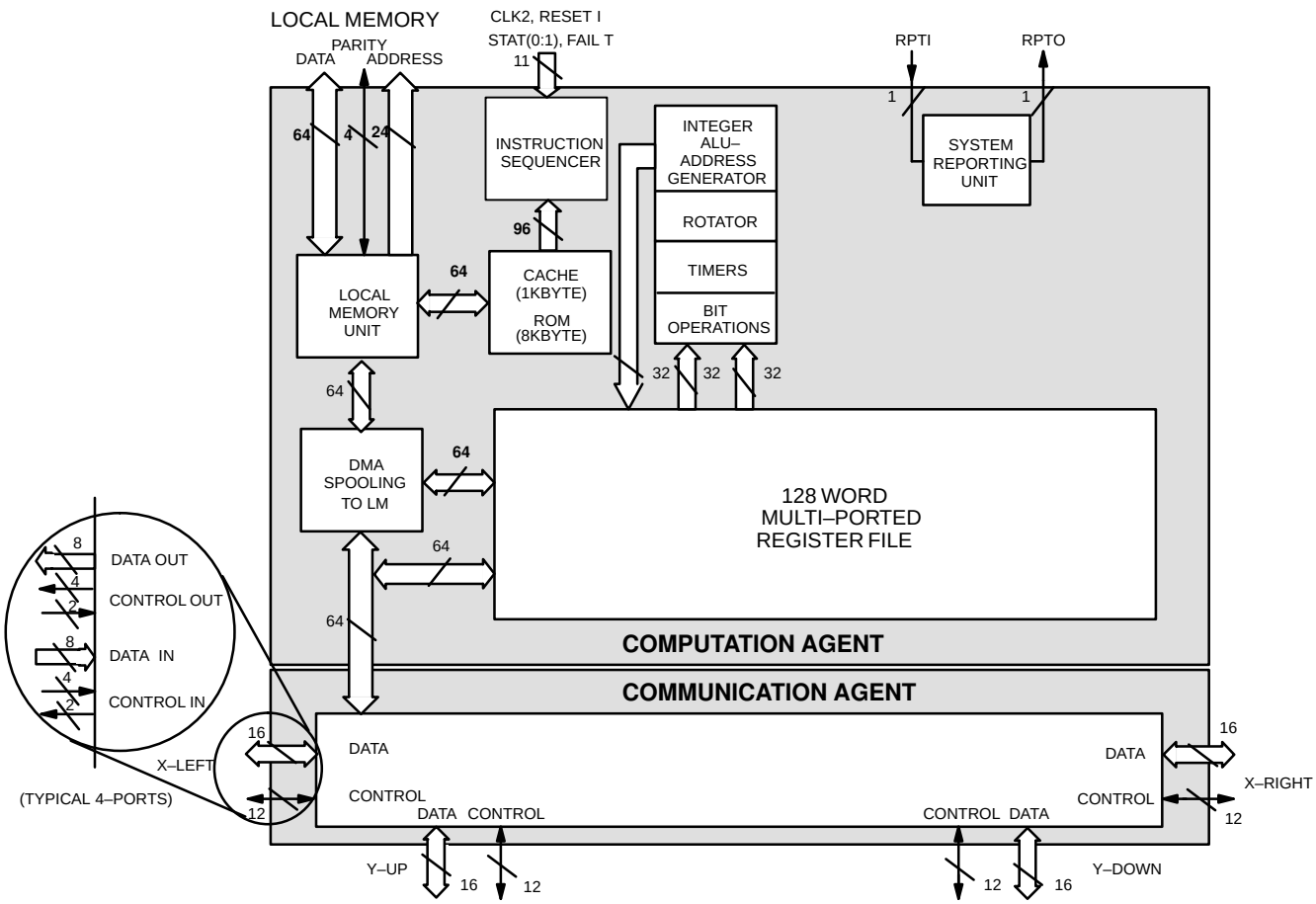
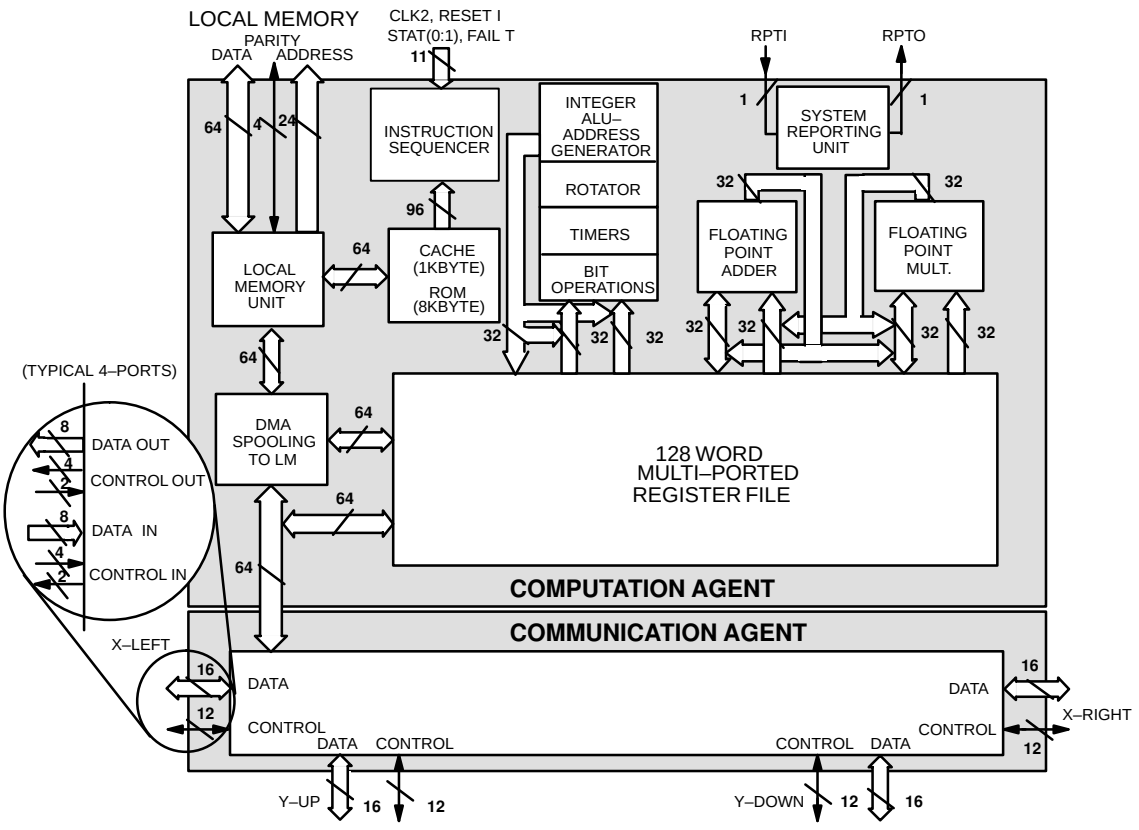

LOCAL MEMORY
CLK2, RESET I
STAT(0:1), FAIL T
PARITY
DATA
ADDRESS
11
RPTI
RPTO
1
1
64
4
24
INSTRUCTION SEQUENCER
INTEGER ALU– ADDRESS GENERATOR
SYSTEM REPORTING UNIT
ROTATOR
96
TIMERS
64
CACHE (1KBYTE)
ROM (8KBYTE)
LOCAL MEMORY UNIT
BIT OPERATIONS
32
32
32
64
DMA SPOOLING TO LM
64
128 WORD MULTI–PORTED REGISTER FILE
64
64
COMPUTATION AGENT
COMMUNICATION AGENT
8
DATA OUT
4
2
CONTROL OUT
8
DATA IN
4
2
CONTROL IN
X–LEFT
(TYPICAL 4–PORTS)
16
12
DATA
CONTROL
DATA
CONTROL
CONTROL
DATA
DATA
CONTROL
16
X–RIGHT
12
Y–UP
16
12
12
16
Y–DOWN


LOCAL MEMORY
PARITY
DATA
ADDRESS
CLK2, RESET I
STAT(0:1), FAIL T
RPTI
RPTO
INSTRUCTION SEQUENCER
INTEGER ALU– ADDRESS GENERATOR
ROTATOR
TIMERS
BIT OPERATIONS
SYSTEM REPORTING UNIT
FLOATING POINT ADDER
FLOATING POINT MULT.
LOCAL MEMORY UNIT
CACHE (1KBYTE) ROM (8KBYTE)
DMA SPOOLING TO LM
128 WORD MULTI–PORTED REGISTER FILE
(TYPICAL 4–PORTS)
DATA OUT
CONTROL OUT
DATA IN
CONTROL IN
COMPUTATION AGENT
COMMUNICATION AGENT
X–LEFT
X–RIGHT
DATA
CONTROL
Y–UP
Y–DOWN