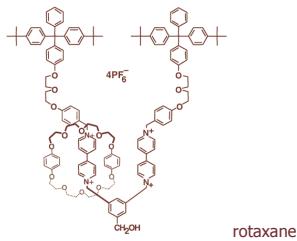


Electronically Configurable Molecular-Based Logic Gates

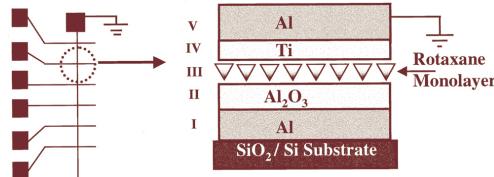
C. P. Collier, E. W. Wong, M. Belohradský, F. M. Raymo, J. F. Stoddart, P. J. Kuekes, R. S. Williams, and J. R. Heath, Science 1999 July 16; 285: 391-394.



© 2001 D.W. Greve

Electronically Configurable Molecular-Based Logic Gates

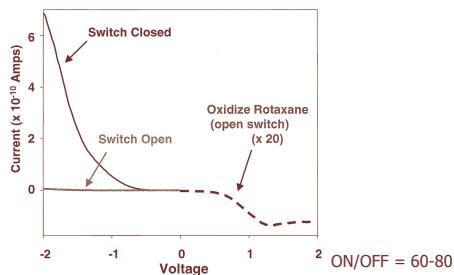
C. P. Collier, E. W. Wong, M. Belohradský, F. M. Raymo, J. F. Stoddart, P. J. Kuekes, R. S. Williams, and J. R. Heath, Science 1999 July 16; 285: 391-394.



© 2001 D.W. Greve

Electronically Configurable Molecular-Based Logic Gates

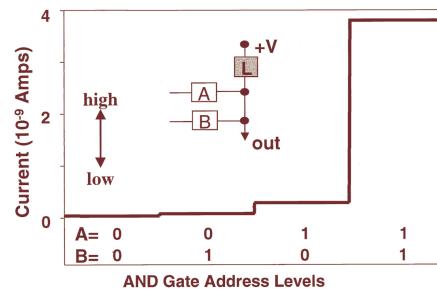
C. P. Collier, E. W. Wong, M. Belohradský, F. M. Raymo, J. F. Stoddart, P. J. Kuekes, R. S. Williams, and J. R. Heath, Science 1999 July 16; 285: 391-394.



© 2001 D.W. Greve

Electronically Configurable Molecular-Based Logic Gates

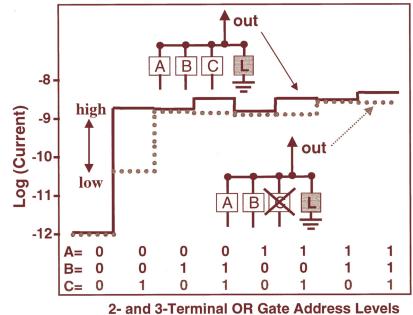
C. P. Collier, E. W. Wong, M. Belohradský, F. M. Raymo, J. F. Stoddart, P. J. Kuekes, R. S. Williams, and J. R. Heath, Science 1999 July 16; 285: 391-394.



© 2001 D.W. Greve

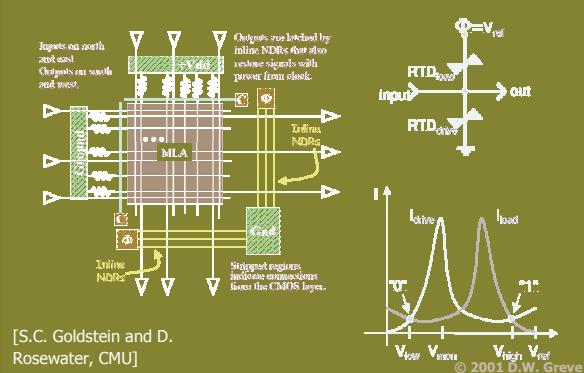
Electronically Configurable Molecular-Based Logic Gates

C. P. Collier, E. W. Wong, M. Belohradský, F. M. Raymo, J. F. Stoddart, P. J. Kuekes, R. S. Williams, and J. R. Heath, Science 1999 July 16; 285: 391-394.



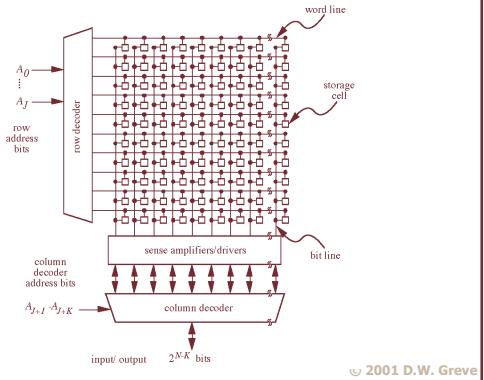
© 2001 D.W. Greve

Architectural concept

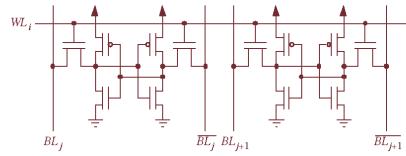


[S.C. Goldstein and D. Rosewater, CMU]

Array structures-memory

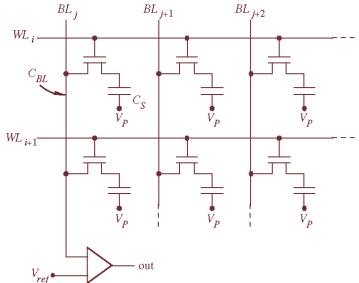


Array structures-SRAM



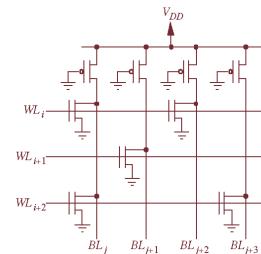
© 2001 D.W. Greve

Array structures-DRAM



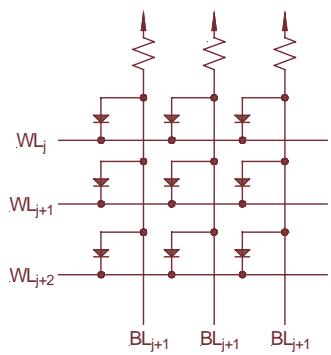
© 2001 D.W. Greve

Array structures-ROM (MOS)



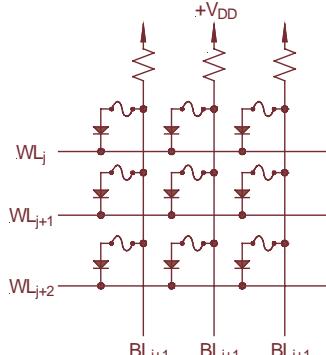
© 2001 D.W. Greve

Array structures-ROM (bipolar)



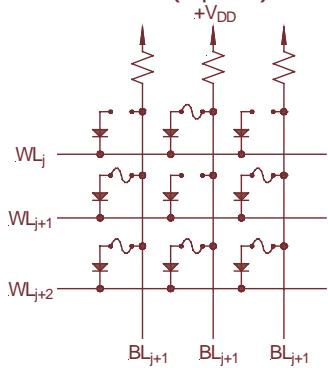
© 2001 D.W. Greve

Array structures-ROM (bipolar)



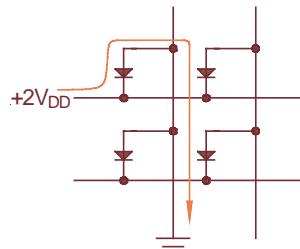
© 2001 D.W. Greve

Array structures-ROM (bipolar)



© 2001 D.W. Greve

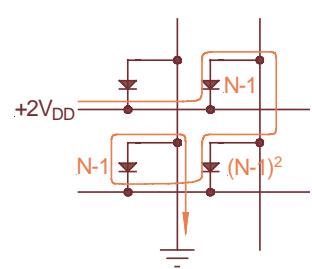
Sneak paths-writing



Assume programmable diode is written with twice read voltage in reverse direction

© 2001 D.W. Greve

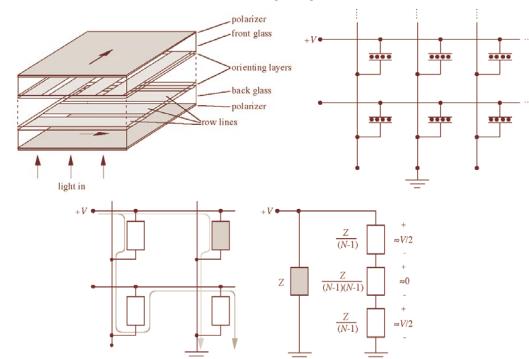
Other diodes during writing



Worst case- N-1 junctions are written with half writing voltage N-1 (?) times

© 2001 D.W. Greve

Passive matrix LCD display



© 2001 D.W. Greve

Summary/conclusions/cautions

- many devices/architectures under study
- nanodevices have a long way to go
- many practical problems which cannot be ignored

© 2001 D.W. Greve