Lecture 25:

Under the Hood, Part 1: Implementing Message Passing

Parallel Computer Architecture and Programming CMU 15-418/15-618, Fall 2019

Today's Theme



A. Y. OWEN Toponge they Shriving the Engine of His First Car, a 1971 Mercury

Two Perrose Contactions

Message passing model (abstraction)

- Threads operate within their own private address spaces
- Threads communicate by sending/receiving messages
 - send: specifies recipient, buffer to be transmitted, and optional message identifier ("tag")
 - receive: sender, specifies buffer to store data, and optional message identifier
 - Sending messages is <u>the only way</u> to exchange data between threads 1 and 2



Illustration adopted from Culler, Singh, Gupta

Message passing systems

- **Popular software library: MPI (message passing interface)**
- Hardware need not implement system-wide loads and stores to execute message passing programs (need only be able to communicate messages)
 - Can connect commodity systems together to form large parallel machine (message passing is a programming model for **clusters**)



Image credit: IBM





Cluster of workstations (Infiniband network)

Network Transaction



- **One-way transfer of information from a source output buffer to a** destination input buffer
 - causes some action at the destination
 - e.g., deposit data, state change, reply
 - occurrence is not directly visible at source

Shared Address Space Abstraction

Source



Fundamentally a two-way request/response protocol - writes have an acknowledgement

Destination

Read request

Memory access

Read response

Key Properties of SAS Abstraction

- Source and destination addresses are specified by source of the request
 - a degree of logical coupling and trust
- No storage logically "outside the application address space(s)"
 - may employ temporary buffers for transport
- **Operations are fundamentally request-response**
- **Remote operation can be performed on remote memory**
 - logically does not require intervention of the remote processor

Message Passing Implementation Options

Synchronous:

- Send completes after matching receive and source data sent
- Receive completes after data transfer complete from matching send

Asynchronous:

Send completes after send buffer may be reused



Synchronous Message Passing

Source



Data is not transferred until target address is known Limits contention and buffering at the destination Performance?

Destination

Receive(Psrc, local VA, len)

Asynchronous Message Passing: Optimistic Destination Source



Good news:

source does not stall waiting for the destination to receive

Bad news:

storage is required within the message layer (?)

Receive(Psrc, local VA, len)

Asynchronous Message Passing: Conservative

Source



- Where is the buffering?
- Contention control? Receiver-initiated protocol? What about short messages?

Destination

Key Features of Message Passing Abstraction



Source knows send address, destination knows receive address

- after handshake they both know both
- **Arbitrary storage "outside of the local address spaces"**
 - may post many sends before any receives
- **Fundamentally a 3-phase transaction**

Credit-Based Async Message Passing

Motivation:

- Optimistic is good for short messages (lower latency), BUT
- **Conservative** is safer in general (avoid buffer overflow)
- **Basic Idea (A Hybrid Approach):**
- pre-allocate limited amount of space ("credit") per sender
- if sender knows it has sufficient credit at a receiver:
 - it can go ahead and send the message optimistically
- otherwise, send the message conservatively
- **Tracking credit limit:**
 - decreased upon send; increases piggybacked with msgs

Challenge: Avoiding Fetch Deadlock

- Must continue accepting messages, even when cannot source msgs
 - what if incoming transaction is a request?
 - each may generate a response, which cannot be sent!
 - what happens when internal buffering is full?

Approaches:

- 1. Logically independent request/reply networks
 - physical networks
 - virtual channels with separate input/output queues
- 2. Bound requests and reserve input buffer space
 - K(P-1) requests + K responses per node
 - service discipline to avoid fetch deadlock?
- NACK on input buffer full 3.
 - NACK delivery?





Implementation Challenges: Big Picture

- **One-way transfer of information**
- No global knowledge, nor global control
 - barriers, scans, reduce, global-OR give fuzzy global state
- Very large number of concurrent transactions
- Management of input buffer resources
 - many sources can issue a request and over-commit destination before any see the effect
- Latency is large enough that you are tempted to "take risks"
 - e.g., optimistic protocols; large transfers; dynamic allocation