Resource Allocation for Data-Parallel Computing in Networks with Data Locality

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Abstract—This paper studies resource allocation for data-parallel applications in a networked computing system with data locality. Data-parallel computing tasks have two components: data and computation. To support efficient data-processing in the system, the resource allocation algorithm should jointly consider load-balancing, data transmissions and processing scheduling. In this paper, we consider a general model of a computing system where the computing system is a network represented by a graph, with nodes being computing devices or switches and edges being communication links. The data chunks stored at the nodes can be processed locally or be transmitted to other computing nodes in the network to be processed. The throughput of such a system for processing data-parallel applications is determined jointly by the computing capacity of the nodes and the communication capacity of the network, and the resource allocation algorithm should strike a right balance between computing and communication. In this paper, we propose a throughput-optimal resource allocation algorithm, which is able to control the tradeoff between the expected amount of data transmitted and the expected number of backlogged tasks in steady state through a parameter \(q_h\). We show that the gap between the expected data transmission rate under our algorithm and the optimal value is \(O(1/q_h)\), while the expected number of total backlogged tasks is upper bounded by \(O(q_h)\).

I. INTRODUCTION

Data-parallel computing frameworks, such as MapReduce/Hadoop and Dryad, provide essential infrastructure for big data analytics. Executing a data-processing task requires not only computation resource but also the availability of its corresponding input data, which is usually stored in the same computing system. The coupling between computation and data locality makes the resource allocation problem for data-parallel computing tasks intrinsically rich, and poses challenges to the design of high performance resource allocation algorithms, which has become an active research area (see, e.g., [1]–[9]).

When scheduling data-processing tasks, the system needs to balance the computation loads to maximize the computing throughput. Since each task has associated input data, if we place a task on a device that does not store the data initially, the device needs to obtain a copy of the data through the communication network, which incurs data transmissions in the network. Therefore, to make the computing system efficient, load-balancing, data transmissions and processing scheduling should be considered jointly.

Data locality used to be addressed by a crude model [1]–[6], [9]: data is classified coarsely as data in the same Java Virtual Machine (in Spark [3]), data on the same node, data on a different server on the same rack, and data not in the same rack. A more sophisticated approach is adopted in [8], which explicitly considers the topology and bandwidth of a communication network by assuming a tree topology.

In this paper, we consider a computing system where computing devices are connected by a communication network with a general topology. Specifically, we model the network by a graph, where the nodes are the computing devices and switches and the edges are the communication links between them. The computing nodes also store the data chunks to be processed by computing tasks. When a computing task is submitted to the system, it is first routed to one of the nodes that store its input data. Then the computing task is processed either at the node or is transmitted (together with the data chunk) to a different computing node for processing. Therefore, the resource allocation decisions in this system include where to route an incoming task, whether a node processes a task by itself or transmits the task to its neighbors.

In such a system, the computing throughput is determined by both the computing capacity of the devices and the communication capacity of the network. Our goal is to develop algorithms under which the system is stable for an as large as possible range of incoming traffic intensity, i.e., algorithms that achieve optimal throughput. On top of this, we also want the amount of data transmitted in the network to be minimized in order to minimize the usage of network bandwidth.

By looking at the resource allocation problem that minimizes data transmission subject to stability constraints, we propose a joint load balancing, data transmission and computing scheduling algorithm that is throughput optimal, i.e., it stabilizes the system for any incoming traffic as long as the traffic is supportable under some algorithm. Further, our proposed algorithm has a tuning parameter \(q_h\) that controls the tradeoff between the amount of transmitted data and the total number of backlogged tasks in steady state. We show
that in the steady state, the gap between the expected amount of transmitted data in each time slot under our algorithm and the optimal value is \( O(1/q_{th}) \), and the expected number of total backlogged tasks is upper bounded by \( O(q_{th}) \). Therefore, increasing this parameter makes the amount of transmitted data closer to the optimal value, but results in a possibly larger number of backlogged tasks.

II. RELATED WORK

The problem of scheduling data-processing tasks has attracted much attention since computing systems for large-scale data-processing such as MapReduce/Hadoop [1] were proposed. Besides the computation resources, data locality and the resultant usage of network resources due to data transmission also need to be taken into account. Data locality used to be addressed by a crude model: data is classified coarsely as data in the same JVM (in Spark [3]), data on the same node, data on a different server on the same rack, and data not in the same rack. Based on this model, both heuristic scheduling algorithms (e.g., [1]–[3]) and algorithms with theoretical performance guarantees (e.g., [5], [6], [9]) have been developed. The work [8] explicitly models a communication network with a tree topology, and a throughput optimal scheduling/routing algorithm is developed. The present paper considers a general network topology, which includes the system in [5], [6], [8], [9] as special cases, and further considers heterogeneous computing devices, data types and computing tasks. There is a connection between our approach and the packet scheduling problem that minimizes the path lengths between sources and destinations subject to stability constraints [10], [11], which improves the delay performance compared with the traditional back-pressure algorithm first proposed in the seminal work [12].

III. SYSTEM MODEL

We consider a computing system represented by a graph \( G = (\mathcal{N}, \mathcal{E}) \). The node set \( \mathcal{N} \) is the set of devices, which can be computing devices or switches, and the edge set \( \mathcal{E} \) is the set of communication links. The bandwidth of link \((m, n)\), i.e., the link from node \( m \) to node \( n \), is denoted by \( C_{(m, n)} \). Data chunks are stored on the computing nodes. Let \( \mathcal{D} \) denote the set of data chunks stored in the system, and \( \mathcal{C} \) denote the set of computing operations (e.g., sorting, searching, encryption/decryption). We classify the data into classes such that data chunks of the same class have the same size and require the same processing time for the same computing operation. Let \( \mathcal{K} \) denote the set of classes. We call a computing task a \((\alpha, c)\)-task if it processes a class-\( \alpha \) data chunk with operation \( c \). Let \( \alpha_d \) denote the class to which data chunk \( d \) belongs, \( l_\alpha \) the size of a data chunk in class \( \alpha \), and \( P_{\alpha, c, n} \) be node \( n \)’s computing capacity of processing \((\alpha, c)\)-tasks, e.g., the number of class-\( \alpha \) data chunks node \( n \) can serve with operation \( c \) in one time slot. Note that if node \( n \) is a switch, we let \( P_{\alpha, c, n} = 0 \) and follow the convention \( 0/0 = 0 \) since a switch does not have computing capacity. We also note that a switch node should be able to reach at least one computing node in the graph, which holds in a practical system. Furthermore, let \( S_d \) denote the set of nodes that store data chunk \( d \), and \( D_n \) the set of data chunks stored at node \( n \). The notation introduced above and used throughout the paper is summarized in Table I.

In the computing system considered in this paper, when a computing task \( c \) for data chunk \( d \) is requested, the request (also called a task) is first routed to one of the nodes who stores the data. Then, the computing task is processed either at the node or is transmitted (together with the data chunk) to a different node for processing.

Now let \( A_{d, c}[t] \) denote the number of computing tasks to process data \( d \) with operation \( c \) at time slot \( t \), where we assume that the random process \( (A_{d, c}[t]: t \geq 0) \) is independent and identically distributed (i.i.d.) across time slots. We further assume that \( A_{d, c}[t] \) has bounded mean and variance and has positive probability to be zero, and define \( \lambda_{d, c} = E[A_{d, c}[t]] \). Let \( \lambda_{d, c} = (\lambda_{d, c}, d \in \mathcal{D}, c \in \mathcal{C}) \). We say a computing traffic \( (\lambda_{d, c}) \) can be supported in the system if there exists some algorithm such that the number of unfinished tasks at time slot \( t \), denoted by \( \Phi[t] \), satisfies

\[
\lim_{C \to \infty} \lim_{t \to \infty} \Pr(\Phi[t] > C) = 0.
\]

The capacity region of the computing system consists of supported \((\lambda_{d, c})\)’s. Our first result characterizes the capacity region.

**Theorem 1.** If computing traffic \( (\lambda_{d, c}) \) can be supported in the system, then there exist \( S_d \), \( \lambda_{d, c, n} \geq 0 \), \( \mu_{\alpha, c, (m, n)} \geq 0 \) and \( \gamma_{\alpha, c, (m, n)} \geq 0 \) such that the following inequalities hold:

\[
\sum_{d: \alpha_d = \alpha} \lambda_{d, c, n} + \sum_{m: (m, n) \in \mathcal{E}} \mu_{\alpha, c, (m, n)} \geq 0.
\]

<table>
<thead>
<tr>
<th>( \mathcal{N} )</th>
<th>set of devices</th>
</tr>
</thead>
<tbody>
<tr>
<td>( \mathcal{E} )</td>
<td>set of communication links</td>
</tr>
<tr>
<td>( C_{(m, n)} )</td>
<td>bandwidth of link ((m, n))</td>
</tr>
<tr>
<td>( \mathcal{D} )</td>
<td>set of data stored in the network</td>
</tr>
<tr>
<td>( \mathcal{K} )</td>
<td>set of data classes</td>
</tr>
<tr>
<td>( \mathcal{C} )</td>
<td>set of computing operations</td>
</tr>
<tr>
<td>( \alpha_d )</td>
<td>class to which data chunk ( d ) belongs</td>
</tr>
<tr>
<td>( l_\alpha )</td>
<td>size of a data chunk in class ( \alpha )</td>
</tr>
<tr>
<td>( P_{\alpha, c, n} )</td>
<td>node ( n )’s computing capacity of processing a class-( \alpha ) data chunk with operation ( c )</td>
</tr>
<tr>
<td>( S_d )</td>
<td>set of nodes that store data chunk ( d )</td>
</tr>
<tr>
<td>( D_n )</td>
<td>set of data chunks stored at node ( n )</td>
</tr>
<tr>
<td>( A_{d, c}[t] )</td>
<td>number of computing tasks to process data ( d ) with operation ( c )</td>
</tr>
<tr>
<td>( \lambda_{d, c} )</td>
<td>( \lambda_{d, c} = E[A_{d, c}[t]] )</td>
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TABLE I: Notation Table
a setting, we consider the following optimization problem:

\[ \min_{\lambda, \mu, \gamma} \sum_{(m,n) \in \mathcal{E}} \sum_{\alpha} I_{\alpha,m,n} + \sum_{\alpha,c,m,n} \mu_{\alpha,c,m,n} \]

\[ \text{s.t.} \quad \sum_{d: \alpha d = \alpha} \lambda_{d,c,n} + \sum_{m: \alpha m = \alpha} \mu_{\alpha,c,m,n} \leq \sum_{k: (n,k) \in \mathcal{E}} \mu_{\alpha,c,n,k} + \gamma_{\alpha,c,n}, \quad \forall \alpha, c, n, \]

\[ \sum_{\alpha} \sum_{c} \gamma_{\alpha,c,n} \leq 1, \quad \forall n, \]

\[ \sum_{\alpha} \sum_{c} \lambda_{\alpha,m,n} \leq C_{(m,n)}, \quad \forall m, n, \]

\[ \sum_{\alpha} \lambda_{\alpha,c,n} = \lambda_{d,c}, \quad \forall d, c, \]

More specifically, define \( \Lambda \) to be the capacity region of the computing system. Then for any \( (\lambda_{d,c}) \in \Lambda \), there exist a set of \( \mathcal{S}_d \), \( \mu_{\alpha,c,m,n} \), \( \gamma_{\alpha,c,n} \) such that (1)–(4) hold. To facilitate understanding, we can regard \( \lambda_{d,c,n} \) as the average transmission rate of \( (\alpha,c) \)-tasks from node \( m \) to node \( n \), and \( \gamma_{\alpha,c,n} \) as the average processing rate of \( (\alpha,c) \)-tasks on node \( n \). Then (4) can be explained as a decomposition of the incoming traffic to the nodes that store the corresponding data. Inequality (1) states that the total incoming rate of \( (\alpha,c) \)-tasks to node \( n \) should be less than or equal to the total outgoing rate. The inequalities (2) and (3) are processing capacity and data transmission constraints, respectively.

We assume that \( \mathcal{S}_d \) is pre-determined, i.e., the distributed storage algorithm is independent of data-processing. This is common in nowadays big-computing systems such as MapReduce/Hadoop, where by default each data chunk is replicated three times and distributed to three different machines. The locations of the replicas do not change. In such a setting, we consider the following optimization problem:

\[ \min_{\sum_{(m,n) \in \mathcal{E}} \sum_{\alpha} \sum_{c} I_{\alpha,m,n} + \sum_{\alpha,c,m,n} \mu_{\alpha,c,m,n}} \sum_{d: \alpha d = \alpha} \lambda_{d,c,n} + \sum_{m: \alpha m = \alpha} \mu_{\alpha,c,m,n} \]

\[ \text{s.t.} \quad \sum_{k: (n,k) \in \mathcal{E}} \mu_{\alpha,c,n,k} + \gamma_{\alpha,c,n}, \quad \forall \alpha, c, n, \]

\[ \sum_{\alpha} \sum_{c} \gamma_{\alpha,c,n} \leq 1, \quad \forall n, \]

\[ \sum_{\alpha} \sum_{c} \lambda_{\alpha,m,n} \leq C_{(m,n)}, \quad \forall m, n, \]

\[ \sum_{\alpha} \lambda_{\alpha,c,n} = \lambda_{d,c}, \quad \forall d, c, \]

where the optimization variables are \( \lambda = (\lambda_{d,c,n}, d \in \mathcal{D}, c \in \mathcal{C}, n \in \mathcal{N}) \), \( \mu = (\mu_{\alpha,c,m,n}, \alpha \in \mathcal{K}, (m,n) \in \mathcal{E}) \) and \( \gamma = (\gamma_{\alpha,c,n}, \alpha \in \mathcal{K}, c \in \mathcal{C}, n \in \mathcal{N}) \). Let \( (\lambda^*, \mu^*, \gamma^*) \) denote an optimal solution of this problem. We remark that the objective function (5) is to minimize the data transmitted in the network while guaranteeing all computing tasks are fulfilled. The reason for adding this objective is that there are other applications on the same network that need communication bandwidth. So data transmission should be avoided unless the local node is heavily congested.

To develop a protocol to solve the problem above, we consider the following partial Lagrangian dual subject to constraint (6), where \( q_{\alpha,c,n} \) is the Lagrangian multiplier:

\[ \min_{\lambda, \mu, \gamma} \sum_{(m,n) \in \mathcal{E}} \sum_{\alpha} \sum_{c} I_{\alpha,m,n} + \sum_{\alpha,c,m,n} \mu_{\alpha,c,m,n} \]

\[ \text{s.t.} \quad \sum_{d: \alpha d = \alpha} \lambda_{d,c,n} + \sum_{m: \alpha m = \alpha} \mu_{\alpha,c,m,n} \leq \sum_{k: (n,k) \in \mathcal{E}} \mu_{\alpha,c,n,k} + \gamma_{\alpha,c,n}, \quad \forall \alpha, c, n, \]

\[ \sum_{\alpha} \sum_{c} \gamma_{\alpha,c,n} \leq 1, \quad \forall n, \]

\[ \sum_{\alpha} \sum_{c} \lambda_{\alpha,m,n} \leq C_{(m,n)}, \quad \forall m, n, \]

\[ \sum_{\alpha} \lambda_{\alpha,c,n} = \lambda_{d,c}, \quad \forall d, c, \]

= \min_{\lambda} \sum_{\alpha,c,n} q_{\alpha,c,n} \sum_{d: \alpha d = \alpha} \lambda_{d,c,n} \]

\[ - \max_{\mu} \sum_{\alpha,c,n} \mu_{\alpha,c,n} \gamma_{\alpha,c,n} \]

The equation above leads to the following algorithm including load-balancing, data transmissions and computing scheduling.

**Joint load-balancing, data transmissions and computing scheduling:** At each node \( n \), let \( Q_{\alpha,c,n}[t] \) denote the number of buffered computing tasks that process a class-\( \alpha \) data chunk with operation \( c \) at the beginning of time slot \( t \). Let \( Q[t] = (Q_{\alpha,c,n}[t], \alpha \in \mathcal{K}, c \in \mathcal{C}, n \in \mathcal{N}) \). When a computing task \( c \) for data \( d \) arrives, it is routed to a node \( n \) that achieves

\[ \min_{\alpha,c,n} Q_{\alpha,c,n}[t], \]

i.e., joining the shortest queue. At each link \((n,k)\), consider the \((\alpha,c)\) that achieves

\[ \max_{\alpha,c} \frac{1}{l_{\alpha}} (Q_{\alpha,c,n}[t] - Q_{\alpha,c,k}[t]) - q_{th}, \]

where \( q_{th} \) is a positive parameter. Then node \( n \) transmits computing task in queue \( Q_{\alpha,c,n} \) together with the associated data to node \( k \) when \( (Q_{\alpha,c,n}[t] - Q_{\alpha,c,k}[t])/l_{\alpha} - q_{th} > 0 \). In the performance analysis to be presented in the next section, we will see that the parameter \( q_{th} \) controls the tradeoff between the amount of transmitted data and the total queue length. Furthermore, node \( n \) selects tasks in \( Q_{\alpha,c,n} \) to process such that \((\alpha,c)\) achieves

\[ \max_{\alpha,c} P_{\alpha,c,n} Q_{\alpha,c,n}[t]. \]

Note that under this algorithm, the queueing process \((Q[t]: t \geq 0)\) is a Markov chain. Let its state space be the set of states reachable from the initial state.
IV. PERFORMANCE ANALYSIS

In this section, we analyze the performance of the proposed algorithm. Specifically, we focus on its throughput performance and the tradeoff between the amount of transmitted data and the total queue length, which is closely related to the delay performance.

We first derive the dynamics of the queueing system. At the beginning of each time slot $t$, each node $n$ checks the lengths of the queues on it, i.e., obtains $(Q_{\alpha,c,n}[t], \alpha \in C, c \in C)$. Based on the queue-length information, incoming computing tasks are routed to nodes according to (10). Let $A_{d,c,n}[t]$ denote the number of incoming computing tasks to process data $d$ with operation $c$ that are routed to node $n$. Then each node makes decisions on what tasks (together with the associated data) to transmit and what tasks to process according to (11) and (12), respectively. The transmitted tasks will be received at the end of the time slot. Let $S_{\alpha,c,n}[t]$ denote the number of tasks processed from queue $Q_{\alpha,c,n}$ at time slot $t$, and $S^I_{\alpha,c,(n,k)}[t]$ denote the number of tasks transmitted from queue $Q_{\alpha,c,n}$ to queue $Q_{\alpha,c,k}$ at time slot $t$. Then the queues evolve according to the following equation:

$$Q_{\alpha,c,n}[t+1] = Q_{\alpha,c,n}[t] + \sum_{d:a_d=\alpha} A_{d,c,n}[t] - S_{\alpha,c,n}[t] - \sum_{k:(n,k) \in E} S^I_{\alpha,c,(n,k)}[t] + \sum_{m:(m,n) \in E} S^I_{\alpha,c,(m,n)}[t].$$

(13)

**Theorem 2.** For any traffic $(\lambda_{d,c}) \in \text{int}(\Lambda)$, where $\text{int}(\Lambda)$ is the interior of $\Lambda$, the Markov chain $(Q[t]: t \geq 0)$ is positive recurrent. Further,

$$\lim_{t \to \infty} \mathbb{E} \left[ \sum_{\alpha,c,n} Q_{\alpha,c,n}[t] \right] \leq B_1 qth + B_2$$

(14)

and

$$\lim_{t \to \infty} \mathbb{E} \left[ \sum_{\alpha,c,n} \sum_{(n,k) \in E} l_{\alpha} S^I_{\alpha,c,(n,k)}[t] \right] \leq \sum_{\alpha,c,n} \sum_{(n,k) \in E} l_{\alpha} \mu^*_{\alpha,c,n} + B_3 \frac{1}{qth},$$

(15)

for some positive $B_1$, $B_2$ and $B_3$, where $\mu^*$ together with $\lambda^*$ is an optimal solution of (5) subject to constraints (6)–(9).

It can be verified that the Markov chain $(Q[t]: t \geq 0)$ is irreducible and aperiodic. Then the positive recurrence implies that the traffic $(\lambda_{d,c})$ can be supported in the system by the proposed algorithm since the distribution of the Markov chain converges to the unique stationary distribution. Since the algorithm supports any traffic strictly within the capacity region, we say that the algorithm is throughput optimal.

The bounds in (14) and (15) show that the parameter $q_{th}$ controls the tradeoff between the amount of transmitted data and the total queue length. Specifically, if we use smaller $q_{th}$, then the total queue length becomes smaller but the gap between the amount of transmitted data in the system and the optimal value becomes larger. This is consistent with our intuition: smaller $q_{th}$ means more data transmission but tasks have a better chance finding a less loaded node.

**Proof.** We will prove Theorem 2 for the case that all the nodes are computing devices to highlight the main idea. But note that the theorem holds for the general case.

We first prove the positive recurrence and the bound (14). Consider any traffic $(\lambda_{d,c}) \in \text{int}(\Lambda)$. Then there exists an $\epsilon > 0$ such that $(1+\epsilon)(\lambda_{d,c}) \in \Lambda$. We use the Foster-Lyapunov theorem [13] to prove positive recurrence. Consider the Lyapunov function $V: \mathbb{Z}_+^{\text{K}+\text{C}+\text{N}} \to \mathbb{R}_+$ defined by

$$V(Q) = \sum_{\alpha,c,n} Q^2_{\alpha,c,n}.$$  

(16)

Then the drift can be written as

$$\mathbb{E}[V(Q[t+1]) - V(Q[t]) | Q[t]]$$

$$= 2\mathbb{E} \left[ \sum_{\alpha,c,n} \sum_{d:a_d=\alpha} Q_{\alpha,c,n}[t] A_{d,c,n}[t] - \sum_{\alpha,c,n} Q_{\alpha,c,n}[t] S_{\alpha,c,n}[t] - \sum_{\alpha,c,n} Q_{\alpha,c,n}[t] \sum_{k:(n,k) \in E} S^I_{\alpha,c,(n,k)}[t] + \sum_{\alpha,c,n} Q_{\alpha,c,n}[t] \sum_{m:(m,n) \in E} S^I_{\alpha,c,(m,n)}[t] \right] Q[t]$$

$$+ C_1,$$ 

where $C_1$ is a constant independent of $Q[t]$. Next we analyze the summands on the right hand side.

For the summand corresponding to incoming tasks, there holds

$$\mathbb{E} \left[ \sum_{\alpha,c,n} \sum_{d:a_d=\alpha} Q_{\alpha,c,n}[t] A_{d,c,n}[t] \right] Q[t]$$

$$= \mathbb{E} \sum_{c,d} \sum_{n:m \in S_d} A_{d,c,n}[t] \min_{n:n \in S_d} Q_{a_d,c,n}[t] Q[t]$$

$$= \mathbb{E} \left[ \sum_{c,d} A_{d,c}[t] \min_{n:n \in S_d} Q_{a_d,c,n}[t] Q[t] \right]$$

(17)

where (17) follows from the join-the-shortest-queue policy. Since $(\lambda_{d,c}) \in \text{int}(\Lambda)$, by (1)–(4), there exist nonnegative
\[ \lambda_{d,c,n} \text{'s such that} \]
\[ \sum_{n \in S_d} \lambda_{d,c,n} = \lambda_{d,c}. \tag{18} \]

Therefore,
\[ E \left[ \sum_{\alpha, c, n} \sum_{d : a_\alpha = a c} Q_{\alpha, c, n}[t] A_{d, c, n}[t] \right] Q[t] \]
\[ \leq \sum_{c, d} \sum_{n \in S_d} \lambda_{d, c, n} Q_{d, c, n}[t] \]
\[ = \sum_{\alpha, c, n} \sum_{d : a_\alpha = a c} \lambda_{d, c, n} Q_{\alpha, c, n}[t]. \tag{19} \]

For the summand corresponding to task processing, there holds
\[ E \left[ - \sum_{\alpha, c, n} Q_{\alpha, c, n}[t] S_{\alpha, c, n}[t] \right] Q[t] \]
\[ = E \left[ - \sum_{n} Q_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] \right] Q[t], \tag{20} \]
where
\[ (\alpha^*, c^*, \gamma) \in \arg \max_{(\alpha, c)} P_{\alpha, c, n} Q_{\alpha, c, n}[t], \]
and the equation follows from the processing scheduling policy in (12). By the boundedness of the processing and transmission capacity, we have
\[ - Q_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] \]
\[ \leq - Q_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] P_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}} + C_2, \]
where \( C_2 \) is a constant. By the definition of the capacity region \( \Lambda \), there exist nonnegative \( \gamma_{\alpha, c, n} \)'s such that for any node \( n \),
\[ \sum_{\alpha, c} \gamma_{\alpha, c, n} \leq \frac{1}{1 + \epsilon}. \tag{21} \]

Then
\[ - Q_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] P_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}} \]
\[ = - \frac{\epsilon}{1 + \epsilon} Q_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] P_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}} \]
\[ \leq - \frac{\epsilon}{1 + \epsilon} \frac{1}{|\mathcal{K}|C} \sum_{\alpha, c, n} P_{\alpha, c, n} Q_{\alpha, c, n}[t] \]
\[ - \sum_{\alpha, c} \gamma_{\alpha, c, n} Q_{\alpha, c, n}[t] P_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}} \]
\[ \leq - \frac{\epsilon}{1 + \epsilon} \frac{1}{|\mathcal{K}|C} \sum_{\alpha, c} P_{\alpha, c, n} Q_{\alpha, c, n}[t] \]
\[ - \sum_{\alpha, c} \gamma_{\alpha, c, n} Q_{\alpha, c, n}[t], \]
and therefore,
\[ E \left[ - \sum_{\alpha, c, n} Q_{\alpha, c, n}[t] S_{\alpha, c, n}[t] \right] Q[t] \]
\[ \leq - \sum_{\alpha, c} \gamma_{\alpha, c, n} Q_{\alpha, c, n}[t] - \frac{\epsilon}{1 + \epsilon} \frac{1}{|\mathcal{K}|C} \sum_{\alpha, c} P_{\alpha, c, n} Q_{\alpha, c, n}[t] + nC_2. \tag{22} \]

For the summands corresponding to task transmission, there holds
\[ E \left[ - \sum_{\alpha, c, n} Q_{\alpha, c, n}[t] \sum_{k : (n,k) \in E} S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] \right] Q[t] \]
\[ + \sum_{\alpha, c, n} Q_{\alpha, c, n}[t] \sum_{m : (n,m) \in E} S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] \right] Q[t] \]
\[ = E \left[ - \sum_{(n,k) \in E} (Q_{\alpha, c, n}[t] - Q_{\alpha, c, k}[t]) S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] \right] Q[t] \].

Let
\[ (\alpha^*(n,k), \gamma^*(n,k)) \in \arg \max_{\alpha, c} \frac{1}{l_{\alpha}} (Q_{\alpha, c, n}[t] - Q_{\alpha, c, k}[t]) - q_{th}. \]
Then for any \( (n, k) \in E \),
\[ - \sum_{\alpha, c} (Q_{\alpha, c, n}[t] - Q_{\alpha, c, k}[t]) S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] \]
\[ \leq - \left( \frac{1}{l_{\alpha}} (Q_{\alpha, c, n}[t] - Q_{\alpha, c, k}[t]) - q_{th} \right) \]
\[ \cdot l_{\alpha} S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] - q_{th} \sum_{\alpha, c} l_{\alpha} S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t], \tag{23} \]
where the last term \( q_{th} \sum_{\alpha, c} l_{\alpha} S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] \) can be further removed since it is nonnegative. When \( (Q_{\alpha, c, n}[t] - Q_{\alpha, c, k}[t]) / l_{\alpha} - q_{th} \leq 0 \), the quantities on the both sides of (23) are 0. When \( (Q_{\alpha, c, n}[t] - Q_{\alpha, c, k}[t]) / l_{\alpha} - q_{th} > 0 \), then holds
\[ - \left( \frac{1}{l_{\alpha}} (Q_{\alpha, c, n}[t] - Q_{\alpha, c, k}[t]) - q_{th} \right) \]
\[ \cdot l_{\alpha} S_{\alpha, c, n}^{*, \gamma_{\alpha, c, n}}[t] \]
\[ \leq - \left( \frac{1}{l_{\alpha}} (Q_{\alpha, c, n}[t] - Q_{\alpha, c, k}[t]) - q_{th} \right) \]
\[ \cdot C_{\alpha, n} + C_3, \]
where \( C_3 \) is a constant and again we have used the boundedness of processing and transmission capacity. By the definition of the capacity region \( \Lambda \), there exist nonnegative \( \mu_{\alpha, c, (n,k)} \)'s such that
\[ \sum_{\alpha, c} l_{\alpha} \mu_{\alpha, c, (n,k)} \leq C_{\alpha, n}. \tag{24} \]
Then
\[- \left(\frac{1}{\lambda^*_{n,k}} (Q \alpha^*_{n,k} c^*_{n,k} n[t] - Q \alpha^*_{n,k} c^*_{n,k} k[t]) - qth \right) \cdot C(n,k) \leq - \sum_{\alpha,c} (Q\alpha,c,n[t] - Q\alpha,c,k[t] - l\alpha qth)\mu_{\alpha,c}(n,k).\]

Thus the inequality
\[- \sum_{\alpha,c} (Q\alpha,c,n[t] - Q\alpha,c,k[t]) \leq - \sum_{\alpha,c} (Q\alpha,c,n[t] - Q\alpha,c,k[t] - l\alpha qth)\mu_{\alpha,c}(n,k) + C_3\]
holds for both the case that \((Q\alpha^*_{n,k} c^*_{n,k} n[t] - Q\alpha^*_{n,k} c^*_{n,k} k[t])/l\alpha - qth \leq 0\) and the case that \((Q\alpha^*_{n,k} c^*_{n,k} n[t] - Q\alpha^*_{n,k} c^*_{n,k} k[t])/l\alpha - qth > 0\).

Therefore,
\[
\mathbb{E} \left[ - \sum_{\alpha,c} Q\alpha,c,n[t] \sum_{k:(n,k)\in E} S_1^{\alpha,c}(n,k)[t] \right. \\
+ \sum_{\alpha,c} Q\alpha,c,n[t] \sum_{m:(m,n)\in E} S_1^{\alpha,c,m,n}[t] \left| Q[t]\right] \\
\leq - \sum_{(n,k)\in E} \left( \sum_{\alpha,c} \mu_{\alpha,c,n} - \sum_{m:(m,n)\in E} \mu_{\alpha,c,m,n} \right) \cdot Q\alpha,c,n[t] \\
+ qth \sum_{(m,n)\in E} \sum_{\alpha,c} l\alpha \mu_{\alpha,c,m,n} + n^2 C_3. \tag{25}
\]

Now we combine the bounds on the summands in (19), (22) and (25) to bound the drift. By Theorem 1,
\[
\sum_{d:a=d} \lambda_{d,c,n} + \sum_{m:(m,n)\in E} \mu_{\alpha,c,m,n} \leq \sum_{k:(n,k)\in E} \mu_{\alpha,c,n} + \gamma_{\alpha,c,n}, \forall \alpha,c,n.
\]

Thus
\[
\mathbb{E}[V(Q[t+1]) - V(Q[t]) | Q[t]] \\
\leq - \frac{2\epsilon}{1 + \epsilon |K||C|} \sum_{\alpha,c,n} P_{\alpha,c,n} Q\alpha,c,n[t] \\
+ qth \sum_{(m,n)\in E} \sum_{\alpha,c} l\alpha \mu_{\alpha,c,m,n} + C_4, \tag{26}
\]

where \(C_4 = C_1 + nC_2 + n^2C_3\). Note that \(P_{\alpha,c,n} > 0\) for any \(\alpha, c\) and \(n\). Therefore, there exists \(\delta > 0\) such that the drift is smaller than \(-\delta\) for states outside a finite set and the drift is bounded for states within the finite set. By Foster-Lyapunov theorem \((Q[t]: t \geq 0)\) is positive recurrent.

Since the Markov chain \((Q[t]: t \geq 0)\) is irreducible, aperiodic and positive recurrent, its distribution converges to the stationary distribution, and in the steady state, there holds
\[
\mathbb{E}[V(Q[t+1]) - V(Q[t])] = 0.
\]

We now assume that the Markov chain is in the steady state and take expectation on both sides of (26), which gives
\[
0 \leq - \frac{2\epsilon}{1 + \epsilon |K||C|} \sum_{\alpha,c,n} P_{\alpha,c,n} \mathbb{E}[Q\alpha,c,n[t]] \\
+ qth \sum_{(m,n)\in E} \sum_{\alpha,c} l\alpha \mu_{\alpha,c,m,n} + C_4.
\]

Thus
\[
\mathbb{E} \left[ \sum_{\alpha,c} Q\alpha,c,n[t] \right] \\
\leq qth \sum_{(m,n)\in E} \sum_{\alpha,c} l\alpha \mu_{\alpha,c,m,n} \frac{(1 + \epsilon)|K||C|}{2\epsilon P_{\text{min}}} \\
+ C_4 \frac{(1 + \epsilon)|K||C|}{2\epsilon P_{\text{min}}},
\]
where \(P_{\text{min}} = \min\{P_{\alpha,c,n}: \alpha \in K, c \in C, n \in N\}\). This implies the bound in (14).

To prove the bound on the amount of transmitted data in (15), we consider the optimal solution \((\lambda^*, \mu^*, \gamma^*)\) instead of the rates in (18), (21) and (24). Using similar arguments, except that here we do not remove the last term in (23), we obtain the following bound on the drift
\[
\mathbb{E}[V(Q[t+1]) - V(Q[t]) | Q[t]] \\
\leq - qth \sum_{(m,n)\in E} \sum_{\alpha,c} l\alpha S_1^{\alpha,c,m,n}[t] \\
+ qth \sum_{(m,n)\in E} \sum_{\alpha,c} l\alpha \mu_{\alpha,c,m,n} + C_4. \tag{27}
\]

Again we assume that the Markov chain is in the steady state and take expectation on both sides of (27). Then
\[
0 \leq - qth \mathbb{E} \left[ \sum_{\alpha,c,n} l\alpha S_1^{\alpha,c,m,n}[t] \right] \\
+ qth \sum_{(m,n)\in E} \sum_{\alpha,c} l\alpha \mu_{\alpha,c,m,n} + C_4,
\]
and thus
\[
\mathbb{E} \left[ \sum_{\alpha,c,n} l\alpha S_1^{\alpha,c,m,n}[t] \right] \leq \sum_{\alpha,c,n} l\alpha \mu_{\alpha,c,m,n} + C_4. \tag{28}
\]

This implies the bound in (15) and completes the proof. \(\square\)
V. Conclusions

In this paper, we studied the resource allocation problem of parallel-data computing. Due to the coupling between the data and computation in each task, the problem involves data storage, communication and computing. Our end goal is to support efficient data-processing in the system, so unnecessary transmission should be avoided to reduce the usage of bandwidth resources, while high throughput should be maintained. This led to the networking architecture and the resource allocation algorithm we proposed. We first classified the data into classes and let each node maintain a queue for each (data class, computation operation) pair. Then the algorithm allocates computation loads and schedules data transmission according to these queue lengths. We proved that our proposed algorithm is throughput optimal, i.e., it stabilizes the system for the maximum range of incoming traffic intensity. Beyond throughput, our algorithm can also control the tradeoff between the amount of data transmission and the expected number of backlogged tasks in steady state through a parameter $q_{th}$. We showed that the gap between the expected data transmission rate under our algorithm and the optimal value is $O(1/q_{th})$, and the expected number of total backlogged tasks is upper bounded by $O(q_{th})$.

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References


