

# Manifest Sharing with Session Types

STEPHANIE BALZER AND FRANK PFENNING

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Computer Science Department  
School of Computer Science  
Carnegie Mellon University  
Pittsburgh, PA 15213

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## Abstract

Session-typed languages building on the Curry-Howard isomorphism between linear logic and session-typed communication guarantee session fidelity and deadlock freedom. Unfortunately, these strong guarantees exclude many naturally occurring programming patterns pertaining to shared resources. In this paper, we introduce sharing into a session-typed language where types are stratified into linear and shared layers with modal operators connecting the layers. The resulting language retains session fidelity but not the absence of deadlocks, which can arise from contention for shared processes. We illustrate our language on various examples, such as the dining philosophers problem, and show that sharing recovers the computational power of the untyped asynchronous  $\pi$ -calculus.



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# 1 Introduction

*Session types* [29, 30, 31] prescribe the communication protocols that arise in concurrent programming. Session types and session type libraries have found their ways into various practical programming languages [18, 33, 34, 44, 53] to express such protocols and ensure their adherence at compile-time. Recently, message-passing concurrency has been put onto a firm logical foundation by exhibiting a Curry-Howard isomorphism between linear logic and session-typed communication [8, 9, 57, 62]. Programming languages [26, 58] based on this isomorphism not only guarantee *session fidelity* (preservation) but also a form of *global progress*, since the process graph forms a tree and is acyclic by construction.

Unfortunately, these strong guarantees preclude programming scenarios that naturally demand sharing, such as shared databases or output devices, or implementations that make use of sharing for performance considerations. The shared channels available through the exponential modality in linear logic have a copying semantics [8, 62] and therefore do not provide the correct tools in such applications. In this paper, *shared channels* and *shared processes* always refer to mutable resources.

In this paper, we contribute a session-typed programming language for message-passing concurrency that seamlessly integrates *linear* and *shared* processes. The language allows multiple *aliases* to a shared process to exist, but makes sure that any state-altering communication with such a process only happens once exclusive access to the process has been obtained. At this point, the process becomes linear and can become shared again once it is released, resulting in renunciation of exclusive access. The resulting language retains session fidelity but not the absence of deadlocks, which can arise from contention for shared processes.

A key novelty of our work is to go beyond supporting *acquire-release* as a mere language primitive, but to enrich the type system so that a session type prescribes at which points in the protocol acquisition and release must happen. We generalize the idea of type *stratification* introduced in [49], based on Benton’s LNL 1994 and Reed’s adjoint logic 2009, and stratify session types into a linear and shared layer and support two *modalities* going back and forth between them. We then interpret the modal operator shifting *down* from the shared to the linear layer as a *release* and the operator shifting *up* from the linear to the shared layer as an *acquire*. As a result, we obtain a type system where any form of synchronization, including the acquisition and release of a shared process, is manifest in the session type.

Now that types prescribe the acquisition and release points of shared processes, it is only a small step to making sure that the assumptions by a client attempting to acquire a shared process are actually met. When there is contention for a shared process and one client obtains access at type  $A$  and then releases it again, the release must happen again at the same type  $A$ . This is necessary since the acquire/release cycle is invisible to all other clients. To capture this constraint statically we introduce the notion of an *equi-synchronizing* session type. A session type is equi-synchronizing if it satisfies the invariant that any release restores the session to the same type at which a preceding acquire occurred.

We illustrate our language on various examples, such as producer-consumer queues and dining philosophers, and also demonstrate how nondeterministic choice can be emulated in the resulting language thanks to shared processes. Moreover, we provide an encoding of the untyped asynchronous  $\pi$ -calculus into our language, recovering the computational power of the untyped  $\pi$ -calculus for session-typed, message-passing concurrency.

An interesting question is what the meta-theoretic consequences of the introduction of sharing are. The correspondence between linear logic and session-typed communication [8, 9, 57, 62] established for purely linear session-typed languages seems no longer to hold in its original form. Under this interpretation proofs correspond to processes and cut reduction to communication. With the introduction of sharing, on the other hand, shared channels upon which a process depends may not always be available. Such a computation state corresponds to an *incomplete proof*. Overall, computation is then an interleaving of *proof construction* (acquiring a resource), *proof reduction* (communication), and *proof deconstruction* (releasing a resource). The fact that computation may deadlock is always a failure of proof construction, never communication.

The principal contributions of this paper are:

- the introduction of sharing into session-typed, message-passing, concurrent programming such that sharing is manifest in the type structure via adjoint modalities;
- its elaboration in the programming language  $\text{SILL}_S$ , resulting in type system, synchronous operational semantics,

- and proofs of session fidelity (preservation) and a modified form of progress that characterizes possible deadlocks;
- the notion of an equi-synchronizing session type to guarantee session fidelity without the need for run-time type checking when acquiring a process;
- an encoding of the untyped asynchronous  $\pi$ -calculus into  $\text{SILL}_5$ , reclaiming the expressiveness of the untyped  $\pi$ -calculus;
- an extension of the formal system to accommodate an asynchronous dynamics, using a novel transformation derived from logic;
- a prototype implementation of manifest sharing in Concurrent C0.

The main part of this paper is structured as follows: Section 2 provides a brief introduction to linear session types. Section 3 introduces manifest sharing. Section 4 illustrates manifest sharing on various examples. Section 5 details the semantics of  $\text{SILL}_5$ , including preservation and progress. Section 6 gives the encoding of the untyped asynchronous  $\pi$ -calculus into  $\text{SILL}_5$ . Section 7 provides a brief overview of our implementation. Section 8 summarizes the related work, and Section 9 concludes the main part of this paper with a discussion and some remarks about future work. The appendix gives the complete statics and dynamics of  $\text{SILL}_5$ , proofs of preservation and progress as well as of supporting lemmas and corollaries, and further examples.

## 2 Background

In this section, we provide a short introduction to linear session-typed message-passing concurrency based on the functional language  $\text{SILL}$  [26, 49, 58] built on the Curry-Howard isomorphism between intuitionistic linear logic and session-typed concurrency.  $\text{SILL}$  incorporates processes into a functional core via a linear contextual monad that isolates session-typed concurrency. In this introduction we focus on the linear process layer of  $\text{SILL}$ , which we extend with manifest sharing in Section 3.

*Linear logic* [24] is a substructural logic that restricts the structural rules of weakening and contraction to propositions of the form  $!A$ , where  $!$  is a so-called exponential modality. As result, purely linear propositions (that is, propositions without an exponential modality) can be viewed as resources that must be used *exactly once* in a proof. We adopt the intuitionistic version of linear logic, which yields the following sequent [13]

$$A_1, \dots, A_n \vdash A$$

where  $A_1, \dots, A_n$  are linear antecedents and  $A$  is the succedent.

Under the Curry-Howard isomorphism for intuitionistic linear logic, propositions are related to session types, proofs to processes, and cut reduction in proofs to communication. Appealing to this correspondence, we assign a process term  $P$  to the above judgment and label each hypothesis as well as the conclusion with a *channel*:

$$x_1 : A_1, \dots, x_n : A_n \vdash P :: (x : A)$$

The resulting judgment states that process  $P$  *provides* a service of session type  $A$  along channel  $x$ , *using* the services of session types  $A_1, \dots, A_n$  provided along channels  $x_1, \dots, x_n$ . The assignment of a channel to the conclusion is convenient because, unlike functions, processes do not evaluate to a value but continue to communicate along their providing channel once they have been created. For the judgment to be well-formed, all the channel names have to be distinct. In particular, the channel name to the right of the turnstile cannot appear to its left. This intuitionistic interpretation of linear logic avoids the need for explicit dualization [29, 30, 62] of a session type. Whether a session type is used or provided is determined by its positioning to the left or right, respectively, of the turnstile.

The balance between providing and using a session is established by the two fundamental rules of the sequent calculus that are independent of all logical connectives: cut and identity. Cut states that if  $P$  provides service  $A$  along channel  $x$ , then  $Q$  can use the service along the same channel at the same type. Identity states that, if we are a client of a service  $A$  we can always directly provide  $A$ .

$$\frac{\Delta \vdash P_x :: (x : A) \quad \Delta', x : A \vdash Q_x :: (z : C)}{\Delta, \Delta' \vdash x \leftarrow P_x ; Q_x :: (z : C)} \text{ (T-CUT)} \qquad \frac{}{y : A \vdash \text{fwd } x y :: (x : A)} \text{ (T-Id)}$$



Operationally, the process  $x \leftarrow P_x ; Q_x$  creates a globally fresh channel  $c$ , spawns a new process  $[c/x]P_x$  providing along  $c$ , and continues as  $[c/x]Q_x$ . Conversely, the process  $\text{fwd } c \ d$  terminates after directly identifying channels  $c$  and  $d$ . Here, we have adopted the convention to use  $x, y$ , and  $z$  for channel *variables* and  $c$  and  $d$  for *channels*. Channels are created at run-time and substituted for channel variables in process terms. For type-checking purposes, the distinction between variables and channels is negligible, even though they obey slightly different invariants (see Section B.2); we hence use the metavariables  $x, y$ , and  $z$  to stand for either of them when type-checking process terms.

The Curry-Howard correspondence gives each connective of linear logic an interpretation as a session type. This session type prescribes the kind of message that must be sent or received along a channel of this type and at which type the session continues after the exchange. Table 1 provides an overview of the session types arising from linear logic and their operational meaning. We generalize internal  $A \oplus B$  and external choice  $A \& B$  to n-ary labeled choices  $\oplus\{\bar{l} : A\}$  and  $\&\{\bar{l} : A\}$ , respectively, where we use the overline-notation to denote a sequence, as is usual. We require external and internal choice to comprise at least one label. Otherwise, there would exist a linear channel without observable interaction along it, which is computationally not really interesting and furthermore would complicate our proofs. Because we adopt the intuitionistic version of linear logic, session types are expressed from the point of view of the provider. In the first line of each connective, Table 1 provides the point of view of the *provider* and in the second line the point of view of the *client*. For each connective, its session type before the exchange (**Session type current**) and after the exchange (**Session type continuation**) is given. Likewise, the implementing process term is indicated before the exchange (**Process term current**) and after the exchange (**Process term continuation**). Table 1 shows that the process terms of a provider and a client for a connective come in matching pairs. Both participants' view of the session changes consistently. The process typing rules for the connectives shown in Table 1 can be found in Figure 3. We defer the discussion of the process typing judgment to Section 3.2.

Session type		Process term		Description
current	continuation	current	continuation	
$c : \oplus\{\bar{l} : A\}$	$c : A_h$	$c.l_h ; P$	$P$	provider sends label $l_h$ along $c$
		case $c$ of $\bar{l} \Rightarrow Q$	$Q_h$	client receives label $l_h$ along $c$
$c : \&\{\bar{l} : A\}$	$c : A_h$	case $c$ of $\bar{l} \Rightarrow P$	$P_h$	provider receives label $l_h$ along $c$
		$c.l_h ; Q$	$Q$	client sends label $l_h$ along $c$
$c : A \otimes B$	$c : B$	send $c \ d ; P$	$P$	provider sends channel $d : A$ along $c$
		$y \leftarrow \text{recv } c ; Q_y$	$[d/y]Q_y$	client receives channel $d : A$ along $c$
$c : A \multimap B$	$c : B$	$y \leftarrow \text{recv } c ; P_y$	$[d/y]P_y$	provider receives channel $d : A$ along $c$
		send $c \ d ; Q$	$Q$	client sends channel $d : A$ along $c$
$c : \mathbf{1}$	-	close $c$	-	provider sends "end" along $c$
		wait $c ; Q$	$Q$	provider receives "end" along $c$

Table 1: Overview of linear session types together with their operational meaning.

As an illustration, we consider a protocol on how to interact with a provider of a queue data structure that contains elements of some variable type  $A^1$ . The protocol is defined by the session type below; we will see variations of it throughout this paper.

$$\begin{aligned} \text{queue } A &= \&\{\text{enq} : A \multimap \text{queue } A, \\ &\quad \text{deq} : \oplus\{\text{none} : \mathbf{1}, \text{some} : A \otimes \text{queue } A\}\} \end{aligned}$$

The session type prescribes that a process providing a service of type `queue A`, gives a client the choice to either enqueue (`enq`) or dequeue (`deq`) an element of type  $A$ . Upon receipt of the label `enq`, the providing process expects to receive a channel of type  $A$  to be enqueued and recurs. Upon receipt of the label `deq`, the providing process either indicates that the queue is empty (`none`), in which case it terminates, or that there is a channel stored in

<sup>1</sup>Polymorphism is orthogonal to the investigation of this paper, so we adopt it for the examples without formal treatment, which can be found in the literature [25, 47].

the queue (**some**), in which case it dequeues this element, sends it to the client, and recurs. We adopt an *equi-recursive* [14] interpretation for recursive session types, which requires recursive session types to be *contractive* [22]. This interpretation guarantees that there are no messages associated with the unfolding of a recursive type.

Figure 1 shows two process definitions *elem* and *empty* implementing the session type `queue A`. In SILL, we declare the type of a defined process  $X$  with  $X : \{A \leftarrow A_1, \dots, A_n\}$ , indicating that the process provides a service of type  $A$ , using channels of type  $A_1, \dots, A_n$ . The definition of the process is then given by  $x \leftarrow X \leftarrow y_1, \dots, y_n = P$  where  $P$  is a process term satisfying  $y_1 : A_1, \dots, y_n : A_n \vdash P :: (x : A)$ . A new process  $X$  providing along  $x$  is spawned with an expression of the form  $x \leftarrow X \leftarrow y_1, \dots, y_n ; Q_x$ , where  $Q_x$  is the continuation binding  $x$ . The channels  $y_1, \dots, y_n$  are passed to  $X$  and hence no longer available to  $Q_x$ .

<pre> elem : {queue A ← A, queue A} q ← elem ← x, t =   case q of     enq → y ← recv q ;      % x : A, t : qu A, y : A ⊢ q : qu A     t.enq ; send t y ;      % x : A, t : qu A ⊢ q : qu A     q ← elem ← x, t     deq → q.some ;         % x : A, t : qu A ⊢ q : A ⊗ qu A     send q x ;             % t : qu A ⊢ q : qu A     fwd q t </pre>	<pre> empty : {queue A} q ← empty =   case q of     enq → x ← recv q ;      % x : A ⊢ q : qu A     e ← empty ;           % x : A, e : qu A ⊢ q : qu A     q ← elem ← x, e     deq → q.none ;        % ⊢ q : 1     close q </pre>
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Figure 1: Processes implementing linear session type `queue A`.

The queue in Figure 1 is implemented as a sequence of *elem* processes, ending in an *empty* process. The recursive process *elem* provides a queue along channel  $q$  and uses a channel  $x : A$  (the element in front of the queue) as well as a channel  $t : \text{queue } A$  (the tail of the queue). If it receives an `enq` label and then a channel  $y$ , it simply enqueues  $y$  in the tail  $t$ . If it receives a `deq` label it responds with **some**, followed by the channel  $x$  it holds, and then forwards all future communication along  $q$  to the tail  $t$ . The implementation is highly parallel; in particular, many enqueueing operations can be in flight at the same time. Process *empty*, on the other hand, builds a singleton queue from an element received to be enqueued and returns **none** and terminates when asked to dequeue. Perhaps the most unusual aspect of writing session-typed programs is that the type of channels changes during interactions, as already indicated in Table 1. To make this explicit we annotate the code in Figure 1 with the types of all channels at the various points in a process definition. We abbreviate `queue A` to `qu A` in those comments.

### 3 Manifest Sharing

In this section, we extend the linear process language of the previous section with the capability to share a process among several clients. The shared channels introduced previously [8, 62] via the exponential modality in linear logic have a copying semantics and therefore do not allow sharing of mutable resources as pursued in this paper. We first approach the support of shared processes programmatically, by introducing acquire-release as a primitive to our language. We then derive those primitives as modalities from logic in a stratified system of session types. Lastly, we develop the notion of an equi-synchronizing session type.

#### 3.1 A Programming Perspective

In the intuitionistic linear setting of Section 2, processes form a tree at run-time, guaranteeing that a client of a process is the only client of that process. With the introduction of *shared processes* this invariant no longer holds because there may exist multiple clients that refer to the process by a *shared channel*. To uphold session fidelity, communication along a shared channel must only be possible once exclusive access to the process providing along that channel has been obtained. To this end, we impose an *acquire-release* discipline on shared processes, where an acquire yields exclusive access to a shared process, if the process is available, and a release relinquishes exclusive access. As a result, processes can alternate between linear and shared, where a successful acquire of a shared process turns the process into a linear one, and conversely, a release of a linear process turns the process into a shared one. This view of a process undergoing phases requires an identification of a process with a thread

of control, which is extremely natural in intuitionistic linear logic since we can identify a process with the channel along which it provides a service.

We illustrate the programmatic working of the acquire-release primitives on a schematic producer-consumer scenario in Figure 2. For now, we assume for both processes that the shared channel  $q$  is provided by a shared process of session type **queue**  $A$  that stores shared elements  $x$  of type  $A$ . In program code, we typeset shared channels as well as shared session types in **red** and **bold** font to make them distinguishable from linear channels and session types, which we typeset in black and regular font. Moreover, we assume that the session type **queue**  $A$  recurs rather than terminates upon dequeuing, if the queue is empty, which is more appropriate for a producer-consumer context. In Section 3.2 we clarify how to change the type specification to accommodate these assumptions.

Processes *produce* and *consume* in Figure 2 attempt to communicate with the queue by issuing corresponding acquire and release statements. Process *produce*, for example, issues the statement  $q' \leftarrow \text{acquire } q$ , which, if successful, yields the queue's linear channel  $q'$  along which the process can enqueue the element. Before the process recurs, it releases the now linear **queue** process providing along  $q'$  by issuing  $q \leftarrow \text{release } q'$ . This yields the queue's shared channel  $q$  and gives turn to another producer or consumer.

<pre> <i>produce</i> : { <b>1</b> <math>\leftarrow</math> <b>A</b>, <b>queue</b> <b>A</b> } <i>c</i> <math>\leftarrow</math> <i>produce</i> <math>\leftarrow</math> <b>x</b>, <b>q</b> =   <math>q' \leftarrow \text{acquire } q</math> ;   <math>q'.\text{enq}</math> ;   <math>\text{send } q' \text{ } x</math> ;   <math>q \leftarrow \text{release } q'</math> ;   <i>c</i> <math>\leftarrow</math> <i>produce</i> <math>\leftarrow</math> <b>x</b>, <b>q</b> </pre>	<pre> <i>consume</i> : { <b>1</b> <math>\leftarrow</math> <b>queue</b> <b>A</b> } <i>c</i> <math>\leftarrow</math> <i>consume</i> <math>\leftarrow</math> <b>q</b> =   <math>q' \leftarrow \text{acquire } q</math> ;   <math>q'.\text{deq}</math> ;   case <math>q'</math> of     <b>some</b> <math>\rightarrow</math> <b>x</b> <math>\leftarrow</math> <math>\text{recv } q'</math> ;     <math>q \leftarrow \text{release } q'</math> ;     <i>c</i> <math>\leftarrow</math> <i>consume</i> <math>\leftarrow</math> <b>q</b>     <b>none</b> <math>\rightarrow</math> <b>q</b> <math>\leftarrow \text{release } q'</math> ;     <i>c</i> <math>\leftarrow</math> <i>consume</i> <math>\leftarrow</math> <b>q</b> </pre>
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Figure 2: Acquire-release primitives illustrated on producer-consumer, programmatically. Shared channels are typeset in **red** and **bold** font, linear channels in black and regular font. See Section 3.2 for definition of shared session type **queue**  $A$ .

### 3.2 A Logic Perspective

Like send and receipt of a message, acquire and release denote synchronization points in the communication between processes. If we were to introduce acquire and release as operational primitives only, session types would no longer accurately prescribe the protocols of communication. To restore the descriptive power of session types, we enrich the type system so that the type of a process dictates at which points in the communication acquire and release must happen.

The key idea in pursuit of this goal is to generalize the notion of type *stratification* introduced in [49], based on Benton's LNL 1994 and Reed's adjoint logic 2009, and to stratify session types into a *linear* and *shared* layer. We then connect these layers with *modalities* that go back and forth between them. In Pfenning and Griffith 2015 the modes are U, F, and L for unrestricted, affine, and linear session types, respectively. In this paper, we focus on the interplay between the modes S and L, pertaining to shared and linear session types, respectively. An integration with the remaining modes U and F is straightforward.

The stratification arises from a difference in structural properties that exist for session types at a mode — or propositions, when viewed through the lens of the Curry-Howard correspondence. For example, shared propositions can be weakened, contracted, and exchanged, whereas linear propositions can only be exchanged. The difference in structural properties entails a hierarchy between modes such that a mode with fewer structural properties is at the bottom. The hierarchy for the modes S and L is:

$$S > L$$

The *independence principle* for modes states that proofs of a proposition of a stronger mode (with more structural properties) may not depend on hypotheses of a strictly weaker mode (with fewer structural properties). This is

because a client of a stronger proposition may, for example, reuse the proposition, which would implicitly reuse the weaker proposition on which it depends. More technically, on the logical side, cut elimination would fail without this restriction. As a result, we get separate<sup>2</sup> hypothetical judgments for shared and linear processes which, by definition, obey the independence principle:

$$\Gamma \vdash_{\Sigma} P :: (x_s : A_s)$$

$$\Gamma; \Delta \vdash_{\Sigma} P :: (x_l : A_l)$$

The subscripts denote the respective mode of a channel or session type, and the contexts  $\Gamma$  and  $\Delta$  consist of hypotheses on the typing of shared and linear channels, respectively. The judgments depend on a signature  $\Sigma$  that is populated with all process definitions prior to type-checking, allowing for recursive process definitions.

Given the two layers, we can now define the modality  $\downarrow_l^s A_s$ , which shifts a shared proposition (session type) to a linear one, and the modality  $\uparrow_l^s A_l$ , which shifts a linear proposition (session type) to a shared one. The resulting strata restricted to session types (propositions) at the modes S and L are:<sup>3</sup>

$$A_s \quad \triangleq \quad \uparrow_l^s A_l$$

$$A_l, B_l \quad \triangleq \quad A_l \otimes B_l \mid \mathbf{1} \mid \oplus \{\overline{l : A_l}\} \mid \exists A_s. B_l \mid A_l \multimap B_l \mid \Pi A_s. B_l \mid \& \{\overline{l : A_l}\} \mid \downarrow_l^s A_s$$

We review the new connectives and their operational meaning in Table 2. Together with Table 1, this table defines the connectives supported in  $\text{SILL}_S$ . Besides the new connectives to accommodate acquire-release, which we discuss in more detail below, we introduce the connectives  $\Pi A_s. B_l$  and  $\exists A_s. B_l$  to support shared channel input and output, respectively. These connectives of mixed mode are based on the dependent session type connectives introduced in [12, 64], yet at the present stage our language does not make use of the potentially dependent nature of these connectives. The process typing rules for the connectives of  $\text{SILL}_S$ , excluding the acquire-release connectives, which we discuss below, can be found in Figure 3. A complete listing of all the process typing rules can be found in Figure 17 and Figure 18 in the appendix.

Session type		Process term			Description
current	continuation	current	continuation		
$c_l : \Pi A_s. B_l$	$c_l : B_l$	$\text{send } c_l \ d_s ; P$	$P$		provider sends channel $d_s : A_s$ along $c_l$
		$y_s \leftarrow \text{recv } c_l ; Q_{y_s}$	$[d_s/y_s] Q_{y_s}$		client receives channel $d_s : A_s$ along $c_l$
$c_l : \exists A_s. B_l$	$c_l : B_l$	$y_s \leftarrow \text{recv } c_l ; P_{y_s}$	$[d_s/y_s] P_{y_s}$		provider receives channel $d_s : A_s$ along $c_l$
		$\text{send } c_l \ d_s ; Q$	$Q$		client sends channel $d_s : A_s$ along $c_l$
$c_l : \downarrow_l^s A_s$	$c_s : A_s$	$c_s \leftarrow \text{detach } c_l ; P_{x_s}$	$[c_s/x_s] P_{x_s}$		provider sends “ <b>detach</b> $c_s$ ” along $c_l$
		$x_s \leftarrow \text{release } c_l ; Q_{x_s}$	$[c_s/x_s] Q_{x_s}$		client receives “ <b>detach</b> $c_s$ ” along $c_l$
$c_s : \uparrow_l^s A_l$	$c_l : A_l$	$c_l \leftarrow \text{acquire } c_s ; Q_{x_l}$	$[c_l/x_l] Q_{x_l}$		client sends “ <b>acquire</b> $c_l$ ” along $c_s$
		$x_l \leftarrow \text{accept } c_s ; P_{x_l}$	$[c_l/x_l] P_{x_l}$		provider receives “ <b>acquire</b> $c_l$ ” along $c_s$

Table 2: Overview of shared session types together with their operational meaning. See Table 1 for linear connectives.

We are now in a position to define the typing of the acquire-release discipline outlined in the previous section. In particular, we must determine what the types of the channels should be to which acquire and release are applied. Observing that an acquire transforms a shared channel into a linear one, the natural choice is to type the shared channel of an acquire with the modality  $\uparrow_l^s A_l$ . Analogously, the linear channel of a release should be typed with the modality  $\downarrow_l^s A_s$  as it transforms a linear channel into a shared one. Because we adopt an intuitionistic formulation, which avoids the need for explicit dualization of a session type, we get both a left and right rule for each primitive. The notions of acquire and release are naturally formulated from the point of view of a client, so we use those terms in the left rules. For the right rules, we use the terms *accept* and *detach* with the meaning that an accept accepts

<sup>2</sup>We could have chosen an combined judgment with a combined context and corresponding projections onto each mode, as employed in [49] for a richer structure of modes. For this paper, we have chosen separate judgments and contexts for clarity of presentation.

<sup>3</sup>Shared counterparts of all the linear connectives can be defined at the shared level as well, but for the purposes of this paper we will keep the shared layer as simple as possible.

$$\begin{array}{c}
\frac{}{\Gamma; y_L : A_L \vdash_{\Sigma} \text{fwd } x_L y_L :: (x_L : A_L)} \text{(T-ID}_L\text{)} \qquad \frac{\hat{A} \leq A_S}{\Gamma, y_S : \hat{A} \vdash_{\Sigma} \text{fwd } x_S y_S :: (x_S : A_S)} \text{(T-ID}_S\text{)} \\
\hline
\Gamma = \overline{w_S} : \hat{B} \quad \overline{\hat{B}} \leq \overline{B_S} \quad \Delta = \overline{y_L} : \overline{B_L} \quad (x'_L : A_L \leftarrow X_L \leftarrow \overline{y'_L} : \overline{B_L}, \overline{w'_S} : \overline{B_S} = P_{x'_L, \overline{y'_L}, \overline{w'_S}}) \in \Sigma \quad \Gamma, \Gamma', \Delta', x_L : A_L \vdash_{\Sigma} Q_{x_L} :: (z_L : C_L) \\
\hline
\Gamma, \Gamma'; \Delta, \Delta' \vdash_{\Sigma} x_L \leftarrow X_L \leftarrow \overline{y_L}, \overline{w_S}; Q_{x_L} :: (z_L : C_L) \text{(T-SPAWN}_{LL}\text{)} \\
\hline
\Gamma = \overline{y_S} : \hat{B} \quad \overline{\hat{B}} \leq \overline{B} \quad (x'_S : A_S \leftarrow X_S \leftarrow \overline{y'_S} : \overline{B} = P_{x'_S, \overline{y'_S}}) \in \Sigma \quad \Gamma, \Gamma', x_S : A_S; \Delta \vdash_{\Sigma} Q_{x_S} :: (z_L : C_L) \\
\hline
\Gamma, \Gamma'; \Delta \vdash_{\Sigma} x_S \leftarrow X_S \leftarrow \overline{y_S}; Q_{x_S} :: (z_L : C_L) \text{(T-SPAWN}_{LS}\text{)} \\
\hline
\Gamma = \overline{y_S} : \hat{B} \quad \overline{\hat{B}} \leq \overline{B} \quad (x'_S : A_S \leftarrow X_S \leftarrow \overline{y'_S} : \overline{B} = P_{x'_S, \overline{y'_S}}) \in \Sigma \quad \Gamma, \Gamma', x_S : A_S \vdash_{\Sigma} Q_{x_S} :: (z_S : C_S) \\
\hline
\Gamma, \Gamma' \vdash_{\Sigma} x_S \leftarrow X_S \leftarrow \overline{y_S}; Q_{x_S} :: (z_S : C_S) \text{(T-SPAWN}_{SS}\text{)} \\
\hline
\frac{\Gamma; \Delta \vdash_{\Sigma} Q :: (z_L : C_L)}{\Gamma; \Delta, x_L : \mathbf{1} \vdash_{\Sigma} \text{wait } x_L; Q :: (z_L : C_L)} \text{(T-I}_L\text{)} \qquad \frac{}{\Gamma; \cdot \vdash_{\Sigma} \text{close } x_L :: (x_L : \mathbf{1})} \text{(T-I}_R\text{)} \\
\hline
\frac{\Gamma; \Delta, x_L : B_L, y_L : A_L \vdash_{\Sigma} Q_{y_L} :: (z_L : C_L)}{\Gamma; \Delta, x_L : A_L \otimes B_L \vdash_{\Sigma} y_L \leftarrow \text{recv } x_L; Q_{y_L} :: (z_L : C_L)} \text{(T-}\otimes\text{)}_L \qquad \frac{\Gamma; \Delta \vdash_{\Sigma} P :: (x_L : B_L)}{\Gamma; \Delta, y_L : A_L \vdash_{\Sigma} \text{send } x_L y_L; P :: (x_L : A_L \otimes B_L)} \text{(T-}\otimes\text{)}_R \\
\hline
\frac{\Gamma, y_S : A_S; \Delta, x_L : B_L \vdash_{\Sigma} Q_{y_S} :: (z_L : C_L)}{\Gamma; \Delta, x_L : (\exists A_S. B_L) \vdash_{\Sigma} y_S \leftarrow \text{recv } x_L; Q_{y_S} :: (z_L : C_L)} \text{(T-}\exists\text{)}_L \qquad \frac{\hat{A} \leq A_S \quad \Gamma, y_S : \hat{A}; \Delta \vdash_{\Sigma} P :: (x_L : B_L)}{\Gamma, y_S : \hat{A}; \Delta \vdash_{\Sigma} \text{send } x_L y_S; P :: (x_L : (\exists A_S. B_L))} \text{(T-}\exists\text{)}_R \\
\hline
\frac{\Gamma; \Delta, x_L : B_L \vdash_{\Sigma} Q :: (z_L : C_L)}{\Gamma; \Delta, x_L : A_L \multimap B_L, y_L : A_L \vdash_{\Sigma} \text{send } x_L y_L; Q :: (z_L : C_L)} \text{(T-}\multimap\text{)}_L \qquad \frac{\Gamma; \Delta, y_L : A_L \vdash_{\Sigma} P_{y_L} :: (x_L : B_L)}{\Gamma; \Delta \vdash_{\Sigma} y_L \leftarrow \text{recv } x_L; P_{y_L} :: (x_L : A_L \multimap B_L)} \text{(T-}\multimap\text{)}_R \\
\hline
\frac{\hat{A} \leq A_S \quad \Gamma, y_S : \hat{A}; \Delta, x_L : B_L \vdash_{\Sigma} Q :: (z_L : C_L)}{\Gamma, y_S : \hat{A}; \Delta, x_L : (\Pi A_S. B_L) \vdash_{\Sigma} \text{send } x_L y_S; Q :: (z_L : C_L)} \text{(T-}\Pi\text{)}_L \qquad \frac{\Gamma, y_S : A_S; \Delta \vdash_{\Sigma} P_{y_S} :: (x_L : B_L)}{\Gamma; \Delta \vdash_{\Sigma} y_S \leftarrow \text{recv } x_L; P_{y_S} :: (x_L : (\Pi A_S. B_L))} \text{(T-}\Pi\text{)}_R \\
\hline
\frac{(\forall i) \Gamma; \Delta, x_L : A_{L_i} \vdash_{\Sigma} Q_i :: (z_L : C_L)}{\Gamma; \Delta, x_L : \oplus \{\overline{l} : \overline{A_L}\} \vdash_{\Sigma} \text{case } x_L \text{ of } \overline{l} \Rightarrow \overline{Q} :: (z_L : C_L)} \text{(T-}\oplus\text{)}_L \qquad \frac{\Gamma; \Delta \vdash_{\Sigma} P :: (x_L : A_{L_h})}{\Gamma; \Delta \vdash_{\Sigma} x_L.l_h; P :: (x_L : \oplus \{\overline{l} : \overline{A_L}\})} \text{(T-}\oplus\text{)}_R \\
\hline
\frac{\Gamma; \Delta, x_L : A_{L_h} \vdash_{\Sigma} Q :: (z_L : C_L)}{\Gamma; \Delta, x_L : \& \{\overline{l} : \overline{A_L}\} \vdash_{\Sigma} x_L.l_h; Q :: (z_L : C_L)} \text{(T-}\&\text{)}_L \qquad \frac{(\forall i) \Gamma; \Delta \vdash_{\Sigma} P_i :: (x_L : A_{L_i})}{\Gamma; \Delta \vdash_{\Sigma} \text{case } x_L \text{ of } \overline{l} \Rightarrow \overline{P} :: (x_L : \& \{\overline{l} : \overline{A_L}\})} \text{(T-}\&\text{)}_R
\end{array}$$

Figure 3: Remaining process typing rules not shown inline.

an acquire and a detach initiates a release. We review each pair of rules in turn, along with their operational semantics:

The typing of the pair acquire-accept is defined by the following rules:

$$\frac{\Gamma, x_S : \uparrow_L^S A_L; \Delta, x_L : A_L \vdash_{\Sigma} Q_{x_L} :: (z_L : C_L)}{\Gamma, x_S : \uparrow_L^S A_L; \Delta \vdash_{\Sigma} x_L \leftarrow \text{acquire } x_S; Q_{x_L} :: (z_L : C_L)} \text{(T-}\uparrow\text{)}_L^S \qquad \frac{\Gamma; \cdot \vdash_{\Sigma} P_{x_L} :: (x_L : A_L)}{\Gamma \vdash_{\Sigma} x_L \leftarrow \text{accept } x_S; P_{x_L} :: (x_S : \uparrow_L^S A_L)} \text{(T-}\uparrow\text{)}_R^S$$

An acquire is applied to the shared channel  $x_S$  along which the shared process offers and yields the process' linear channel  $x_L$ , when successful. The shared channel  $x_S$  is still available to the continuation  $Q_{x_L}$ . By accepting an acquire request by a client along its shared channel  $x_S$ , a shared process transitions to a linear process, now offering along its linear channel  $x_L$ . Since the independence principle forbids a shared process to depend on linear channels, the now linear process starts out with an empty linear context.

(D-ID <sub>L</sub> )	$\text{proc}(a_L, \text{fwd } a_L b_L) \longrightarrow a_L = b_L, a_S = b_S$
(D-ID <sub>S</sub> )	$\text{proc}(a_S, \text{fwd } a_S b_S) \longrightarrow \text{unavail}(a_S), a_S = b_S$
(D-SPAWN <sub>LL</sub> )	$\text{proc}(a_L, x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L}) \longrightarrow \text{proc}(a_L, [b_L/x_L] Q_{x_L}), \text{proc}(b_L, [b_L/x'_L, \overline{c_L}/\overline{y_L}, \overline{c_S}/\overline{y_S}] P_{x'_L, \overline{y_L}, \overline{y_S}}), \text{unavail}(b_S)$ for $x'_L : A_L \leftarrow X_L \leftarrow \overline{y_L} : \overline{B_L}, \overline{y_S} : \overline{B_S} = P_{x'_L, \overline{y_L}, \overline{y_S}} \in \Sigma$ and $b$ fresh
(D-SPAWN <sub>LS</sub> )	$\text{proc}(a_L, x_S \leftarrow X_S \leftarrow \overline{c_S}; Q_{x_S}) \longrightarrow \text{proc}(a_L, [b_S/x_S] Q_{x_S}), \text{proc}(b_S, [b_S/x'_S, \overline{c_S}/\overline{y_S}] P_{x'_S, \overline{y_S}})$ for $x'_S : A_S \leftarrow X_S \leftarrow \overline{y_S} : \overline{B_S} = P_{x'_S, \overline{y_S}} \in \Sigma$ and $b$ fresh
(D-SPAWN <sub>SS</sub> )	$\text{proc}(a_S, x_S \leftarrow X_S \leftarrow \overline{c_S}; Q_{x_S}) \longrightarrow \text{proc}(a_S, [b_S/x_S] Q_{x_S}), \text{proc}(b_S, [b_S/x'_S, \overline{c_S}/\overline{y_S}] P_{x'_S, \overline{y_S}})$ for $x'_S : A_S \leftarrow X_S \leftarrow \overline{y_S} : \overline{B_S} = P_{x'_S, \overline{y_S}} \in \Sigma$ and $b$ fresh
(D-1)	$\text{proc}(c_L, \text{wait } a_L; Q), \text{proc}(a_L, \text{close } a_L) \longrightarrow \text{proc}(c_L, Q)$
(D- $\otimes/\exists$ )	$\text{proc}(c_L, y \leftarrow \text{recv } a_L; Q_y), \text{proc}(a_L, \text{send } a_L b; P) \longrightarrow \text{proc}(c_L, [b/y] Q_y), \text{proc}(a_L, P)$
(D- $\rightarrow/\Pi$ )	$\text{proc}(c_L, \text{send } a_L b; Q), \text{proc}(a_L, y \leftarrow \text{recv } a_L; P_y) \longrightarrow \text{proc}(c_L, Q), \text{proc}(a_L, [b/y] P_y)$
(D- $\oplus$ )	$\text{proc}(c_L, \text{case } a_L \text{ of } \overline{l} \Rightarrow Q), \text{proc}(a_L, a_L.l_h; P) \longrightarrow \text{proc}(c_L, Q_h), \text{proc}(a_L, P)$
(D- $\&$ )	$\text{proc}(c_L, a_L.l_h; Q), \text{proc}(a_L, \text{case } a_L \text{ of } \overline{l} \Rightarrow P) \longrightarrow \text{proc}(c_L, Q), \text{proc}(a_L, P_h)$

Figure 4: Remaining multiset rewriting rules not shown inline.

Operationally, we capture the dynamics of **SILL<sub>S</sub>** by *multiset rewriting rules* [11]. A multiset rewriting rule is generally of the form  $S_1, \dots, S_n \longrightarrow T_1, \dots, T_m$  and denotes a transition from  $S_1, \dots, S_n$  to the  $T_1, \dots, T_m$  where each  $S_i$  and  $T_j$  is a formula capturing some aspect of the current state of the computation. In our setting, we use the rules to capture a transition in the configuration of processes that arise from a program. As we discuss in Section 5.1, we use the predicates  $\text{proc}(c_m, P)$  and  $\text{unavail}(a_S)$  to define the states of a configuration. The former denotes a process with process term  $P$  that provides along channel  $c_m$  at mode  $m$ , the latter acts as a placeholder for a shared process providing along channel  $a_S$  that is currently not available. Multiset rewriting rules are local in that they only mention the parts of a configuration they rewrite. The synchronous dynamics of the pair acquire-accept is given by the following rule:

$$\begin{array}{c} \text{proc}(c_L, x_L \leftarrow \text{acquire } a_S; Q_{x_L}), \text{proc}(a_S, x_L \leftarrow \text{accept } a_S; P_{x_L}) \\ \longrightarrow \text{proc}(c_L, [a_L/x_L] Q_{x_L}), \text{proc}(a_L, [a_L/x_L] P_{x_L}), \text{unavail}(a_S) \end{array} \quad (\text{D-}\uparrow_L^S)$$

The above rule indicates that, at run-time, for every process there exists one channel,  $a$ , with two modes, a linear one,  $a_L$ , and a shared one,  $a_S$ . When a process shifts between modes, it switches between the two modes of its offering channel. This channel is substituted at the appropriate mode for the variables occurring within process terms. The typing rules  $(\text{T-}\uparrow_L^S)$  and  $(\text{T-}\uparrow_L^R)$  create a fresh variable  $x_L$ , for which the already existing channel  $a$  at mode **L** is substituted. The offering channel together with its two modes is created when a process is spawned.

Figure 4 gives the dynamics of the remaining connectives in **SILL<sub>S</sub>**. A complete listing of all the multiset rewriting rules can be found in Figure 20 in the appendix. The side condition  $b$  fresh indicates allocation of a globally fresh channel and the equality  $a = b$  expresses that  $b$  is substituted for  $a$  in the entire configuration. For convenience, we write multiset rewriting rules such that a providing process appears to the right of its client. Multiset rewriting rules, however, are unordered.

The typing of the pair release-detach is defined by the following rules:

$$\frac{\Gamma, x_S : A_S; \Delta \vdash_\Sigma Q_{x_S} :: (z_L : C_L)}{\Gamma; \Delta, x_L : \downarrow_L^S A_S \vdash_\Sigma x_S \leftarrow \text{release } x_L; Q_{x_S} :: (z_L : C_L)} \quad (\text{T-}\downarrow_L^S) \quad \frac{\Gamma \vdash_\Sigma P_{x_S} :: (x_S : A_S)}{\Gamma; \cdot \vdash_\Sigma x_S \leftarrow \text{detach } x_L; P_{x_S} :: (x_L : \downarrow_L^S A_S)} \quad (\text{T-}\downarrow_L^R)$$

The rules are essentially inverse to the typing rules of acquire-release; we point out that rule  $(\text{T-}\downarrow_L^R)$  requires the linear context to be empty, to satisfy the independence principle. Operationally, the rules have the following semantics:

$$\begin{array}{c} \text{proc}(c_L, x_S \leftarrow \text{release } a_L; Q_{x_S}), \text{proc}(a_L, x_S \leftarrow \text{detach } a_L; P_{x_S}), \text{unavail}(a_S) \\ \longrightarrow \text{proc}(c_L, [a_S/x_S] Q_{x_S}), \text{proc}(a_S, [a_S/x_S] P_{x_S}) \end{array} \quad (\text{D-}\downarrow_L^S)$$

This time the rules shift the process from **S** to **L**, by switching the offering channel from  $a_L$  to  $a_S$  and by substituting the channel  $a_S$  for the variable  $x_S$ .

Let's now return to the producer-consumer example and work out what the type specifications have to be. The processes *produce* and *consume* in Figure 2 have been devised under the assumption that the channel  $q$  is a shared

channel to a shared queue and that the shared queue process recurs rather than terminates upon dequeuing, if the queue is empty. For this to be the case, we change the session type `queue` from Section 2 as follows:

$$\text{queue } A_s = \uparrow_L^s \& \{ \text{enq} : \Pi A_s . \downarrow_L^s \text{queue } A_s, \\ \text{deq} : \oplus \{ \text{none} : \downarrow_L^s \text{queue } A_s, \text{some} : \exists A_s . \downarrow_L^s \text{queue } A_s \} \}$$

With this change, the code in Figure 2 is type-correct as it is written. The new definition of session type `queue`  $A_s$  uses the previously introduced dependent linear session types  $\Pi A_s . B_L$  and  $\exists A_s . B_L$  for shared channel input and output, respectively, and prescribes the following synchronization pattern: When a process of type `queue`  $A_s$  is spawned, it starts out as a shared process that first must be acquired. Any of the defined sequences of inputs and outputs then are executed while the process is linear. After such an exchange, the process recurs at type  $\downarrow_L^s \text{queue } A_s$ . Since `queue`  $A_s$  is defined as  $\uparrow_L^s \& \{ \dots \}$ , the type  $\downarrow_L^s \text{queue } A_s$  amounts to the type  $\downarrow_L^s \uparrow_L^s \& \{ \dots \}$ . This means that in its recursion, the process will first need to be released to become a shared process of type `queue`  $A_s$ . Looking at the implementations of processes *produce* and *consume* in Figure 2, we can see that they comply with the acquire-release pattern dictated by the above session type. For example, after process *produce* has sent the channel  $x$  along channel  $q'$ , the channel  $q'$  is of type  $\downarrow_L^s \text{queue } A_s$ , which is why process *produce* releases that channel before it recurs.

Having changed the specification of session type `queue`  $A_s$ , we must correspondingly change the implementations of processes *empty* and *elem* shown in Figure 1; the result is given in Figure 5. The code predominantly contains the matching pairs `accept` and `detach` as well as `acquire` and `release`, respectively. For example, the first statement in process *empty* accepts an acquire request from a client. Similarly, the statement  $q \leftarrow \text{detach } q'$  initiates a release by a client.

<pre> empty : {queue A<sub>s</sub>} q ← empty =   q' ← accept q ;   case q' of     enq → x ← rcv q' ;            e ← empty ;            q ← detach q' ;            q ← elem ← x, e     deq → q'.none ;            q ← detach q' ;            q ← empty </pre>	<pre> elem : {queue A<sub>s</sub> ← A<sub>s</sub>, queue A<sub>s</sub>} q ← elem ← x, t =   q' ← accept q ;   case q' of     enq → y ← rcv q' ;            t' ← acquire t ;            t'.enq ; send t' y ;            t ← release t' ;            q ← detach q' ;            q ← elem ← x, t     deq → q'.some ;            send q' x ;            q ← detach q' ;            fwd q t </pre>
---	---

Figure 5: Implementation of a shared queue. See Figure 1 for linear version.

Session type `queue`  $A_s$  pinpoints a typical pattern of shared process programming where a shared recursive session type  $Y_s = \uparrow_L^s A_L$  recurs at type  $\downarrow_L^s Y_s$ . The benefits of this pattern are two-fold: on the one hand, it guarantees that the session type  $Y_s$  allows for perpetual acquire-release cycles and, on the other hand, it makes sure that all acquired processes are released at recursion point because linearity forbids any linear channels to be left behind.

Comparing this shared version of session type `queue` with its linear version in Section 2, we note that the shared version does no longer require the elements stored in the queue to be linear. The elements can either be of a shared type  $A_s$  or of a shifted linear type  $\uparrow_L^s A_L$ . This choice is not available for a linear queue because the queue can only be consumed if all the elements stored in it can be consumed too. For the same reason it is now also possible for the shared queue to recur upon dequeuing rather than terminating, if the queue is empty.

### 3.3 Equi-Synchronizing Session Types

So far we have achieved that a client communicates with a shared process in mutual exclusion from other clients and that the acquire and release points of a shared process manifest in its session type. There remains a last threat

to session fidelity that we need to address: erroneous assumptions by a client on a shared process' type. These can come about, for example, in the following scenario: two clients  $Q_1$  and  $Q_2$  are trying to acquire access to the same shared channel  $c_s$  at type  $\uparrow_L^s A_L$ . Let's assume that  $Q_1$  succeeds and then later releases  $c_L$  to a *different* type  $\uparrow_L^s B_L$ . Once  $Q_2$  finally obtains access to  $c_s$ ,  $Q_2$  and the provider will disagree on the type of the channel  $c_L$ : the provider will think that  $c_L : B_L$ , while  $Q_2$  will think that  $c_L : A_L$ , thereby violating session fidelity.

To guarantee preservation without resorting to run-time checks, we introduce the notion of an *equi-synchronizing* session type. A session type is equi-synchronizing if it imposes the invariant on a process to be *released* to the *same* type at which the process was previously *acquired*. No constraint is imposed on channels that were never acquired. For example, our shared queue  $A_s$  from Section 3.2

$$\text{queue } A_s = \uparrow_L^s \& \{ \text{enq} : \Pi A_s. \downarrow_L^s \text{queue } A_s, \\ \text{deq} : \oplus \{ \text{none} : \downarrow_L^s \text{queue } A_s, \text{some} : \exists A_s. \downarrow_L^s \text{queue } A_s \} \}$$

is equi-synchronizing because, in each branch, it releases a channel back to type  $\text{queue } A_s$ , which is the type at which the channel must have been acquired.

We formally define the notion of an equi-synchronizing session type in Figure 6, giving a coinductive definition. The definition is based on the judgment

$$\vdash_{\Sigma} (A, \hat{D}) \text{ esync}$$

where  $\hat{D}$  represents a constraint on the type at which a channel of type  $A$  can be released to. If  $\hat{D} = \top$ , then there is no constraint on a future release, if  $\hat{D} = D_s$  then any release must take place to type  $D_s$ . There is a third possibility,  $\hat{D} = \perp$  which means that  $A$  may never be released. This is necessary only for the proof of session fidelity (see Section 5.2).

We say that the type  $A$  *equi-synchronizes* to  $\hat{D}$  or that  $\hat{D}$  is  $A$ 's equi-synchronizing constraint. Underlying the coinductive definition of an equi-synchronizing session type is the notion of a *continuation type*. To check that a type  $A$  equi-synchronizes to the type  $\uparrow_L^s D_L$ , the rules in Figure 6 transitively step through  $A$ 's continuation (starting from  $(A, \top)$ ) until the first acquisition point  $\uparrow_L^s B_L$  is encountered. At this point, the type  $\uparrow_L^s B_L$  is set to be the equi-synchronizing constraint, and the rules transitively step through each continuation of  $B_L$  until the first release point  $\downarrow_L^s C_s$  is encountered. The session type is equi-synchronizing, if it holds that  $C_s = \uparrow_L^s D_L$  at each such release point.

$$\begin{array}{c} \frac{(\forall i) \vdash_{\Sigma} (A_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (\oplus \{ \bar{l} : A_L \}, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\oplus})} \quad \frac{(\forall i) \vdash_{\Sigma} (A_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (\& \{ \bar{l} : A_L \}, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\&})} \\ \\ \frac{\vdash_{\Sigma} (B_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (A_L \otimes B_L, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\otimes})} \quad \frac{\vdash_{\Sigma} (B_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (A_L \multimap B_L, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\multimap})} \\ \\ \frac{\vdash_{\Sigma} (B_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (\exists A_s. B_L, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\exists})} \quad \frac{\vdash_{\Sigma} (B_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (\Pi A_s. B_L, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\Pi})} \quad \frac{}{\vdash_{\Sigma} (\mathbf{1}, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\mathbf{1}})} \\ \\ \frac{\vdash_{\Sigma} (A_L, \uparrow_L^s A_L) \text{ esync}}{\vdash_{\Sigma} (\uparrow_L^s A_L, \top) \text{ esync}} \text{ (T-ESYNC}_{\uparrow_L^s})} \quad \frac{\vdash_{\Sigma} (D_s, \top) \text{ esync}}{\vdash_{\Sigma} (\downarrow_L^s D_s, D_s) \text{ esync}} \text{ (T-ESYNC}_{\downarrow_L^s-1})} \quad \frac{\vdash_{\Sigma} (D_s, \top) \text{ esync}}{\vdash_{\Sigma} (\downarrow_L^s D_s, \top) \text{ esync}} \text{ (T-ESYNC}_{\downarrow_L^s-2})} \end{array}$$

Figure 6: Equi-synchronizing session type, coinductively defined.

Let's exercise the rules in Figure 6 on our shared  $\text{queue } A_s$ . We start out with  $\vdash_{\Sigma} (\text{queue } A_s, \top) \text{ esync}$ . Since session types are interpreted equi-recursively and are contractive [22], we can "silently" replace  $\text{queue } A_s$  with its definition, which means we have to check

$$\vdash_{\Sigma} (\uparrow_L^s \& \{ \text{enq} : \dots \text{ deq} : \dots \}, \top) \text{ esync}$$



According to rule (T-ESYNC $\uparrow_L^s$ ), we set the equi-synchronizing constraint to `queue  $A_s$` , requiring us to check for the continuation that

$$\vdash_{\Sigma} (\&\{\text{enq} : \dots \text{deq} : \dots\}, \text{queue } A_s) \text{ esync}$$

According to rule (T-ESYNC $\&$ ), we are required to check for each continuation that

$$\begin{aligned} &\vdash_{\Sigma} (\Pi A_s. \downarrow_L^s \text{queue } A_s, \text{queue } A_s) \text{ esync} \\ &\vdash_{\Sigma} (\oplus\{\text{none} : \downarrow_L^s \text{queue } A_s, \text{some} : \exists A_s. \downarrow_L^s \text{queue } A_s\}, \text{queue } A_s) \text{ esync} \end{aligned}$$

Let's consider the first branch. According to rule (T-ESYNC $\Pi$ ) we must check that

$$\vdash_{\Sigma} (\downarrow_L^s \text{queue } A_s, \text{queue } A_s) \text{ esync}$$

which, according to rule (T-ESYNC $\downarrow_L^s-1$ ), amounts to the check

$$\vdash_{\Sigma} (\text{queue } A_s, \top) \text{ esync}$$

This is the check we started out with, allowing us to succeed on this branch since our rules are interpreted coinductively. Since the same holds true for all branches, we conclude that the session type `queue  $A_s$`  is equi-synchronizing, unfolding type definitions where necessary.

Not all branches must actually release. For example, the variant

$$\text{queue } \mathbf{A}_s = \uparrow_L^s \&\{\text{enq} : \Pi \mathbf{A}_s. \downarrow_L^s \text{queue } \mathbf{A}_s, \text{deq} : \oplus\{\text{none} : \mathbf{1}, \text{some} : \exists \mathbf{A}_s. \downarrow_L^s \text{queue } \mathbf{A}_s\}\}$$

of the shared queue above is equi-synchronizing even though the queue terminates upon dequeuing in case of an empty queue. In that case, the queue can effectively no longer be acquired.

As we will show in more detail in Section 5.2, the equi-synchronizing invariants are at the core of the preservation proof, requiring us to show that each process maintains its equi-synchronizing constraint along all possible transitions. The three possible constraints  $\hat{D}$ , namely  $\top$ ,  $\uparrow_L^s A_L$ , and  $\perp$ , are related by the following partial order, for any  $A_L$ :

$$\top \geq \uparrow_L^s A_L \geq \perp$$

This relationship becomes relevant for substitutions, where we allow substituting a channel of a smaller type for variables or channels of a bigger type at the client side (see Section 5.2).

When checking the signature  $\Sigma$ , recursive session type definitions are checked to be both contractive and equi-synchronizing and process definitions are checked to provide an equi-synchronizing session type. The check is initiated with  $\top$  as a constraint to convey that any initial release is unconstrained. A purely linear session type  $A_L$  with neither acquire nor release points will thus satisfy the constraint  $\vdash_{\Sigma} (A_L, \top) \text{ esync}$  and also the even stronger condition  $\vdash_{\Sigma} (A_L, \perp) \text{ esync}$ .

## 4 More Examples

In this section, we illustrate manifest sharing on several examples. Section E provides additional examples, including an “imperative” style of a queue implementation that maintains a reference to the back of the queue.

### 4.1 Dining Philosophers

The dining philosophers problem [19] is a prime example designed to illustrate the issues of enforcing mutual exclusion in the presences of circular dependencies among processes. It's precisely because of circularity that the dining philosophers problem cannot be modelled in the purely linear language presented in Section 2. With sharing at our disposal, however, we are now able to model the dining philosophers problem. The result is given in Figure 7.

$\text{lfork} = \downarrow_{\perp}^s \text{sfork}$	$\text{fork\_proc} : \{\text{sfork}\}$	$\text{thinking} : \{\text{phil} \leftarrow \text{sfork}, \text{sfork}\}$	$\text{eating} : \{\text{phil} \leftarrow \text{lfork}, \text{lfork}\}$
$\text{sfork} = \uparrow_{\perp}^s \text{lfork}$	$\mathbf{c} \leftarrow \text{fork\_proc} =$	$c \leftarrow \text{thinking} \leftarrow \text{left}, \text{right} =$	$c \leftarrow \text{eating} \leftarrow \text{left}', \text{right}' =$
$\text{phil} = 1$	$c' \leftarrow \text{accept } \mathbf{c};$	$(* \text{thinking} *)$	$(* \text{eating} *)$
	$\mathbf{c} \leftarrow \text{detach } c';$	$\text{left}' \leftarrow \text{acquire left};$	$\text{right} \leftarrow \text{release right}';$
	$\mathbf{c} \leftarrow \text{fork\_proc}$	$\text{right}' \leftarrow \text{acquire right};$	$\text{left} \leftarrow \text{release left}';$
		$c \leftarrow \text{eating} \leftarrow \text{left}', \text{right}'$	$c \leftarrow \text{thinking} \leftarrow \text{left}, \text{right}$

Figure 7: Dining philosophers.

The implementation defines the mutually dependent session types `lfork` and `sfork` and the session type `phil`, representing a fork and a philosopher, respectively. In support of the spirit of the example, the former allow perpetual acquire-release cycles and are implemented by process `fork_proc`. Session type `phil`, on the other hand, denotes a trivial linear session, which is implemented by the processes `thinking` and `eating`. As the names suggest, process `thinking` represents a philosopher that is thinking, whereas process `eating` represents a philosopher that is eating. A thinking philosopher has shared channel references to the forks on their left and right. Once the philosopher is done thinking, they attempt to acquire their right and left fork and transition to eating, if successful. An eating philosopher, on the other hand, has linear channel references to the forks on their left and right, which they release, once they are done eating and before transitioning to thinking. We can set up a table of 4 philosophers using the following lines of code:

```
f0 ← fork_proc ; f1 ← fork_proc ; f2 ← fork_proc ; f3 ← fork_proc ;
p0 ← thinking ← f0, f1 ; p1 ← thinking ← f1, f2 ; p2 ← thinking ← f2, f3 ; p3 ← thinking ← f3, f0 ;
```

The above setup faithfully matches the circular table and can lead to a deadlock, as pointed out by Dijkstra, if every philosopher picks up the fork on their left and then blocks, waiting for the fork on their right. We can avoid this deadlock by following Dijkstra’s originally proposed solution to impose a partial order on the forks and acquiring the forks in ascending order. This can be achieved by reversing the order of the arguments in the last line to `p3 ← thinking ← f0, f3`.

## 4.2 Atomicity

Another benefit of making the acquire and release points of a process manifest in the type structure is that *atomic* sections [21] become explicit. Since the statements between an up- and a downshift are executed while the process is linear, they are guaranteed to be executed without interference.

We illustrate atomicity on the example of printing to standard out from a concurrent program. To make sure that the print statements will be issued to standard out in the order that they appear in a given thread, we represent the standard output stream by a shared process that obeys the mutually recursive session types `s_stdout` and `l_stdout` in Figure 8. The protocol defined by those session types requires a client to acquire standard out before being able to print to it and then to release it upon completion. The processes `p` and `v` implement the session types `s_stdout` and `l_stdout`, respectively. We have chosen their names in reminiscence of Dijkstra’s semaphore operations P and V. The lines of code below demonstrate how a client interacts with atomic standard out for printing, assuming the channel `out` of type `s_stdout` to be available as a system service:

```
out' ← acquire out ; out'.enter ;
out'.print ; send out' "Hello " ; out'.print ; send out' "shared " ; out'.print ; send out' "world!" ;
out'.leave ; out ← release out' ;
```

In session type `l_stdout`, we take the liberty to use the connective  $\triangleright$ , a connective introduced in [26, 58] to support value input. The type “string  $\triangleright$  l\_stdout” describes as session that receives a value of type `string` and then continues as a session of type `l_stdout`. Toninho et al. [58] show how to safely integrate a functional layer with a process layer

$\mathbf{s\_stdout} = \uparrow_{\mathbb{L}}^s \& \{ \text{enter} : \mathbb{L\_stdout} \}$ $\mathbb{L\_stdout} = \& \{ \text{print} : \text{string} \supset \mathbb{L\_stdout},$ $\text{leave} : \downarrow_{\mathbb{L}}^s \mathbf{s\_stdout} \}$	$p : \{ \mathbf{s\_stdout} \}$ $\mathbf{c} \leftarrow p =$ $c' \leftarrow \text{accept } \mathbf{c} ;$ $\text{case } c' \text{ of}$ $  \text{enter} \rightarrow c' \leftarrow v$	$v : \{ \mathbb{L\_stdout} \}$ $c' \leftarrow v =$ $\text{case } c' \text{ of}$ $  \text{print} \rightarrow x \leftarrow \text{recv } c' ;$ $\text{print } x ;$ $c' \leftarrow v$ $  \text{leave} \rightarrow \mathbf{c} \leftarrow \text{detach } c' ;$ $\mathbf{c} \leftarrow p$
--	--	--

Figure 8: Atomic standard output. The connective  $\supset$  denotes value input, an orthogonal concept introduced in [26, 58].

by means of a linear contextual monad. Those results are orthogonal to sharing and generalize to our language. The statement *print* in process *v*, lastly, abstracts the actual print primitive on a given platform. To prevent races on this primitive, processes *p* and *v* are internal, and the only way for users to interact with standard out is via the system service *out*.

### 4.3 Nondeterminism

Acquire-release introduces *nondeterminism* into our language because it is unknown which client among several clients that acquire a shared process will succeed. We use this property to implement binary nondeterministic choice in our language.

Figure 9 gives the definition of session type *coin* and its implementing, mutually recursive processes *coin\_head* and *coin\_tail*. Session type *coin* indicates which side of the coin is currently facing up. In the implementation each interaction flips the coin to its opposite side.

$\mathbf{coin} = \uparrow_{\mathbb{L}}^s \oplus \{ \text{head} : \downarrow_{\mathbb{L}}^s \mathbf{coin}, \text{tail} : \downarrow_{\mathbb{L}}^s \mathbf{coin} \}$	$\text{coin\_head} : \{ \mathbf{coin} \}$ $\mathbf{c} \leftarrow \text{coin\_head} =$ $c' \leftarrow \text{accept } \mathbf{c} ;$ $c'.\text{head} ;$ $\mathbf{c} \leftarrow \text{detach } c' ;$ $\mathbf{c} \leftarrow \text{coin\_tail}$	$\text{coin\_tail} : \{ \mathbf{coin} \}$ $\mathbf{c} \leftarrow \text{coin\_tail} =$ $c' \leftarrow \text{accept } \mathbf{c} ;$ $c'.\text{tail} ;$ $\mathbf{c} \leftarrow \text{detach } c' ;$ $\mathbf{c} \leftarrow \text{coin\_head}$
---	--	--

Figure 9: Session type *coin* with implementing processes *coin\_head* and *coin\_tail*.

Figure 10 shows the process *nd\_choice* which nondeterministically sends *yes* or *no* and then terminates. Process *nd\_choice* achieves nondeterminism by reading a coin that it shares with process *coin\_flipper*. Since both processes try to acquire the coin concurrently and the coin switches sides when read, the value read by *nd\_choice* depends on the order in which the coin is acquired. For a client of this service, see Figure 11 where it is used to model nondeterminism inherent in the (untyped) asynchronous  $\pi$ -calculus.

## 5 Semantics

In this section, we complete the discussion of the semantics of  $\text{SILL}_5$ , by giving the configuration typing rules as well as elaborating on preservation and progress. A complete listing of  $\text{SILL}_5$ 's abstract syntax, statics, and dynamics as well as proofs of preservation and progress can be found in the appendix. In the last subsection, we sketch an asynchronous dynamics for  $\text{SILL}_5$ , which relies on a novel transformation derived from logic.

<pre> nd_choice : {⊕{yes : 1, no : 1}} d ← nd_choice =   c ← coin_head ;   f ← coin_flipper ← c ;   c' ← acquire c ;   case c' of     head → c ← release c' ; d.yes ; wait f ; close d     tail → c ← release c' ; d.no ; wait f ; close d </pre>	<pre> coin_flipper : {1 ← coin} d ← coin_flipper ← c =   c' ← acquire c ;   case c' of     head → c ← release c' ; close d     tail → c ← release c' ; close d </pre>
---	---

Figure 10: Binary nondeterministic choice.

## 5.1 Configuration Typing

At run-time, a SILL<sub>S</sub> program evolves into a number of linear and shared processes as well as placeholders for formerly shared processes that are currently linear. To type the resulting *configuration*  $\Omega$ , we divide the configuration into a *linear* part  $\Theta$  and a *shared* part  $\Lambda$ , subject to the following well-formedness conditions:

$$\begin{array}{ll}
\Omega \triangleq \cdot \mid \Lambda; \Theta & (\forall a.\text{proc}(a_L, -) \in \Theta \implies \text{unavail}(a_S) \in \Lambda) \\
\Lambda \triangleq \cdot \mid \text{proc}(a_S, P_{a_S}), \Lambda' \mid \text{unavail}(a_S), \Lambda' & (\text{proc}(a_S, -), \text{unavail}(a_S) \text{ not in } \Lambda') \\
\Theta \triangleq \cdot \mid \text{proc}(a_L, P_{a_L}), \Theta' & (\text{proc}(a_L, -) \text{ not in } \Theta')
\end{array}$$

The side conditions make sure that no other process (or placeholder) exists yet in the configuration that provides along the same channel and that for every linear process there exists a placeholder along the shared mode of the channel. The division is justified by the hierarchy between mode S and L, making sure that shared processes cannot depend on linear processes. We use the following typing judgment to type a configuration:

$$\Gamma \vDash_{\Sigma} \Lambda; \Theta :: \Gamma; \Delta$$

The judgment expresses that the configuration  $\Lambda; \Theta$  provides the shared channels in  $\Gamma$  and the linear channels in  $\Delta$ . To permit cyclic dependencies along shared channels, a configuration is type-checked relative to all shared channels, which is the reason why  $\Gamma$  appears to the left of the turnstile. The typing of a configuration is defined by the following rule:

$$\frac{\Gamma \vDash_{\Sigma} \Lambda :: \Gamma \quad \Gamma \vDash_{\Sigma} \Theta :: \Delta}{\Gamma \vDash_{\Sigma} \Lambda; \Theta :: \Gamma; \Delta} \text{ (T-}\Omega\text{)}$$

The rule relies on the judgment  $\Gamma \vDash_{\Sigma} \Theta :: \Delta$  for typing  $\Theta$  and the judgment  $\Gamma \vDash_{\Sigma} \Lambda :: \Gamma$  for typing  $\Lambda$ . The judgment  $\Gamma \vDash_{\Sigma} \Theta :: \Delta$  expresses that the configuration  $\Theta$  provides the linear channels in  $\Delta$ , using the shared channels in  $\Gamma$ . The typing of  $\Theta$  is defined by the following two rules:

$$\frac{}{\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)} \text{ (T-}\Theta_1\text{)} \quad \frac{(a_S : \hat{B}) \in \Gamma \quad \vdash_{\Sigma} (A_L, \hat{B}) \text{ esync} \quad \Gamma; \Delta' \vdash_{\Sigma} P_{a_L} :: (a_L : A_L) \quad \Gamma \vDash_{\Sigma} \Theta :: \Delta, \Delta'}{\Gamma \vDash_{\Sigma} \text{proc}(a_L, P_{a_L}), \Theta :: (\Delta, a_L : A_L)} \text{ (T-}\Theta_2\text{)}$$

Rule (T- $\Theta_2$ ) is of particular interest as it imposes an order on linear configurations. By requiring that all the linear channels  $\Delta'$  used by  $\text{proc}(a_L, P_{a_L})$  are provided by the remaining configuration  $\Theta$ , the rule “flattens” the linear process tree such that for any process the providers of the channels used by the process are to the right of the process in the configuration. We maintain this order only for typing purposes, at run-time any permutations of a well-typed configuration are permissible. The rule also enforces that a linear configuration only provides the channels that are not used internally to the configuration. For example, the channels  $\Delta'$  consumed by  $\text{proc}(a_L, P_{a_L})$  are no longer provided as part of the resulting configuration  $\text{proc}(a_L, P_{a_L}), \Theta$ . An initial configuration  $\Lambda; \Theta$  would be typed as  $\Gamma \vDash_{\Sigma} \Lambda; \Theta :: (\Gamma; c_L : \mathbf{1})$ , where the process providing along channel  $c_L$  is the main program thread and  $\Lambda$  may provide some pre-defined shared system services such a *out* as in Section 4.2. The premises  $(a_S : \hat{B}) \in \Gamma$  and  $\vdash_{\Sigma} (A_L, \hat{B}) \text{ esync}$  of rule (T- $\Theta_2$ ), constrain the type to which  $\text{proc}(a_L, P_{a_L})$  must be released to.

Unlike the typing rules for  $\Theta$ , the typing rules for  $\Lambda$  do not impose any order on the shared processes. Any attempt would be futile anyway because the reference structure along shared channels may not adhere to any pattern and could, for example, be cyclic. We use the judgment  $\Gamma \vDash_{\Sigma} \Lambda :: \Gamma'$  to type such configurations, expressing that  $\Lambda$  offers the shared channels in  $\Gamma'$ , using the shared channels in  $\Gamma$ . The typing rules for  $\Lambda$  are:

$$\begin{array}{c}
\frac{}{\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)} \text{ (T-}\Lambda_1\text{)} \qquad \frac{\vdash_{\Sigma} (\uparrow_{\mathbb{L}}^s A_{\mathbb{L}}, \top) \text{ esync} \quad \Gamma \vdash_{\Sigma} P_{a_s} :: (a_s : \uparrow_{\mathbb{L}}^s A_{\mathbb{L}})}{\Gamma \vDash_{\Sigma} \text{proc}(a_s, P_{a_s}) :: (a_s : \uparrow_{\mathbb{L}}^s A_{\mathbb{L}})} \text{ (T-}\Lambda_2\text{)} \qquad \frac{}{\Gamma \vDash_{\Sigma} \text{unavail}(a_s) :: (a_s : \hat{A})} \text{ (T-}\Lambda_3\text{)} \\
\frac{\Gamma \vDash_{\Sigma} \Lambda :: \Gamma' \quad \Gamma \vDash_{\Sigma} \Lambda' :: \Gamma''}{\Gamma \vDash_{\Sigma} \Lambda, \Lambda' :: \Gamma', \Gamma''} \text{ (T-}\Lambda_4\text{)}
\end{array}$$

Rule (T- $\Lambda_4$ ) permits breaking up a configuration  $\Lambda$  into its subparts at any point. Rule (T- $\Lambda_2$ ) carries again an equi-synchronizing invariant as a premise, indicating that the type to which  $\text{proc}(a_s, P_{a_s})$  must be released is not yet significant.

## 5.2 Preservation and Progress

In this section, we state preservation and progress for  $\text{SILL}_5$  and review the key issues that had to be addressed to prove preservation and progress. The proofs of preservation and progress can be found in Section D.

The challenges that arise from extending the linear system discussed in Section 2 with manifest sharing are twofold. For preservation, we need to make sure that clients will encounter shared processes at the type they would like to acquire them. For progress, we need to account for the possibility of deadlock due to cyclic dependencies along shared channels or termination of a process providing a shared service, while ruling out other forms of failure of progress.

To address the first challenge, we have introduced the notion of an equi-synchronizing session type in Section 3.3, which statically imposes the invariant that each shared channel is released to the same session type at which it was acquired (if at all). The preservation proof shows that this invariant is maintained for each channel along any possible transition, as captured in the corresponding premises of rules (T- $\Theta_2$ ) and (T- $\Lambda_2$ ). Key are the three form of type constraints  $\hat{D}$  with  $\vdash_{\Sigma} (A, \hat{D}) \text{ esync}$  where  $A$  is the current type of a linear process providing along  $a_{\mathbb{L}}$ :

1.  $\hat{D} = \top$ , indicating that there is no constraint on a future release of  $a_{\mathbb{L}}$  because  $a_{\mathbb{L}}$  has never been shared.  $\hat{D} = \top$  holds initially, when a linear process is spawned, and continues to hold until the process is released for the first time to become shared. Processes which remain linear throughout their lifetime will never be subject to an equi-synchronization constraint.
2.  $\hat{D} = D_s$ , indicating that if there is a future release of  $a_{\mathbb{L}}$  to a shared channel  $a_s$ , then  $a_s$  must have type  $D_s$ . Preservation holds since we have statically checked that  $\vdash_{\Sigma} (A, \hat{D}) \text{ esync}$  and this property is maintained along all continuations of  $A$ .
3.  $\hat{D} = \perp$ , expressing that  $a_s$  must never be released, which means that any client attempting to acquire  $a_s$  will be blocked forever. The need for  $\perp$  is subtle. Imagine we forward between two linear channels  $\text{fwd } a_{\mathbb{L}} \ b_{\mathbb{L}}$ . The forward has to identify not only  $a_{\mathbb{L}}$  and  $b_{\mathbb{L}}$ , but also the underlying shared channels  $a_s$  and  $b_s$ , because releasing one now amounts to releasing the other:

$$\text{proc}(a_{\mathbb{L}}, \text{fwd } a_{\mathbb{L}} \ b_{\mathbb{L}}) \longrightarrow a_{\mathbb{L}} = b_{\mathbb{L}}, a_s = b_s \quad \text{(D-ID}_{\mathbb{L}}\text{)}$$

While the types of  $a_{\mathbb{L}}$  and  $b_{\mathbb{L}}$  must be at the same  $A$ , it is possible that the constraints on the releases of  $a_{\mathbb{L}}$  and  $b_{\mathbb{L}}$  are  $\vdash (A, D_s) \text{ esync}$  and  $\vdash (A, D'_s) \text{ esync}$  for  $D_s \neq D'_s$ . This can arise because  $a_{\mathbb{L}}$  and  $b_{\mathbb{L}}$  may have different histories. Preservation still holds in this case because there can not be a down shift in any continuation of  $A$  (shown by coinduction on the definition of  $\text{esync}$ ), so neither  $a_{\mathbb{L}}$  nor  $b_{\mathbb{L}}$  could ever be released. Formally, this is conveniently expressed as  $\vdash_{\Sigma} (A, \perp) \text{ esync}$ .

The introduction of  $\perp$  requires us to generalize all the typing rules where a process uses a shared channel. For example, we change rule (T- $\uparrow_{\mathbb{L}}^s$ ) as follows:

$$\frac{\hat{B} \leq \uparrow_{\mathbb{L}}^s A_{\mathbb{L}} \quad \Gamma, x_s : \hat{B}; \Delta, x_{\mathbb{L}} : A_{\mathbb{L}} \vdash_{\Sigma} Q_{x_{\mathbb{L}}} :: (z_{\mathbb{L}} : C_{\mathbb{L}})}{\Gamma, x_s : \hat{B}; \Delta \vdash_{\Sigma} x_{\mathbb{L}} \leftarrow \text{acquire } x_s; Q_{x_{\mathbb{L}}} :: (z_{\mathbb{L}} : C_{\mathbb{L}})} \text{ (T-}\uparrow_{\mathbb{L}}^s\text{)}$$

In contrast to the rule introduced in Section 3.2 the above rule accounts for the possibility of a shared process to be of type  $\perp$ . In this case, a client can freely choose the type of the process to be acquired because it will never succeed in acquiring that process. As can be seen in Figure 3, the rules (T-ID<sub>S</sub>), (T-SPAWN<sub>LL</sub>), (T-SPAWN<sub>LS</sub>), (T-SPAWN<sub>SS</sub>), (T- $\exists$ <sub>R</sub>), and (T- $\Pi$ <sub>L</sub>) require an analogous treatment.

We can finally state the preservation theorem. It expresses that the types of the providing linear channels are maintained along transitions and that new shared channels may be allocated.

**Theorem 1** (Preservation). *If  $\Gamma \vDash_{\Sigma} \Lambda; \Theta :: \Gamma; \Delta$  and  $\Lambda; \Theta \longrightarrow \Lambda'; \Theta'$ , then  $\Gamma' \vDash_{\Sigma} \Lambda'; \Theta' :: \Gamma'; \Delta$ , for some  $\Lambda'$ ,  $\Theta'$ , and  $\Gamma'$ .*

*Proof.* Preservation is proved by induction on the dynamics, constructing a derivation of a well-typed configuration  $\Gamma' \vDash_{\Sigma} \Lambda''; \Theta'' :: \Gamma'; \Delta$ , where  $\Lambda''$  and  $\Theta''$  are permutations of  $\Lambda'$  and  $\Theta'$ , respectively, and using a variety of substitution lemmas and inversion. Note that the linear context  $\Delta$  remains the same: freshly spawned linear channels have both a provider and client and are therefore not part of the interface. The set of shared channels however can grow.  $\square$

Our progress theorem is based on the notion of a *poised* process introduced in [49]. A  $\text{proc}(a, P_a)$  is *poised* if it is communicating along its providing channel. The poised forms of processes in SILL<sub>S</sub> are:

<i>Receiving</i>	<i>Sending</i>
$\text{proc}(a_{\perp}, y \leftarrow \text{recv } a_{\perp}; P_y)$	$\text{proc}(a_{\perp}, \text{send } a_{\perp} b; P)$
$\text{proc}(a_{\perp}, \text{case } a_{\perp} \text{ of } \overline{l} \Rightarrow \overline{P})$	$\text{proc}(a_{\perp}, \text{close } a_{\perp})$
$\text{proc}(a_s, x_{\perp} \leftarrow \text{accept } a_s; P_{x_{\perp}})$	$\text{proc}(a_{\perp}, a_{\perp}.l_h; P)$
	$\text{proc}(a_{\perp}, x_s \leftarrow \text{detach } a_{\perp}; P_{x_s})$

A linear configuration  $\Theta$  is poised if all  $\text{proc}(a_{\perp}, P_{a_{\perp}}) \in \Theta$  are poised and a shared configuration  $\Lambda$  is poised if all  $\text{proc}(a_s, P_{a_s}) \in \Lambda$  are poised.

To account for the possibility of deadlock, we introduce the notion of a *blocked* process. We say that a process is *blocked along*  $a_s$  if it has the form  $\text{proc}(c_{\perp}, x_{\perp} \leftarrow \text{acquire } a_s; Q_{x_{\perp}})$ . We then state the progress theorem such as to express that being blocked is the *only* way the whole configuration may be stuck [27]. Case (2-c) captures the scenario where a blocked process cannot proceed because the shared channel is unavailable. A successful acquire, on the other hand, is represented as part of case (2-a).

**Theorem 2** (Progress). *If  $\Gamma \vDash_{\Sigma} \Lambda; \Theta :: \Gamma; \Delta$ , then either*

1.  $\Lambda \longrightarrow \Lambda'$ , for some  $\Lambda'$ , or
2.  $\Lambda$  is poised and
  - (a)  $\Lambda; \Theta \longrightarrow \Lambda'; \Theta'$ , for some  $\Lambda'$  and  $\Theta'$ , or
  - (b)  $\Theta$  is poised, or
  - (c) some process in  $\Theta$  is blocked along  $a_s$  and  $\text{unavail}(a_s) \in \Lambda$ .

*Proof.* Progress is proved by induction on the typing of the configurations  $\Lambda$  and  $\Theta$ .  $\square$

At the top level, we have  $\Delta = (c_0 : \mathbf{1})$ , which means that if  $\Theta$  is poised then it and all subcomputations must be finished, trying to close  $c_0$ . If it cannot transition, then the remaining possibility is that some process in  $\Theta$  is blocked along a shared channel. A blocked process may wait indefinitely in case of a deadlock, or because the underlying shared process has terminated, or may never be released. Dining philosophers (Figure 7), for instance, is an example leading to a classic deadlock due to a cyclic dependency along the shared forks. Section E.2 gives another example.

### 5.3 Asynchronous Dynamics

The synchronous operational semantics we have provided for SILL<sub>S</sub> is simple, but not realistic in many applications. Fortunately, we can easily model *asynchronous output* in the existing language in a logically meaningful way. In

order to explain this, we reintroduce the general form of cut (spawn) which is not tied to process definitions, and remind the reader of the identity (forward) rule. For simplicity, we restrict the presentation to the linear case; the shown technique directly generalizes to the shared case.

$$\frac{\Delta \vdash P_x :: (x : A) \quad \Delta', x : A \vdash Q_x :: (z : C)}{\Delta, \Delta' \vdash_{\Sigma} x \leftarrow P_x ; Q_x :: (z : C)} \text{ (T-CUT)} \qquad \frac{}{y : A \vdash_{\Sigma} \text{fwd } x \ y :: (x : A)} \text{ (T-ID)}$$

To asynchronously send a channel  $y$  along  $x$  we spawn a new process which carries the message  $y$ , immediately followed by forwarding.

$$\text{send } x \ y ; P \quad \simeq \quad x' \leftarrow (\text{send } x \ y ; \text{fwd } x' \ x) ; [x'/x]P$$

Intuitively, the spawned process ( $\text{send } x \ y ; \text{fwd } x' \ x$ ) represents the message  $y$  sent along  $x$  with fresh continuation channel  $x'$  [17]. The continuation channel is necessary so that multiple messages sent along the same channel are guaranteed to arrive in the correct order. It is easy to see that, if the synchronous form on the left is well-typed, then so is the asynchronous form on the right. Logically, we can obtain the proof of the left from the proof of the right by a commuting conversion and reduction of cut with identity.

Operationally, the single synchronous reduction

$$\begin{aligned} & \text{proc}(c, \text{send } a \ b ; P), \text{proc}(a, y \leftarrow \text{recv } a ; Q_y) \\ & \longrightarrow \text{proc}(c, P), \text{proc}(a, [b/y]Q_y) \end{aligned}$$

is now decomposed into several steps, where  $P$  can proceed with its continuation before  $b$  is received.

$$\begin{aligned} & \text{proc}(c, x' \leftarrow (\text{send } a \ b ; \text{fwd } x' \ a) ; [x'/a]P), \text{proc}(a, y \leftarrow \text{recv } a ; Q_y) \\ & \longrightarrow \text{proc}(c, [a'/a]P), \text{proc}(a', \text{send } a \ b ; \text{fwd } a' \ a), \text{proc}(a, y \leftarrow \text{recv } a ; Q_y) \quad (\text{spawn, } a' \text{ fresh}) \\ & \longrightarrow \text{proc}(c, [a'/a]P), \text{proc}(a', \text{fwd } a' \ a), \text{proc}(a, [b/y]Q_y) \quad (\text{receive}) \\ & \longrightarrow \text{proc}(c, [a'/a]P), \text{proc}(a', [a'/a][b/y]Q_y) \quad (\text{forward}) \end{aligned}$$

Since  $a'$  is chosen globally fresh and  $a$  is linear, the result is an  $\alpha$ -variant of the synchronous outcome. This technique can be applied to all send operations of the semantics. Effectively, this allows a program written in the synchronous style to be executed fully asynchronously.

The caveat is that we would not want to translate `acquire` in this manner even though the logical semantics dictates it must be a send operation [49]. The reason is that a process would no longer block when trying to acquire a shared channel. Instead it would continue until the corresponding linear channel is actually used to receive a message, which is not the intended meaning. In the implementation (see Section 7) all sends are asynchronous, using a more efficient message buffer instead of explicit continuation channels, except for `acquire` which blocks until the shared channel becomes available.

Alternatively, we could directly provide an asynchronous semantics for all the operations and use an additional acknowledgment step (a “double shift” [49]) to ensure that acquiring a shared resource is synchronous. For this paper, we have chosen the former route because it simplifies the operational semantics and therefore our theorems: without loss of expressiveness, we do not have to explicitly deal with messages or message queues.

## 6 Recovering the Computational Power of the Untyped $\pi$ -calculus

When we view Howard’s original isomorphism between typed  $\lambda$ -calculus and intuitionistic natural deduction [32] as a type assignment system for untyped  $\lambda$ -terms, we lose much of the computational power of the untyped  $\lambda$ -calculus. For example, normalization for natural deduction implies termination of computation on well-typed  $\lambda$ -terms, while arbitrary  $\lambda$ -terms may not have a normal form. However, there is a simple way we can embed *all* untyped  $\lambda$ -terms if we add recursive types. In linear instances of the Curry-Howard correspondence, just adding recursion appears insufficient to recover the computational power of the asynchronous  $\pi$ -calculus [62], and so far there has been no logically motivated and fully satisfactory way to do so.<sup>4</sup>

<sup>4</sup>Other recent work in this direction in the setting of classical linear logic by Atkey et al. [2] uses quite different techniques from ours.

In this section, we give an encoding of the asynchronous, untyped  $\pi$ -calculus into  $\text{SILL}_5$ , demonstrating that shared channels recover the computational power of the untyped  $\pi$ -calculus. The key points to address in the encoding are that (i)  $\pi$ -calculus channels may connect arbitrarily many processes, (ii) messages sent along a  $\pi$ -calculus channel may arrive in arbitrary order, and (iii)  $\pi$ -calculus channels are untyped. Furthermore, since the  $\pi$ -calculus permits deadlock, it is important here that  $\text{SILL}_5$  also admits deadlock.

The basic idea of our encoding is to translate  $\pi$ -calculus *processes* to *linear*  $\text{SILL}_5$  processes of type  $\mathbf{1}$ , and  $\pi$ -calculus *channels* to *shared*  $\text{SILL}_5$  processes of a universal shared type  $\mathcal{U}_5$ . The latter are unordered buffers and obey the following protocol:

$$\mathcal{U}_5 = \uparrow_{\mathbf{1}}^s \& \{ \text{ins} : \Pi \mathcal{U}_5, \downarrow_{\mathbf{1}}^s \mathcal{U}_5, \\ \text{del} : \oplus \{ \text{none} : \downarrow_{\mathbf{1}}^s \mathcal{U}_5, \\ \text{some} : \exists \mathcal{U}_5, \downarrow_{\mathbf{1}}^s \mathcal{U}_5 \} \}$$

Type  $\mathcal{U}_5$  provides the choice to either send (ins) or receive (del) a channel. In the latter case, it communicates whether the buffer is empty (none) or not empty (some) and delivers a channel in the buffer in the latter case. Figure 11 shows the processes *empty* and *elem* that implement session type  $\mathcal{U}_5$ . To guarantee that the resulting buffer is unordered, process *elem* nondeterministically inserts the received channel at an arbitrary point in the buffer, using *nd\_choice* defined in Figure 10. It is also possible and slightly more complicated to postpone the nondeterministic choice to the deletion operation.

$$\begin{array}{ll} \text{empty} : \{ \mathcal{U}_5 \} & \text{elem} : \{ \mathcal{U}_5 \leftarrow \mathcal{U}_5, \mathcal{U}_5 \} \\ \mathbf{c} \leftarrow \text{empty} = & \mathbf{c} \leftarrow \text{elem} \leftarrow \mathbf{x}, \mathbf{d} = \\ \mathbf{c}' \leftarrow \text{accept } \mathbf{c}; & \mathbf{c}' \leftarrow \text{accept } \mathbf{c}; \\ \text{case } \mathbf{c}' \text{ of} & \text{case } \mathbf{c}' \text{ of} \\ | \text{ins} \rightarrow \mathbf{x} \leftarrow \text{recv } \mathbf{c}'; & | \text{ins} \rightarrow \mathbf{y} \leftarrow \text{recv } \mathbf{c}'; \\ \quad \mathbf{e} \leftarrow \text{empty}; & \quad \text{ndc} \leftarrow \text{nd\_choice}; \\ \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \mathbf{c} \leftarrow \text{elem} \leftarrow \mathbf{x}, \mathbf{e} & \quad \text{case ndc of} \\ | \text{del} \rightarrow \mathbf{c}'.\text{none}; & | \text{yes} \rightarrow \mathbf{e} \leftarrow \text{elem} \leftarrow \mathbf{x}, \mathbf{d}; \\ \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \mathbf{c} \leftarrow \text{empty} & \quad \text{wait ndc}; \\ & \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \mathbf{c} \leftarrow \text{elem} \leftarrow \mathbf{y}, \mathbf{e} \\ & | \text{no} \rightarrow \mathbf{d}' \leftarrow \text{acquire } \mathbf{d}; \\ & \quad \mathbf{d}'.\text{ins}; \text{send } \mathbf{d}' \mathbf{y}; \\ & \quad \mathbf{d} \leftarrow \text{release } \mathbf{d}'; \\ & \quad \text{wait ndc}; \\ & \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \mathbf{c} \leftarrow \text{elem} \leftarrow \mathbf{x}, \mathbf{d} \\ & | \text{del} \rightarrow \mathbf{c}'.\text{some}; \\ & \quad \text{send } \mathbf{c}' \mathbf{x}; \\ & \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \text{fwd } \mathbf{c} \mathbf{d} \end{array}$$

Figure 11: Processes *empty* and *elem* implement session type  $\mathcal{U}_5$ , representing a  $\pi$ -calculus channel. To guarantee that the resulting buffer is unordered, process *elem* nondeterministically inserts the received channel at an arbitrary point in the buffer, using process *nd\_choice* defined in Figure 10.

The linear  $\text{SILL}_5$  processes representing  $\pi$ -calculus processes now simply amount to “producers” and “consumers” of shared channels of type  $\mathcal{U}_5$ . Any number of such processes can communicate along a  $\pi$ -calculus channel by acquiring the shared  $\text{SILL}_5$  channel of universal type.

We are now ready to give the encoding of processes. We first review the syntax of the *asynchronous monadic*  $\pi$ -calculus [41, 52], defining the set  $P^\pi$  of  $\pi$ -calculus process terms. We follow the presentation in [3]:

$$P \triangleq 0 \mid \bar{x}(y) \mid x(y).P \mid \nu x P \mid P_1 \mid P_2 \mid !P$$

0 denotes an inactive process.  $\bar{x}(y)$  represents an asynchronous send of  $y$  along channel  $x$ .  $x(y).P$  represents the receiving of a channel along channel  $x$ , after which the process continues with executing  $P$  with the received channel bound to  $y$  in  $P$ . The action prefix  $x(y)$  acts as a guard, making sure that  $P$  can only become active once the



input has occurred.  $\nu x P$  introduces a new channel  $x$  that is bound in  $P$ .  $P_1 \mid P_2$  denotes parallel composition of  $P_1$  and  $P_2$  and  $!P$  replication of  $P$ .

Our translation shown in Figure 12 yields for each  $\pi$ -calculus process term  $P^\pi$  a corresponding linear process  $\llbracket P^\pi \rrbracket_a$  in  $\text{SILL}_S$ , satisfying the typing judgment

$$\Gamma; \cdot \vdash_{\Sigma} \llbracket P^\pi \rrbracket_a :: (a_l : \mathbf{1})$$

where  $\Gamma$  consists of declarations  $x_S : \mathcal{U}_S$  for every shared channel in the overall process configuration. We use type  $\mathbf{1}$  since all communication goes through  $\pi$ -calculus channels, which are mapped to shared channels in  $\Gamma$ . This is also the reason why there are no linear channels in the context. Of course, as shared channels are acquired when send or receive operations are modeled, we communicate with the buffer along a linear channel until it is released again.

$\llbracket 0 \rrbracket_a$	$=$	$\text{close } a$		$\text{snd} : \{(\Pi \mathcal{U}_S. \mathbf{1}) \leftarrow \mathcal{U}_S\}$
$\llbracket \bar{c}(b) \rrbracket_a$	$=$	$p \leftarrow \text{snd } \mathbf{c};$ $\text{send } p \ \mathbf{b};$ $\text{wait } p;$ $\text{close } a$		$d \leftarrow \text{snd} \leftarrow \mathbf{c} =$ $\mathbf{x} \leftarrow \text{recv } d;$ $c' \leftarrow \text{acquire } \mathbf{c};$ $c'.\text{ins};$ $\text{send } c' \ \mathbf{x};$ $\mathbf{c} \leftarrow \text{release } c';$ $\text{close } d$
$\llbracket c(x).P \rrbracket_a$	$=$	$p \leftarrow \text{poll\_rcv} \leftarrow \mathbf{c};$ $\mathbf{b} \leftarrow \text{recv } p;$ $\text{wait } p;$ $a \leftarrow [\mathbf{b}/x] \llbracket P \rrbracket_a$		
$\llbracket \nu x P \rrbracket_a$	$=$	$\mathbf{e} \leftarrow \text{empty};$ $a \leftarrow [\mathbf{e}/x] \llbracket P \rrbracket_a$		$\text{poll\_rcv} : \{(\exists \mathcal{U}_S. \mathbf{1}) \leftarrow \mathcal{U}_S\}$
$\llbracket P_1 \mid P_2 \rrbracket_a$	$=$	$b \leftarrow \llbracket P_1 \rrbracket_b;$ $c \leftarrow \llbracket P_2 \rrbracket_c;$ $\text{wait } b;$ $\text{wait } c;$ $\text{close } a$		$d \leftarrow \text{poll\_rcv} \leftarrow \mathbf{c} =$ $c' \leftarrow \text{acquire } \mathbf{c};$ $c'.\text{del};$ $\text{case } c' \text{ of}$ $\mid \text{none} \rightarrow \mathbf{c} \leftarrow \text{release } c';$ $\quad d \leftarrow \text{poll\_rcv} \leftarrow \mathbf{c}$ $\mid \text{some} \rightarrow \mathbf{x} \leftarrow \text{recv } c';$ $\quad \mathbf{c} \leftarrow \text{release } c';$ $\quad \text{send } d \ \mathbf{x};$ $\quad \text{close } d$
$\llbracket !P \rrbracket_a$	$=$	$\text{Rec}_{!P}^a$ where		
$\text{Rec}_{!P}^a$	$=$	$b \leftarrow \llbracket P \rrbracket_b;$ $c \leftarrow \text{Rec}_{!P}^c;$ $\text{wait } b;$ $\text{wait } c;$ $\text{close } a$		

$\text{empty} : \{\mathcal{U}_S\}$  is defined in Figure 11

Figure 12: Translation from untyped asynchronous  $\pi$ -calculus processes to  $\text{SILL}_S$  and auxiliary processes  $\text{snd}$  and  $\text{poll\_rcv}$ .

Because of the different semantic basis (asynchronous  $\pi$ -calculus on one hand and multiset rewriting on the other), and the question what precisely is observable about a computation, the precise nature of the correspondence between traces in the source and target is difficult to formulate and prove and left to future work (see Section 9 for further remarks).

## 7 Implementation

We briefly describe our implementation of manifest sharing in the context of a type-safe C-like imperative language with session types called Concurrent C0 [65], which is an extension of C0 [1, 48] designed for and used in an introductory imperative programming course [50]. Because session-typed programming follows a monadic style, this imperative implementation is semantically adequate for exploration of the expressive power and programming style for manifest sharing. We have transliterated all the examples in this papers into Concurrent C0 and they will be made available with the implementation artifact should this submission be accepted. Besides an occasional

illustrative use of imperative language features (e.g., loops in place of recursion, or mutable arrays instead of sequences), the only significant difference is the lack of parametric polymorphism in Concurrent C0. Examples have therefore been modified to use either base types such as `int`, or ad hoc polymorphism in the form of `void*` which engenders tagging of values with their dynamic type to ensure type safety. The implementation uses asynchronous message passing, as described in Section 5.3. Moreover, the downshift modality  $\downarrow_L^S$  has no explicit syntax but implicitly precedes every upshift  $\uparrow_L^S$ . This is adequate since, just as in this paper, the only constructor of shared mode is an upshift so there is no other possible continuation.

The compiler translates C0 source to C. Each logical thread of control is implemented as an operating system thread as provided by the `pthread` library. Message passing is implemented via shared memory. Each channel is therefore a data structure in shared memory that can progress through linear and shared phases. Figure 13 provides a schematic overview of this data structure. While linear, access is shared between a provider and a client. The channel contains a current direction of communication and a message queue implemented as a ring buffer whose size is calculated from the session type. Access to the buffer for send and receive operations is protected by a mutex and associated condition variable. In the shared phase, there will be zero or one provider and an arbitrary number of clients. The channel therefore contains a flag that indicates whether the channel is currently available to be acquired. This flag is turned off when the channel is acquired by one of the clients and remains off until the client has been detached and the provider is ready to accept another client. Access to this flag is protected by a separate mutex and condition variable. The operating system scheduler will then nondeterministically select one of the clients.

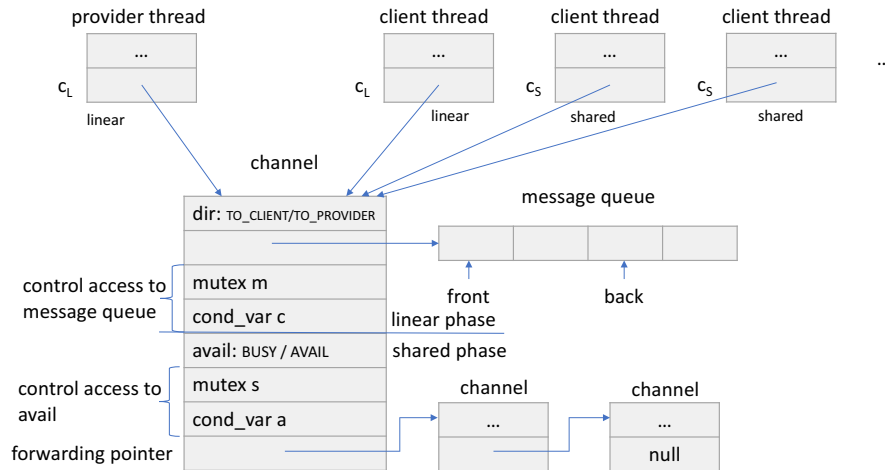


Figure 13: Schematic overview of channel data structure internal to the Concurrent C0 compiler.

As might be expected from the theory, the most difficult aspect of the implementation is forwarding. For forwarding between two linear channels, `fwd c d`, we send a message `FWD c` along `d`, or `FWD d` along `c`, depending on the current direction of communication. Then the thread executing the forward terminates. When the `FWD e` message arrives (where `e` is either `c` or `d`, depending on the direction), the recipient changes its internal reference to the shared channel to `e`, effectively now continuing communication along `e`. For more details and some failed alternatives, see [65].

Unfortunately, this strategy fails for forwarding between two shared channels, `fwd c d`, because there is no effective way to notify all clients of `c` to now communicate along `d` via a message. Instead, before terminating, the provider installs a forwarding pointer from `c` to `d` and marks the availability of `c`. Attempts to acquire `c` will follow the forwarding pointer to `d`. A potential client may have to follow a whole chain of such forwarding pointers. However, each client has to do so at most once.

Returning to a linear forward: when we execute `fwd c d` for linear channels `c` and `d` that were once shared, the semantics requires that we also forward between the underlying shared channels. For example, if the client replaces references to `c` by references to `d` and `d` is eventually released, then subsequent attempts to acquire `c` should obtain access to `d`. In order to account for this scenario, we also install the forwarding pointer from `c` to `d` upon a linear

forward if the channel has ever been a shared channel with possibly multiple waiting clients.

The current implementation of Concurrent C0 does not deallocate channels that were shared at any point during the program execution. We conjecture that manifest sharing admits an effective reference counting garbage collector by transforming the typing derivation to make implicit applications of weakening and contraction explicit. This is one of the immediately planned items of future work.

## 8 Related Work

Our work is situated in the family of works on session types [22, 29, 30, 31] among which it extends work based on the Curry-Howard isomorphism between linear logic and session-typed communication [8, 9, 57, 58, 62] with manifest sharing. We have already summarized that work in Section 2 and have pointed out that the shared channels available through the exponential modality in linear logic have a copying semantics and therefore cannot accommodate the examples presented in this paper. Perhaps most closely related is work by Atkey et al. [2] which proceeds by conflating dual pairs of types in classical linear logic, whereas in this paper we maintain the original interpretation of propositions as session types, but provide an alternative operational semantics for a shared layer of channels separated from the linear types by a pair of adjoint modalities.

From the point of view of protocol expression, our work is related to the line of research that uses *typestate* [56] for protocol checking [5, 15, 20, 40] or program verification [45], in a sequential, object-oriented context. Whereas first approaches [15] support a rather restricted set of aliasing patterns to facilitate modular protocol checking, subsequent approaches lift some of the imposed restrictions, notably by combining aliasing information with *typestate* [5, 43] or *rely-guarantee*-based reasoning [40]. Most closely related to our work is Fährndrich’s and DeLine’s work 2002 on adoption and focus for protocol checking in an object-oriented language. In the resulting language, linear and non-linear objects coexist such that every non-linear object (adoptee) has a linear adopter. Aliases are permitted to adoptees, as long as access goes through the adopter and mutating access happens in a temporary scope, called *focus*. While an aliased object is in *focus*, access to the object via another alias is disabled by *capability tracking*. From this aspect, a *focus scope* bears resemblance to a *critical section* arising between *acquire* and *release* points in our system, even though adoption and *focus* are employed in a purely sequential setting. Whilst capabilities are treated as resources, the underlying type system is not linear, but the required semantics is achieved by threading the capabilities through program execution.

From the point of view of allowing controlled aliasing in a concurrent setting, our work is related to permission-based logics [6, 28, 39, 54] and concurrent separation logic [7, 36, 46, 59, 60]. Permission-based logics maintain a distinction between read and write access to a shared memory location, allowing read access even if only a fractional permission [6] is held, whereas write access requires the entire permission. From a session type perspective, this distinction is less relevant because any communication, input (write) and output (read) alike, amounts to a change in protocol state and thus must be protected sufficiently. Separation logic shares with linear logic the separating conjunction to reason about resource consumption, but uses a Hoare-style reasoning approach that is extrinsic to the type system, whereas resource-awareness is intrinsic to our type system via the Curry-Howard correspondence. Moreover, both permission-based logics and concurrent separation logic target shared-memory concurrency, whereas our work is situated in the realm of message-passing concurrency, offering a different level of abstraction.

Linear types have also found various applications in systems programming. Walker and Watkins 2001, for example, combine linear types with regions [23], and Smith et al. relax the operational “use-once” semantics of linear types [61] to exploit pointer aliasing for destructive operations. Similar observations have been made by Castegren and Wrigstad 2016 in the context of implementing lock-free algorithms. Our work differs from these approaches in that it is based on a richer semantics of linearity derived from the Curry-Howard isomorphism between linear logic and session-typed communication. Moreover, our work employs a message-passing approach to concurrency rather than a shared-memory-based approach. From this perspective, our work has closer ties with the Rust systems programming language [42], which supports message-passing concurrency in an affine setting. Shared data in Rust is normally immutable, but Rust also supports various abstractions (e.g., *mutexes*) that support the safe mutation of shared data. We have found that the programming patterns arising in  $\text{SILL}_5$  readily translate into Rust code with *mutexes*. Rust *mutexes*, however, are dynamic notions only, and Rust does furthermore not support protocol expression.

## 9 Discussion and Future Work

We have presented an extension of logic-based session-typed message-passing concurrency by permitting shared resources encapsulated in processes. This allows elegant expression of examples such as queues with multiple producers and multiple consumers, dining philosophers, shared databases, shared input and output devices, or nondeterministic choice. In fact, all of the asynchronous  $\pi$ -calculus can now be embedded in a statically typed framework satisfying session fidelity by modeling  $\pi$ -calculus channels as shared processes maintaining a nondeterministic message buffer. We were able to maintain the view of linear propositions as session types, sequent proofs as processes, and linear proof reduction as communication. To accommodate shared processes, we had to generalize the usual Curry-Howard correspondence and allow interleaved proof construction (acquire), proof reduction (communication), and proof deconstruction (release). Proof construction may fail, which manifests operationally as deadlock. Key insights are the decomposition of the exponential modality  $!A$  into  $\downarrow_{\mathbb{L}}^S \uparrow_{\mathbb{L}}^S A_{\mathbb{L}}$  inspired by adjoint logic and the insistence on equi-synchronizing types which guarantees that a shared process is always released at the same type it was acquired. The former makes sharing manifest in the type; the latter guarantees session fidelity without runtime checking of types.

On the theory side, we plan to consider how to overlay a likely very different type system or static analysis in order to recover absence of deadlocks. Some recent promising work in this direction [37, 38] in a different context may be adaptable to our situation. We are also interested in relaxing the restriction on equi-synchronization. A first avenue to pursue is to extend our definitions to support subtyping, along the lines of Gay and Hole [22]. Another possibility is to complement the static approach with run-time type-checking to maintain session fidelity [35], particularly in a distributed setting. On the implementation side, we would like to develop the proof-theoretic foundation of a reference counting implementation so that resources associated with shared processes that are no longer accessible can be released.

Finally, the embedding of the asynchronous  $\pi$ -calculus into  $\text{SILL}_S$  raises the interesting question of how precise the modeling is. While we can easily relate computation traces, other traditional notions of concurrency theory such as bisimulation do not immediately apply since our semantics is given as a multiset rewriting system. We conjecture that a slightly modified interpretation with late application of nondeterministic choice describes a bisimulation, according to the definitions mapped out by Deng et al. 2016.

## A Abstract Syntax

Table 3 defines the abstract syntax of  $\text{SILL}_S$ . As is usual, we use the overline-notation to denote a sequence. For example,  $\oplus\{\overline{l : A_L}\}$  stands for  $\oplus\{l_1 : A_{L1}, \dots, l_n : A_{Ln}\}$ . By convention, lines without a left-hand side are separated by  $|$  from their preceding line.

Sort		Abstract Form	Remarks
Metavariables	$\triangleq$	$x_m, y_m, z_m$ $a_m, b_m, c_m, d_m$ $l$	variable (or channel) channel label
Modes $m$	$\triangleq$	$S \mid L$	with $S > L$
Constraint $\hat{A}, \hat{B}, \hat{C}, \hat{D}$	$\triangleq$	$\top$ $\perp$ $\uparrow_L^S A_L$	no yet detached can no longer be acquired constraint on which type must be released to
Session types $A_m, B_m, C_m, D_m$	$\triangleq$	$\oplus\{\overline{l : A_L}\}$ $\&\{\overline{l : A_L}\}$ $A_L \otimes B_L$ $A_L \multimap B_L$ $\exists A_S. B_L$ $\Pi A_S. B_L$ $\mathbf{1}_L$ $\downarrow_L^S A_S$ $\uparrow_L^S A_L$ $Y_m$	internal choice, at least one label external choice, at least one label linear channel output linear channel input shared channel output shared channel input termination downshift upshift type variable
Definition $X_m, Y_m$	$\triangleq$	$x_m : A_m \leftarrow X_m \leftarrow \overline{y : B} = P_{x_m, \overline{y}}$ $Y_m = A_m$	process definition recursive session type definition
Process $P, Q$	$\triangleq$	$x_L.l_h ; P$ <b>case</b> $x_L$ <b>of</b> $\overline{l} \Rightarrow P$ <b>send</b> $x_L y_m ; P$ $y_m \leftarrow \text{recv } x_L ; P_{y_m}$ <b>close</b> $x_L$ <b>wait</b> $x_L ; Q$ <b>fwd</b> $x_m y_m$ $x_m \leftarrow X_m \leftarrow \overline{y} ; Q_{x_m}$ $x_S \leftarrow \text{detach } x_L ; P_{x_S}$ $x_S \leftarrow \text{release } x_L ; Q_{x_S}$ $x_L \leftarrow \text{acquire } x_S ; Q_{x_L}$ $x_L \leftarrow \text{accept } x_S ; P_{x_L}$	label output label input channel output channel input terminate process wait for process to terminate forward $x$ to $y$ spawn detach (i.e., shift for $\downarrow_L^S R$ ) release (i.e., shift for $\downarrow_L^S L$ ) acquire (i.e., shift for $\uparrow_L^S L$ ) accept (i.e., shift for $\uparrow_L^S R$ )

Table 3: Abstract syntax.

## B Statics

### B.1 Signature Checking

A well-formed *signature*  $\Sigma$  consists of a finite set of process definitions and recursive session type definitions. A process definition  $x_m : A_m \leftarrow X_m \leftarrow \overline{y : B} = P_{x_m, \overline{y}}$  associates a name  $X$  with a process term  $P$  and indicates the name and type of the process' providing channel and the names and types of its argument channels. A recursive session type definition  $Y_m = A_m$  associates a name  $Y$  with a type term  $A$ :

$$\Sigma \triangleq \begin{array}{l} \cdot \\ | \quad x_m : A_m \leftarrow X_m \leftarrow \overline{y : B} = P_{x_m, \overline{y}}, \Sigma' \quad (X_m \text{ not in } \Sigma') \\ | \quad Y_m = A_m, \Sigma' \quad (Y_m \text{ not in } \Sigma') \end{array}$$

We assume that the signature  $\Sigma$  is populated prior to type-checking. Each process definition  $x_m : A_m \leftarrow X_m \leftarrow \overline{y} : \overline{B} = P_{x_m, \overline{y}}$  gives rise to a corresponding persistent predicate  $\text{!def}(x_m : A_m \leftarrow X_m \leftarrow \overline{y} : \overline{B} = P_{x_m, \overline{y}})$  with variables  $x_m$  and  $\overline{y}$  bound by  $P$  that are subject to  $\alpha$ -variance to guarantee freshness. We use the subscript notation to separate the linear  $\overline{y}_L$  from the shared  $\overline{y}_S$  part of the arguments  $\overline{y}$ .

The rules for type-checking the signature are given in Figure 14, Figure 15, and Figure 16. For recursive session types, we adopt the *equi-recursive* [14] interpretation developed by Gay and Hole 2005, requiring recursive session types to be *contractive*. This requirement is stipulated in rule (T- $\Sigma_3$ ) and defined by the rules shown in Figure 15. Moreover, we require session types to be *equi-synchronizing*, a property we introduce in this paper (see Section 3.3) to guarantee that a process is *released* to the *same* type at which it was *acquired*. The property of equi-synchronizing establishes session fidelity without the need for run-time checks at acquisition points.

$$\begin{array}{c}
\frac{}{\vdash_{\Sigma} (\cdot) \text{ sig}} \text{ (T-}\Sigma_1\text{)} \\
\frac{\overline{y}_S : \overline{B}_S; \overline{y}_L : \overline{B}_L \vdash_{\Sigma} P_{x_m, \overline{y}} :: (x_m : A_m) \quad \vdash_{\Sigma} (A_m, \top) \text{ esync} \quad \vdash_{\Sigma} \Sigma' \text{ sig}}{\vdash_{\Sigma} (x_m : A_m \leftarrow X_m \leftarrow \overline{y}_L : \overline{B}_L, \overline{y}_S : \overline{B}_S = P_{x_m, \overline{y}}), \Sigma' \text{ sig}} \text{ (T-}\Sigma_2\text{)} \\
\frac{\vdash_{\Sigma} A_m \text{ contr} \quad \vdash_{\Sigma} (A_m, \top) \text{ esync} \quad \vdash_{\Sigma} \Sigma' \text{ sig}}{\vdash_{\Sigma} Y_m = A_m, \Sigma' \text{ sig}} \text{ (T-}\Sigma_3\text{)}
\end{array}$$

Figure 14: Signature checking.

$$\begin{array}{ccc}
\frac{}{\vdash_{\Sigma} \oplus \{\overline{l} : A_L\} \text{ contr}} \text{ (T-CONTR}_{\oplus}\text{)} & \frac{}{\vdash_{\Sigma} \& \{\overline{l} : A_L\} \text{ contr}} \text{ (T-CONTR}_{\&}\text{)} & \frac{}{\vdash_{\Sigma} A_L \otimes B_L \text{ contr}} \text{ (T-CONTR}_{\otimes}\text{)} \\
\frac{}{\vdash_{\Sigma} A_L \multimap B_L \text{ contr}} \text{ (T-CONTR}_{\multimap}\text{)} & \frac{}{\vdash_{\Sigma} \exists A_S. B_L \text{ contr}} \text{ (T-CONTR}_{\exists}\text{)} & \frac{}{\vdash_{\Sigma} \Pi A_S. B_L \text{ contr}} \text{ (T-CONTR}_{\Pi}\text{)} \\
\frac{}{\vdash_{\Sigma} \mathbf{1} \text{ contr}} \text{ (T-CONTR}_{\mathbf{1}}\text{)} & \frac{}{\vdash_{\Sigma} \downarrow_m^r A_r \text{ contr}} \text{ (T-CONTR}_{\downarrow_m^r}\text{)} & \frac{}{\vdash_{\Sigma} \uparrow_m^r A_m \text{ contr}} \text{ (T-CONTR}_{\uparrow_m^r}\text{)}
\end{array}$$

Figure 15: Contractive recursive session type.

## B.2 Process Typing

To type *process terms*, we use the judgments:

$$\begin{array}{c}
\Gamma \vdash_{\Sigma} P :: (x_s : A_s) \\
\Gamma; \Delta \vdash_{\Sigma} P :: (x_L : A_L)
\end{array}$$

The judgments rely on the signature  $\Sigma$  and on the contexts  $\Gamma$  and  $\Delta$  to type shared and linear channels, respectively. The judgment indicates that the process  $P$  provides a service of session type  $A_m$  along channel  $x_m$ , given the typing of services provided by processes along the channels in  $\Delta$  (and  $\Gamma$ ). Since channels are substituted for variables at run-time, we allow the metavariables  $x_m, y_m, z_m$  to stand for both variables and channels.

The context  $\Gamma$  is a *structural* context that consists of a finite set of assumptions of the form  $x_{s_i} : A_{s_i}$ , associating with each shared variable or channel a session type. A *well-formed* structural context is defined as follows:

$$\Gamma \triangleq \cdot \mid \Gamma', x_s : \hat{A} \quad (x_s \text{ not in } \Gamma')$$

$$\begin{array}{c}
\frac{(\forall i) \vdash_{\Sigma} (A_{L_i}, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (\oplus \{\overline{l} : A_L\}, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\oplus}\text{)} \qquad \frac{(\forall i) \vdash_{\Sigma} (A_{L_i}, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (\&\{\overline{l} : A_L\}, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\&}\text{)} \\
\\
\frac{\vdash_{\Sigma} (B_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (A_L \otimes B_L, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\otimes}\text{)} \qquad \frac{\vdash_{\Sigma} (B_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (A_L \multimap B_L, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\multimap}\text{)} \\
\\
\frac{\vdash_{\Sigma} (B_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (\exists A_S, B_L, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\exists}\text{)} \qquad \frac{\vdash_{\Sigma} (B_L, \hat{D}) \text{ esync}}{\vdash_{\Sigma} (\Pi A_S, B_L, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\Pi}\text{)} \\
\\
\frac{}{\vdash_{\Sigma} (\mathbf{1}, \hat{D}) \text{ esync}} \text{ (T-ESYNC}_{\mathbf{1}}\text{)} \\
\\
\frac{\vdash_{\Sigma} (A_L, \uparrow_L^S A_L) \text{ esync}}{\vdash_{\Sigma} (\uparrow_L^S A_L, \top) \text{ esync}} \text{ (T-ESYNC}_{\uparrow_L^S}\text{)} \qquad \frac{\vdash_{\Sigma} (D_S, \top) \text{ esync}}{\vdash_{\Sigma} (\downarrow_L^S D_S, D_S) \text{ esync}} \text{ (T-ESYNC}_{\downarrow_L^S-1}\text{)} \qquad \frac{\vdash_{\Sigma} (D_S, \top) \text{ esync}}{\vdash_{\Sigma} (\downarrow_L^S D_S, \top) \text{ esync}} \text{ (T-ESYNC}_{\downarrow_L^S-2}\text{)}
\end{array}$$

Figure 16: Equi-synchronizing session type, inductively defined.

The side-condition makes sure that the variable or channel name is unique within the context. When concatenating a context  $\Gamma$  with a context  $\Gamma'$ , variables or channels  $x_s : \hat{A}$  that are shared between  $\Gamma$  and  $\Gamma'$  are contracted, i.e., reduced to one occurrence.

The context  $\Delta$ , on the other hand, is a *linear* context that consists of a finite set of assumptions of the form  $x_{L_i} : A_{L_i}$ , associating with each linear variable or channel a session type. A *well-formed* linear context is defined as follows:

$$\Delta \triangleq \cdot \mid \Delta', x_L : A_L \quad (x_L \text{ not in } \Delta')$$

Again, the side-condition makes sure that the variable or channel name is unique within the context. We define the projections  $\text{dom}(\Gamma)$  and  $\text{dom}(\Delta)$  to project onto the variable or channel names in  $\Gamma$  and  $\Delta$ , respectively, yielding the empty set in case the context is empty.

The well-formedness of a process typing judgment imposes slightly different invariants on variables and channels. Whereas variables are always unique by  $\alpha$ -conversion, uniqueness holds only for linear channels, but not shared channels. For a linear process typing judgment to be well-formed, the following condition must hold:

$$\Gamma; \Delta \vdash_{\Sigma} P :: (x_L : A_L) \quad (x_L \text{ not in } \Delta)$$

The same condition holds only for shared variables in shared process typing judgments:

$$\Gamma \vdash_{\Sigma} P :: (x_S : A_S) \quad (x_S \text{ not in } \Gamma, \text{ if } x_S \text{ is a variable})$$

Since the same shared channel can be substituted for different shared variables, giving rise to the possibility of cycles among shared channels (see Section E.2), the offering channel of a shared process can already occur in its shared context. We point out that no ambiguity arises in that case between the provision and use of a shared channel because  $\text{SILL}_S$  introduces separate constructs for each direction. For example,  $a_S$  in  $x_L \leftarrow \text{accept } a_S; P_{x_L}$  denotes the offering channel whereas it denotes a used channel in  $x_L \leftarrow \text{acquire } a_S; Q_{x_L}$ .

The typing rules for process terms are given in Figure 17 and Figure 18. Due to the difference in invariants shared variables and channels have to obey, a context of the form  $\Gamma, x_S : \hat{A}$  conveys the information that  $x_S$  is fresh, if  $x$  is a variable.

### B.3 Configuration Typing

Figure 19 gives the typing rules for a  $\text{SILL}_S$  configuration  $\Omega$ . A configuration  $\Omega$  is divided into a *linear* part  $\Theta$  and a *shared* part  $\Lambda$ , subject to the following well-formedness conditions:

$$\begin{array}{c}
\frac{}{\Gamma; y_L : A_L \vdash_{\Sigma} \text{fwd } x_L y_L :: (x_L : A_L)} \text{ (T-ID}_L\text{)} \qquad \frac{\hat{A} \leq A_S}{\Gamma, y_S : \hat{A} \vdash_{\Sigma} \text{fwd } x_S y_S :: (x_S : A_S)} \text{ (T-ID}_S\text{)} \\
\frac{(x'_L : A_L \leftarrow X_L \leftarrow \overline{y'_L : B_L}, \overline{w_{S'} : B_S} = P_{x'_L, \overline{y'_L}, \overline{w_{S'}}}) \in \Sigma \quad \Gamma = \overline{w_S : \hat{B}} \quad \overline{\hat{B}} \leq \overline{B_S} \quad \Delta = \overline{y_L : B_L}}{\Gamma, \Gamma'; \Delta, \Delta' \vdash_{\Sigma} x_L \leftarrow X_L \leftarrow \overline{y_L}, \overline{w_S}; Q_{x_L} :: (z_L : C_L)} \text{ (T-SPAWN}_{LL}\text{)} \\
\frac{\Gamma = \overline{y_S : \hat{B}} \quad \overline{\hat{B}} \leq \overline{B} \quad (x'_S : A_S \leftarrow X_S \leftarrow \overline{y'_S : B} = P_{x'_S, \overline{y'_S}}) \in \Sigma \quad \Gamma, \Gamma', x_S : A_S; \Delta \vdash_{\Sigma} Q_{x_S} :: (z_L : C_L)}{\Gamma, \Gamma'; \Delta \vdash_{\Sigma} x_S \leftarrow X_S \leftarrow \overline{y_S}; Q_{x_S} :: (z_L : C_L)} \text{ (T-SPAWN}_{LS}\text{)} \\
\frac{\Gamma = \overline{y_S : \hat{B}} \quad \overline{\hat{B}} \leq \overline{B} \quad (x'_S : A_S \leftarrow X_S \leftarrow \overline{y'_S : B} = P_{x'_S, \overline{y'_S}}) \in \Sigma \quad \Gamma, \Gamma', x_S : A_S \vdash_{\Sigma} Q_{x_S} :: (z_S : C_S)}{\Gamma, \Gamma' \vdash_{\Sigma} x_S \leftarrow X_S \leftarrow \overline{y_S}; Q_{x_S} :: (z_S : C_S)} \text{ (T-SPAWN}_{SS}\text{)}
\end{array}$$

Figure 17: Process typing for identity and spawn (cut).

$$\begin{array}{ll}
\Omega \triangleq \cdot \mid \Lambda; \Theta & (\forall a. \text{proc}(a_L, -) \in \Theta \implies \text{unavail}(a_S) \in \Lambda) \\
\Lambda \triangleq \cdot \mid \text{proc}(a_S, P_{a_S}), \Lambda' \mid \text{unavail}(a_S), \Lambda' & (\text{proc}(a_S, -), \text{unavail}(a_S) \text{ not in } \Lambda') \\
\Theta \triangleq \cdot \mid \text{proc}(a_L, P_{a_L}), \Theta' & (\text{proc}(a_L, -) \text{ not in } \Theta')
\end{array}$$

We provide further explanations on the configuration typing in Section 5.1, but point out that Rule (T- $\Omega$ ) permits cyclic dependencies along shared channels by type-checking a configuration relative to all shared channels. Moreover, the rules adhere to the convention to use the metavariables  $x$ ,  $y$ , and  $z$  to stand for variables and the metavariables  $a$ ,  $b$ ,  $c$ , and  $d$  to stand for channels. Channels are run-time artifacts, and thus it is known at this stage whether a metavariable denotes a variable or a channel.

## C Operational Semantics

Figure 20 gives the operational semantics of  $\text{SILL}_S$  using *multiset rewriting rules* [11]. The semantics is *synchronous*, Section 5.3 sketches how to transform this synchronous semantics into an asynchronous one using cut and identity.

A multiset rewriting rule is of the form

$$S_1, \dots, S_n \longrightarrow T_1, \dots, T_m$$

indicating that the state consisting of  $S_1, \dots, S_n$  transitions to the one consisting of  $T_1, \dots, T_m$ . The ' $\longrightarrow$ ' denotes the state transition, substates are separated by ', '. The rule only mentions the part of the state that it rewrites.

We use the following predicates to denote states:

- $\text{proc}(a_m, P_m)$ , for a process providing along channel  $a_m$  and executing the process term  $P_m$ ;
- $\text{unavail}(a_S)$ , as a placeholder for a shared process providing along channel  $a_S$  that is currently not available;
- $!\text{def}(x_m : A_m \leftarrow X_m \leftarrow \overline{y} : \overline{B} = P_{x_m, \overline{y}})$ , for each  $(x_m : A_m \leftarrow X_m \leftarrow \overline{y} : \overline{B} = P_{x_m, \overline{y}}) \in \Sigma$ .

Even though multiset rewriting rules are unordered, we write the rules such that a providing process appears to the right of its client, for ease of reading.

We use the side-condition (*b fresh*) in

$$S \longrightarrow T \quad (b \text{ fresh})$$

to allocate a globally *fresh* channel. The freshly allocated channel may occur in  $T$  but not yet in  $S$ .



$$\begin{array}{c}
\frac{\hat{B} \leq \uparrow_L^s A_L \quad \Gamma, x_S : \hat{B}; \Delta, x_L : A_L \vdash_\Sigma Q_{x_L} :: (z_L : C_L)}{\Gamma, x_S : \hat{B}; \Delta \vdash_\Sigma x_L \leftarrow \mathbf{acquire} x_S; Q_{x_L} :: (z_L : C_L)} \text{(T-}\uparrow_L^s\text{)} \\
\frac{\Gamma, x_S : A_S; \Delta \vdash_\Sigma Q_{x_S} :: (z_L : C_L)}{\Gamma; \Delta, x_L : \downarrow_L^s A_S \vdash_\Sigma x_S \leftarrow \mathbf{release} x_L; Q_{x_S} :: (z_L : C_L)} \text{(T-}\downarrow_L^s\text{)} \\
\frac{\Gamma; \Delta \vdash_\Sigma Q :: (z_L : C_L)}{\Gamma; \Delta, x_L : \mathbf{1} \vdash_\Sigma \mathbf{wait} x_L; Q :: (z_L : C_L)} \text{(T-}\mathbf{1}_L\text{)} \\
\frac{\Gamma; \Delta, x_L : B_L, y_L : A_L \vdash_\Sigma Q_{y_L} :: (z_L : C_L)}{\Gamma; \Delta, x_L : A_L \otimes B_L \vdash_\Sigma y_L \leftarrow \mathbf{recv} x_L; Q_{y_L} :: (z_L : C_L)} \text{(T-}\otimes_L\text{)} \\
\frac{\Gamma, y_S : A_S; \Delta, x_L : B_L \vdash_\Sigma Q_{y_S} :: (z_L : C_L)}{\Gamma; \Delta, x_L : (\exists A_S. B_L) \vdash_\Sigma y_S \leftarrow \mathbf{recv} x_L; Q_{y_S} :: (z_L : C_L)} \text{(T-}\exists_L\text{)} \\
\frac{\Gamma; \Delta, x_L : B_L \vdash_\Sigma Q :: (z_L : C_L)}{\Gamma; \Delta, x_L : A_L \multimap B_L, y_L : A_L \vdash_\Sigma \mathbf{send} x_L y_L; Q :: (z_L : C_L)} \text{(T-}\multimap_L\text{)} \\
\frac{\hat{A} \leq A_S \quad \Gamma, y_S : \hat{A}; \Delta, x_L : B_L \vdash_\Sigma Q :: (z_L : C_L)}{\Gamma, y_S : \hat{A}; \Delta, x_L : (\Pi A_S. B_L) \vdash_\Sigma \mathbf{send} x_L y_S; Q :: (z_L : C_L)} \text{(T-}\Pi_L\text{)} \\
\frac{(\forall i) \Gamma; \Delta, x_L : A_{L_i} \vdash_\Sigma Q_i :: (z_L : C_L)}{\Gamma; \Delta, x_L : \oplus \{\bar{l} : A_L\} \vdash_\Sigma \mathbf{case} x_L \text{ of } \bar{l} \Rightarrow \bar{Q} :: (z_L : C_L)} \text{(T-}\oplus_L\text{)} \\
\frac{\Gamma; \Delta, x_L : A_{L_h} \vdash_\Sigma Q :: (z_L : C_L)}{\Gamma; \Delta, x_L : \& \{\bar{l} : A_L\} \vdash_\Sigma x_L.l_h; Q :: (z_L : C_L)} \text{(T-}\&_L\text{)} \\
\frac{\Gamma; \cdot \vdash_\Sigma P_{x_L} :: (x_L : A_L)}{\Gamma \vdash_\Sigma x_L \leftarrow \mathbf{accept} x_S; P_{x_L} :: (x_S : \uparrow_L^s A_L)} \text{(T-}\uparrow_L^s\text{)} \\
\frac{\Gamma \vdash_\Sigma P_{x_S} :: (x_S : A_S)}{\Gamma; \cdot \vdash_\Sigma x_S \leftarrow \mathbf{detach} x_L; P_{x_S} :: (x_L : \downarrow_L^s A_S)} \text{(T-}\downarrow_L^s\text{)} \\
\frac{}{\Gamma; \cdot \vdash_\Sigma \mathbf{close} x_L :: (x_L : \mathbf{1})} \text{(T-}\mathbf{1}_R\text{)} \\
\frac{\Gamma; \Delta \vdash_\Sigma P :: (x_L : B_L)}{\Gamma; \Delta, y_L : A_L \vdash_\Sigma \mathbf{send} x_L y_L; P :: (x_L : A_L \otimes B_L)} \text{(T-}\otimes_R\text{)} \\
\frac{\hat{A} \leq A_S \quad \Gamma, y_S : \hat{A}; \Delta \vdash_\Sigma P :: (x_L : B_L)}{\Gamma, y_S : \hat{A}; \Delta \vdash_\Sigma \mathbf{send} x_L y_S; P :: (x_L : (\exists A_S. B_L))} \text{(T-}\exists_R\text{)} \\
\frac{\Gamma; \Delta, y_L : A_L \vdash_\Sigma P_{y_L} :: (x_L : B_L)}{\Gamma; \Delta \vdash_\Sigma y_L \leftarrow \mathbf{recv} x_L; P_{y_L} :: (x_L : A_L \multimap B_L)} \text{(T-}\multimap_R\text{)} \\
\frac{\Gamma, y_S : A_S; \Delta \vdash_\Sigma P_{y_S} :: (x_L : B_L)}{\Gamma; \Delta \vdash_\Sigma y_S \leftarrow \mathbf{recv} x_L; P_{y_S} :: (x_L : (\Pi A_S. B_L))} \text{(T-}\Pi_R\text{)} \\
\frac{\Gamma; \Delta \vdash_\Sigma P :: (x_L : A_{L_h})}{\Gamma; \Delta \vdash_\Sigma x_L.l_h; P :: (x_L : \oplus \{\bar{l} : A_L\})} \text{(T-}\oplus_R\text{)} \\
\frac{(\forall i) \Gamma; \Delta \vdash_\Sigma P_i :: (x_L : A_{L_i})}{\Gamma; \Delta \vdash_\Sigma \mathbf{case} x_L \text{ of } \bar{l} \Rightarrow \bar{P} :: (x_L : \& \{\bar{l} : A_L\})} \text{(T-}\&_R\text{)}
\end{array}$$

Figure 18: Process typing for shifts and propositional rules.

Lastly, we use the equation  $a = b$  in

$$S \longrightarrow T, a = b$$

for the global substitution of  $b$  for  $a$  in the entire post-configuration. The channels  $a$  and  $b$  may occur in  $S$ .

$$\begin{array}{c}
\frac{}{\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)} \text{ (T-}\Theta_1\text{)} \\
\frac{(a_S : \hat{B}) \in \Gamma \quad \vdash_{\Sigma} (A_L, \hat{B}) \text{ esync} \quad \Gamma; \Delta' \vdash_{\Sigma} P_{a_L} :: (a_L : A_L) \quad \Gamma \vDash_{\Sigma} \Theta : \Delta, \Delta'}{\Gamma \vDash_{\Sigma} \text{proc}(a_L, P_{a_L}), \Theta :: (\Delta, a_L : A_L)} \text{ (T-}\Theta_2\text{)} \\
\frac{}{\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)} \text{ (T-}\Lambda_1\text{)} \\
\frac{\vdash_{\Sigma} (\uparrow_L^S A_L, \top) \text{ esync} \quad \Gamma \vdash_{\Sigma} P_{a_S} :: (a_S : \uparrow_L^S A_L)}{\Gamma \vDash_{\Sigma} \text{proc}(a_S, P_{a_S}) :: (a_S : \uparrow_L^S A_L)} \text{ (T-}\Lambda_2\text{)} \\
\frac{}{\Gamma \vDash_{\Sigma} \text{unavail}(a_S) :: (a_S : \hat{A})} \text{ (T-}\Lambda_3\text{)} \\
\frac{\Gamma \vDash_{\Sigma} \Lambda :: \Gamma' \quad \Gamma \vDash_{\Sigma} \Lambda' :: \Gamma''}{\Gamma \vDash_{\Sigma} \Lambda, \Lambda' :: \Gamma', \Gamma''} \text{ (T-}\Lambda_4\text{)} \\
\frac{\Gamma \vDash_{\Sigma} \Lambda :: \Gamma \quad \Gamma \vDash_{\Sigma} \Theta :: \Delta}{\Gamma \vDash_{\Sigma} \Lambda; \Theta :: \Gamma; \Delta} \text{ (T-}\Omega\text{)}
\end{array}$$

Figure 19: Configuration typing.

$$\begin{array}{l}
\text{proc}(a_L, \text{fwd } a_L b_L) \quad (\text{D-ID}_L) \\
\longrightarrow a_L = b_L, a_S = b_S \\
\\
\text{proc}(a_S, \text{fwd } a_S b_S) \quad (\text{D-ID}_S) \\
\longrightarrow \text{unavail}(a_S), a_S = b_S \\
\\
\text{proc}(a_L, x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L}), !\text{def}(x'_L : A_L \leftarrow X_L \leftarrow \overline{y_L} : \overline{B_L}, y_S : \overline{B_S} = P_{x'_L, \overline{y_L}, \overline{y_S}}) \quad (\text{D-SPAWN}_{LL}) \\
\longrightarrow \text{proc}(a_L, [b_L/x'_L] Q_{x_L}), \text{proc}(b_L, [b_L/x'_L, \overline{c_L}/\overline{y_L}, \overline{c_S}/\overline{y_S}] P_{x'_L, \overline{y_L}, \overline{y_S}}), \text{unavail}(b_S) \quad (b \text{ fresh}) \\
\\
\text{proc}(a_L, x_S \leftarrow X_S \leftarrow \overline{c_S}; Q_{x_S}), !\text{def}(x'_S : A_S \leftarrow X_S \leftarrow \overline{y_S} : \overline{B_S} = P_{x'_S, \overline{y_S}}) \quad (\text{D-SPAWN}_{LS}) \\
\longrightarrow \text{proc}(a_L, [b_S/x'_S] Q_{x_S}), \text{proc}(b_S, [b_S/x'_S, \overline{c_S}/\overline{y_S}] P_{x'_S, \overline{y_S}}) \quad (b \text{ fresh}) \\
\\
\text{proc}(a_S, x_S \leftarrow X_S \leftarrow \overline{c_S}; Q_{x_S}), !\text{def}(x'_S : A_S \leftarrow X_S \leftarrow \overline{y_S} : \overline{B_S} = P_{x'_S, \overline{y_S}}) \quad (\text{D-SPAWN}_{SS}) \\
\longrightarrow \text{proc}(a_S, [b_S/x'_S] Q_{x_S}), \text{proc}(b_S, [b_S/x'_S, \overline{c_S}/\overline{y_S}] P_{x'_S, \overline{y_S}}) \quad (b \text{ fresh}) \\
\\
\text{proc}(c_L, x_S \leftarrow \text{release } a_L; Q_{x_S}), \text{proc}(a_L, x_S \leftarrow \text{detach } a_L; P_{x_S}), \text{unavail}(a_S) \quad (\text{D-}\downarrow_L^S - \text{release/detach}) \\
\longrightarrow \text{proc}(c_L, [a_S/x_S] Q_{x_S}), \text{proc}(a_S, [a_S/x_S] P_{x_S}) \\
\\
\text{proc}(c_L, x_L \leftarrow \text{acquire } a_S; Q_{x_L}), \text{proc}(a_S, x_L \leftarrow \text{accept } a_S; P_{x_L}) \quad (\text{D-}\uparrow_L^S - \text{acquire/accept}) \\
\longrightarrow \text{proc}(c_L, [a_L/x_L] Q_{x_L}), \text{proc}(a_L, [a_L/x_L] P_{x_L}), \text{unavail}(a_S) \\
\\
\text{proc}(c_L, \text{wait } a_L; Q), \text{proc}(a_L, \text{close } a_L) \quad (\text{D-1}) \\
\longrightarrow \text{proc}(c_L, Q) \\
\\
\text{proc}(c_L, y \leftarrow \text{recv } a_L; Q_y), \text{proc}(a_L, \text{send } a_L b; P) \quad (\text{D-}\otimes/\exists) \\
\longrightarrow \text{proc}(c_L, [b/y] Q_y), \text{proc}(a_L, P) \\
\\
\text{proc}(c_L, \text{send } a_L b; Q), \text{proc}(a_L, y \leftarrow \text{recv } a_L; P_y) \quad (\text{D-}\multimap/\Pi) \\
\longrightarrow \text{proc}(c_L, Q), \text{proc}(a_L, [b/y] P_y) \\
\\
\text{proc}(c_L, \text{case } a_L \text{ of } \overline{l \Rightarrow Q}), \text{proc}(a_L, a_L.l_h; P) \quad (\text{D-}\oplus) \\
\longrightarrow \text{proc}(c_L, Q_h), \text{proc}(a_L, P) \\
\\
\text{proc}(c_L, a_L.l_h; Q), \text{proc}(a_L, \text{case } a_L \text{ of } \overline{l \Rightarrow P}) \quad (\text{D-}\&) \\
\longrightarrow \text{proc}(c_L, Q), \text{proc}(a_L, P_h)
\end{array}$$

Figure 20: Multiset rewriting system defining synchronous operational dynamics.

## D Preservation and Progress

### D.1 Definitions, Lemmas, and Corollaries

#### D.1.1 Definitions

**Definition 1** (Configuration Substitution). *Substitution in configurations is defined as follows:*

$$\begin{aligned}
& [b_L/a_L](\cdot) \triangleq (\cdot) \\
& [b_L/a_L](\text{proc}(c_L, Q), \Theta) \triangleq \text{proc}(c_L, [b_L/a_L] Q), [b_L/a_L] \Theta \quad (\text{where } a_L \neq c_L) \\
& [b_S/a_S](\cdot) \triangleq (\cdot) \\
& [b_S/a_S](\text{proc}(c_L, Q), \Theta) \triangleq \text{proc}(c_L, [b_S/a_S] Q), [b_S/a_S] \Theta \\
& [b_S/a_S](\cdot) \triangleq (\cdot) \\
& [b_S/a_S](\text{proc}(c_S, Q), \Lambda) \triangleq \text{proc}(c_S, [b_S/a_S] Q), [b_S/a_S] \Lambda \quad (\text{where } a_S \neq c_S) \\
& [b_S/a_S](\text{unavail}(c_S), \Lambda) \triangleq \text{unavail}(c_S), [b_S/a_S] \Lambda
\end{aligned}$$

For a well-typed configuration  $\Gamma \vdash_{\Sigma} \text{proc}(c_L, [b_L/a_L] Q), [b_L/a_L] \Theta :: \Delta$  where  $a_L$  occurs in  $Q$ , it follows by induction on the configuration typing that  $[b_L/a_L] \Theta = \Theta$ . In order for  $a_L$  to be used in  $Q$  it must be offered by the sub-configuration  $\Theta$  and hence cannot be used by any process in  $\Theta$ . Both side-conditions are easily met in all cases Definition 1 is used.

**Definition 2** (Concretization). *A concretization is a partial order on structural contexts  $\Gamma$  and is inductively defined by the following rules, relying on the partial order  $\perp \leq \uparrow_L^S C_L \leq \top$ , for any  $C_L$ :*

$$\frac{}{\Gamma \triangleleft (\cdot)} \text{ (T-}\triangleleft_1) \qquad \frac{\hat{A} \leq \hat{B} \quad \Gamma' \triangleleft \Gamma}{\Gamma', x_S : \hat{A} \triangleleft \Gamma, x_S : \hat{B}} \text{ (T-}\triangleleft_2)$$

**Definition 3** (Poised Process and Configuration). *A  $\text{proc}(a, P_a)$  is poised if it is communicating along its providing channel. The poised processes in  $\text{SILL}_S$  are:*

<i>Receiving</i>	<i>Sending</i>
$\text{proc}(a_L, y \leftarrow \text{recv } a_L ; P_y)$	$\text{proc}(a_L, \text{send } a_L b ; P)$
$\text{proc}(a_L, \text{case } a_L \text{ of } \overline{l} \Rightarrow P)$	$\text{proc}(a_L, \text{close } a_L)$
$\text{proc}(a_S, x_L \leftarrow \text{accept } a_S ; P_{x_L})$	$\text{proc}(a_L, a_L.l_h ; P)$
	$\text{proc}(a_L, x_S \leftarrow \text{detach } a_L ; P_{x_S})$

A configuration  $\Theta$  is poised if and only if all  $\text{proc}(a_L, P_{a_L}) \in \Theta$  are poised. A configuration  $\Lambda$  is poised if and only if all  $\text{proc}(a_S, P_{a_S}) \in \Lambda$  are poised.

**Definition 4** (Blocked Process). *A process is blocked along  $a_S$  if it has the form  $\text{proc}(c_L, x_L \leftarrow \text{acquire } a_S ; Q_{x_L})$ .*

#### D.1.2 Lemmas and Corollaries

**Lemma 1** (Process Term Substitution). *Given the partial order  $\perp \leq \uparrow_L^S C_L \leq \top$ , for any  $C_L$ , the following substitutions are type-preserving and thus admissible:*

1. If  $\Gamma; \Delta \vdash_{\Sigma} P :: (x_L : A_L)$ , then, for any fresh  $y_L : A_L$ ,  $\Gamma; \Delta \vdash_{\Sigma} [y_L/x_L] P :: (y_L : A_L)$ .

2. If  $\Gamma; \Delta, y_L : B_L \vdash_{\Sigma} P :: (x_L : A_L)$ , then, for any fresh  $z_L : B_L$ ,  $\Gamma; \Delta, z_L : B_L \vdash_{\Sigma} [z_L/y_L] P :: (x_L : A_L)$ .
3. If  $\Gamma, y_S : \hat{B}; \Delta \vdash_{\Sigma} P :: (x_L : A_L)$ , then, for any fresh  $z_S : \hat{C}$  such that  $\hat{C} \leq \hat{B}$ ,  $\Gamma, z_S : \hat{C}; \Delta \vdash_{\Sigma} [z_S/y_S] P :: (x_L : A_L)$ .
4. If  $\Gamma, y_S : \hat{C}, y'_S : \hat{C}; \Delta \vdash_{\Sigma} P :: (x_L : A_L)$ , then  $\Gamma, y_S : \hat{C}; \Delta \vdash_{\Sigma} [y_S/y'_S] P :: (x_L : A_L)$ .
5. If  $\Gamma \vdash_{\Sigma} P :: (x_S : A_S)$ , then, for any fresh  $y_S : A_S$ ,  $\Gamma, y_S : A_S \vdash_{\Sigma} [y_S/x_S] P :: (y_S : A_S)$ .
6. If  $\Gamma, x_S : A_S, x'_S : A_S \vdash_{\Sigma} P :: (x'_S : A_S)$ , then  $\Gamma, x_S : A_S \vdash_{\Sigma} [x_S/x'_S] P :: (x_S : A_S)$ .
7. If  $\Gamma, y_S : \hat{B} \vdash_{\Sigma} P :: (x_S : A_S)$ , then, for any fresh  $z_S : \hat{C}$  such that  $\hat{C} \leq \hat{B}$ ,  $\Gamma, z_S : \hat{C} \vdash_{\Sigma} [z_S/y_S] P :: (x_S : A_S)$ .
8. If  $\Gamma, y_S : \hat{C}, y'_S : \hat{C} \vdash_{\Sigma} P :: (x_S : A_S)$ , then  $\Gamma, y_S : \hat{C} \vdash_{\Sigma} [y_S/y'_S] P :: (x_S : A_S)$ .

*Proof.* We prove each case in turn:

*Lemma 1-1:* By  $\alpha$ -equivalence.

*Lemma 1-2:* By  $\alpha$ -equivalence.

*Lemma 1-3 and 1-7:* By simultaneous induction on  $\Gamma, y_S : \hat{B}; \Delta \vdash_{\Sigma} P :: (x_L : A_L)$  and  $\Gamma, y_S : \hat{B} \vdash_{\Sigma} P :: (x_S : A_S)$ :

- $\Gamma, y_S : \hat{B}; y_L : A_L \vdash_{\Sigma} \text{fwd } x_L y_L :: (x_L : A_L)$  (this case)  
 $\Gamma, z_S : \hat{C}; y_L : A_L \vdash_{\Sigma} \text{fwd } x_L y_L :: (x_L : A_L)$  (since  $y_S$  does not occur in the process term)  
for some fresh  $z_S : \hat{C}$  such that  $\hat{C} \leq \hat{B}$
- $\Gamma, y_S : \hat{B} \vdash_{\Sigma} \text{fwd } x_S y_S :: (x_S : B_S)$  and  $\hat{B} \leq B_S$  (this case)  
 $\Gamma, z_S : \hat{C} \vdash_{\Sigma} \text{fwd } x_S z_S :: (x_S : B_S)$  (by (T-ID<sub>S</sub>) and since  $\hat{C} \leq B_S$  by transitivity of  $\leq$ )  
for some fresh  $z_S : \hat{C}$  such that  $\hat{C} \leq \hat{B}$
- $\Gamma, x'_S : \hat{A}, y_S : \hat{B} \vdash_{\Sigma} \text{fwd } x_S x'_S :: (x_S : A_S)$  and  $\hat{A} \leq A_S$  (this case)  
 $\Gamma, x'_S : \hat{A}, z_S : \hat{C} \vdash_{\Sigma} \text{fwd } x_S x'_S :: (x_S : A_S)$  (since  $y_S$  does not occur in the process term)  
for some fresh  $z_S : \hat{C}$  such that  $\hat{C} \leq \hat{B}$
- $\Gamma, \Gamma', y_S : \hat{B}; \Delta, \Delta' \vdash_{\Sigma} x'_L \leftarrow X_L \leftarrow \overline{y_L}, \overline{w_S}, y_S; Q_{x'_L} :: (x_L : A_L)$  (this case)  
 $(x''_L : A'_L \leftarrow X_L \leftarrow \overline{y'_L}, \overline{w'_S}, y'_S : B_S = P_{x'_L, \overline{y'_L}, \overline{w'_S}, y'_S}) \in \Sigma$  (this case)  
 $\hat{B} \leq \overline{B_S}$  and  $\Gamma = w_S : \hat{B}$  and  $\Delta = \overline{y_L} : \overline{B_L}$  (this case)  
 $\Gamma, \Gamma', y_S : \hat{B}; \Delta', x'_L : A'_L \vdash_{\Sigma} Q_{x'_L} :: (x_L : A_L)$  (this case)  
 $\Gamma, \Gamma', z_S : \hat{C}; \Delta', x'_L : A'_L \vdash_{\Sigma} [z_S/y_S] Q_{x'_L} :: (x_L : A_L)$  (by I.H.)  
for some fresh  $z_S : \hat{C}$  such that  $\hat{C} \leq \hat{B}$   
 $\Gamma, \Gamma', z_S : \hat{C}; \Delta, \Delta' \vdash_{\Sigma} x'_L \leftarrow X_L \leftarrow \overline{y_L}, \overline{w_S}, z_S; [z_S/y_S] Q_{x'_L} :: (x_L : A_L)$  (by (T-SPAWN<sub>LL</sub>))
- $\Gamma, \Gamma', y_S : \hat{B}; \Delta, \Delta' \vdash_{\Sigma} x'_L \leftarrow X_L \leftarrow \overline{y_L}, \overline{w_S}; Q_{x'_L} :: (x_L : A_L)$  (this case)  
where  $y_S \notin \overline{w_S}$   
 $(x''_L : A'_L \leftarrow X_L \leftarrow \overline{y'_L}, \overline{w'_S} : B_S = P_{x'_L, \overline{y'_L}, \overline{w'_S}}) \in \Sigma$  (this case)  
 $\hat{B} \leq \overline{B_S}$  and  $\Gamma = w_S : \hat{B}$  and  $\Delta = \overline{y_L} : \overline{B_L}$  (this case)  
 $\Gamma, \Gamma', y_S : \hat{B}; \Delta', x'_L : A'_L \vdash_{\Sigma} Q_{x'_L} :: (x_L : A_L)$  (this case)  
 $\Gamma, \Gamma', z_S : \hat{C}; \Delta', x'_L : A'_L \vdash_{\Sigma} [z_S/y_S] Q_{x'_L} :: (x_L : A_L)$  (by I.H.)  
for some fresh  $z_S : \hat{C}$  such that  $\hat{C} \leq \hat{B}$   
 $\Gamma, \Gamma', z_S : \hat{C}; \Delta, \Delta' \vdash_{\Sigma} x'_L \leftarrow X_L \leftarrow \overline{y_L}, \overline{w_S}; [z_S/y_S] Q_{x'_L} :: (x_L : A_L)$  (by (T-SPAWN<sub>LL</sub>))
- $\Gamma, \Gamma', y_S : \hat{B}; \Delta \vdash_{\Sigma} x'_S \leftarrow X_S \leftarrow \overline{w_S}, y_S; Q_{x'_S} :: (x_L : A_L)$  (this case)  
 $(x''_S : A_S \leftarrow X_S \leftarrow \overline{w'_S}, y'_S : B_S = P_{x'_S, \overline{w'_S}, y'_S}) \in \Sigma$  (this case)  
 $\hat{B} \leq \overline{B_S}$  and  $\Gamma = w_S : \hat{B}$  (this case)  
 $\Gamma, \Gamma', y_S : \hat{B}, x'_S : A_S; \Delta \vdash_{\Sigma} Q_{x'_S} :: (x_L : A_L)$  (this case)  
 $\Gamma, \Gamma', z_S : \hat{C}, x'_S : A_S; \Delta \vdash_{\Sigma} [z_S/y_S] Q_{x'_S} :: (x_L : A_L)$  (by I.H.)  
for some fresh  $z_S : \hat{C}$  such that  $\hat{C} \leq \hat{B}$   
 $\Gamma, \Gamma', z_S : \hat{C}; \Delta \vdash_{\Sigma} x'_S \leftarrow X_S \leftarrow \overline{w_S}, z_S; [z_S/y_S] Q_{x'_S} :: (x_L : A_L)$  (by (T-SPAWN<sub>LS</sub>))

- $\Gamma, \Gamma', y_s : \hat{B}; \Delta \vdash_{\Sigma} x'_s \leftarrow X_s \leftarrow \overline{w_s}; Q_{x'_s} :: (x_L : A_L)$  (this case)  
*where  $y_s \notin \overline{w_s}$*   
 $(x''_s : A_s \leftarrow X_s \leftarrow \overline{w'_s : B_s} = P_{x'_s, \overline{w'_s}}) \in \Sigma$  (this case)  
 $\hat{B} \leq \overline{B_s}$  and  $\Gamma = w_s : \hat{B}$  (this case)  
 $\Gamma, \Gamma', y_s : \hat{B}, x'_s : A_s; \Delta \vdash_{\Sigma} Q_{x'_s} :: (x_L : A_L)$  (this case)  
 $\Gamma, \Gamma', z_s : \hat{C}, x'_s : A_s; \Delta \vdash_{\Sigma} [z_s/y_s] Q_{x'_s} :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, \Gamma', z_s : \hat{C}; \Delta \vdash_{\Sigma} x'_s \leftarrow X_s \leftarrow \overline{w_s}; [z_s/y_s] Q_{x'_s} :: (x_L : A_L)$  (by (T-SPAWN<sub>LS</sub>))
- $\Gamma, \Gamma', y_s : \hat{B} \vdash_{\Sigma} x'_s \leftarrow X_s \leftarrow \overline{w_s}, y_s; Q_{x'_s} :: (x_s : A_s)$  (this case)  
 $(x''_s : A'_s \leftarrow X_s \leftarrow \overline{w'_s : B_s}, y'_s : B_s = P_{x'_s, \overline{w'_s}, y'_s}) \in \Sigma$  (this case)  
 $\hat{B} \leq \overline{B_s}$  and  $\Gamma = w_s : \hat{B}$  (this case)  
 $\Gamma, \Gamma', y_s : \hat{B}, x'_s : A'_s \vdash_{\Sigma} Q_{x'_s} :: (x_s : A_s)$  (this case)  
 $\Gamma, \Gamma', z_s : \hat{C}, x'_s : A'_s \vdash_{\Sigma} [z_s/y_s] Q_{x'_s} :: (x_s : A_s)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, \Gamma', z_s : \hat{C} \vdash_{\Sigma} x'_s \leftarrow X_s \leftarrow \overline{w_s}, z_s; [z_s/y_s] Q_{x'_s} :: (x_s : A_s)$  (by (T-SPAWN<sub>SS</sub>))
- $\Gamma, \Gamma', y_s : \hat{B} \vdash_{\Sigma} x'_s \leftarrow X_s \leftarrow \overline{w_s}; Q_{x'_s} :: (x_s : A_s)$  (this case)  
*where  $y_s \notin \overline{w_s}$*   
 $(x''_s : A'_s \leftarrow X_s \leftarrow \overline{w'_s : B_s} = P_{x'_s, \overline{w'_s}}) \in \Sigma$  (this case)  
 $\hat{B} \leq \overline{B_s}$  and  $\Gamma = w_s : \hat{B}$  (this case)  
 $\Gamma, \Gamma', y_s : \hat{B}, x'_s : A'_s \vdash_{\Sigma} Q_{x'_s} :: (x_s : A_s)$  (this case)  
 $\Gamma, \Gamma', z_s : \hat{C}, x'_s : A'_s \vdash_{\Sigma} [z_s/y_s] Q_{x'_s} :: (x_s : A_s)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, \Gamma', z_s : \hat{C} \vdash_{\Sigma} x'_s \leftarrow X_s \leftarrow \overline{w_s}; [z_s/y_s] Q_{x'_s} :: (x_s : A_s)$  (by (T-SPAWN<sub>SS</sub>))
- $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} x'_L \leftarrow \text{acquire } y_s; Q_{x'_L} :: (x_L : A_L)$  (this case)  
 $\hat{B} \leq \uparrow_L^s C_L$  (this case)  
 $\Gamma, y_s : \hat{B}; \Delta, x'_L : C_L \vdash_{\Sigma} Q_{x'_L} :: (x_L : A_L)$  (this case)  
 $\Gamma, z_s : \hat{C}; \Delta, x'_L : C_L \vdash_{\Sigma} [z_s/y_s] Q_{x'_L} :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\hat{C} \leq \uparrow_L^s C_L$  (by transitivity of  $\leq$ )  
 $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} x'_L \leftarrow \text{acquire } z_s; [z_s/y_s] Q_{x'_L} :: (x_L : A_L)$  (by (T- $\uparrow_L^s$ ))
- $\Gamma, y_s : \hat{B}, y'_s : \hat{D}; \Delta \vdash_{\Sigma} x'_L \leftarrow \text{acquire } y'_s; Q_{x'_L} :: (x_L : A_L)$  (this case)  
 $\hat{D} \leq \uparrow_L^s C_L$  (this case)  
 $\Gamma, y_s : \hat{B}, y'_s : \hat{D}; \Delta, x'_L : C_L \vdash_{\Sigma} Q_{x'_L} :: (x_L : A_L)$  (this case)  
 $\Gamma, z_s : \hat{C}, y'_s : \hat{D}; \Delta, x'_L : C_L \vdash_{\Sigma} [z_s/y_s] Q_{x'_L} :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, z_s : \hat{C}, y'_s : \hat{D}; \Delta \vdash_{\Sigma} x'_L \leftarrow \text{acquire } y'_s; [z_s/y_s] Q_{x'_L} :: (x_L : A_L)$  (by (T- $\uparrow_L^s$ ))
- $\Gamma, y_s : \hat{B} \vdash_{\Sigma} x_L \leftarrow \text{accept } x_s; P_{x_L} :: (x_s : \uparrow_L^s A_L)$  (this case)  
 $\Gamma, y_s : \hat{B}; \cdot \vdash_{\Sigma} P_{x_L} :: (x_L : A_L)$  (this case)  
 $\Gamma, z_s : \hat{C}; \cdot \vdash_{\Sigma} [z_s/y_s] P_{x_L} :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, z_s : \hat{C} \vdash_{\Sigma} x_L \leftarrow \text{accept } x_s; [z_s/y_s] P_{x_L} :: (x_s : \uparrow_L^s A_L)$  (by (T- $\uparrow_L^s$ ))
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : \downarrow_L^s A_s \vdash_{\Sigma} x'_s \leftarrow \text{release } x'_L; Q_{x'_s} :: (x_L : A_L)$  (this case)

- $\Gamma, y_s : \hat{B}, x'_s : A_s; \Delta \vdash_{\Sigma} Q_{x'_s} :: (x_L : A_L)$  (this case)  
 $\Gamma, z_s : \hat{C}, x'_s : A_s; \Delta \vdash_{\Sigma} [z_s/y_s] Q_{x'_s} :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, z_s : \hat{C}; \Delta, x'_L : \downarrow_L^s A_s \vdash_{\Sigma} x'_s \leftarrow \text{release } x'_L; [z_s/y_s] Q_{x'_s} :: (x_L : A_L)$  (by (T- $\downarrow_L^s$ ))
- $\Gamma, y_s : \hat{B}; \cdot \vdash_{\Sigma} x_s \leftarrow \text{detach } x_L; P_{x_s} :: (x_L : \downarrow_L^s A_s)$  (this case)  
 $\Gamma, y_s : \hat{B} \vdash_{\Sigma} P_{x_s} :: (x_s : A_s)$  (this case)  
 $\Gamma, z_s : \hat{C} \vdash_{\Sigma} [z_s/y_s] P_{x_s} :: (x_s : A_s)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, z_s : \hat{C}; \cdot \vdash_{\Sigma} x_s \leftarrow \text{detach } x_L; [z_s/y_s] P_{x_s} :: (x_L : \downarrow_L^s A_s)$  (by (T- $\downarrow_L^s$ R))
- $\Gamma, y_s : \hat{B}; \Delta, y_L : \mathbf{1} \vdash_{\Sigma} \text{wait } y_L; Q :: (x_L : A_L)$  (this case)  
 $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} Q :: (x_L : A_L)$  (this case)  
 $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} [z_s/y_s] Q :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, z_s : \hat{C}; \Delta, y_L : \mathbf{1} \vdash_{\Sigma} \text{wait } y_L; [z_s/y_s] Q :: (x_L : A_L)$  (by (T- $\mathbf{1}$ L))
- $\Gamma, y_s : \hat{B}; \cdot \vdash_{\Sigma} \text{close } x_L :: (x_L : \mathbf{1})$  (this case)  
 $\Gamma, z_s : \hat{C}; \cdot \vdash_{\Sigma} \text{close } x_L :: (x_L : \mathbf{1})$  (since  $y_s$  does not occur in the process term)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : B_L \otimes C_L \vdash_{\Sigma} y_L \leftarrow \text{recv } x'_L; Q_{y_L} :: (x_L : A_L)$  (this case)  
 $\Gamma, y_s : \hat{B}; \Delta, x'_L : C_L, y_L : B_L \vdash_{\Sigma} Q_{y_L} :: (x_L : A_L)$  (this case)  
 $\Gamma, z_s : \hat{C}; \Delta, x'_L : C_L, y_L : B_L \vdash_{\Sigma} [z_s/y_s] Q_{y_L} :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, z_s : \hat{C}; \Delta, x'_L : B_L \otimes C_L \vdash_{\Sigma} y_L \leftarrow \text{recv } x'_L; [z_s/y_s] Q_{y_L} :: (x_L : A_L)$  (by (T- $\otimes$ L))
- $\Gamma, y_s : \hat{B}; \Delta, y_L : A_L \vdash_{\Sigma} \text{send } x_L y_L; P :: (x_L : A_L \otimes B_L)$  (this case)  
 $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} P :: (x_L : B_L)$  (this case)  
 $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} [z_s/y_s] P :: (x_L : B_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, z_s : \hat{C}; \Delta, y_L : A_L \vdash_{\Sigma} \text{send } x_L y_L; [z_s/y_s] P :: (x_L : A_L \otimes B_L)$  (by (T- $\otimes$ R))
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : (\exists A_s. B_L) \vdash_{\Sigma} y'_s \leftarrow \text{recv } x'_L; Q_{y'_s} :: (x_L : A_L)$  (this case)  
 $\Gamma, y_s : \hat{B}, y'_s : A_s; \Delta, x'_L : B_L \vdash_{\Sigma} Q_{y'_s} :: (x_L : A_L)$  (this case)  
 $\Gamma, z_s : \hat{C}, y'_s : A_s; \Delta, x'_L : B_L \vdash_{\Sigma} [z_s/y_s] Q_{y'_s} :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\Gamma, z_s : \hat{C}; \Delta, x'_L : (\exists A_s. B_L) \vdash_{\Sigma} y'_s \leftarrow \text{recv } x'_L; [z_s/y_s] Q_{y'_s} :: (x_L : A_L)$  (by (T- $\exists$ L))
- $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} \text{send } x_L y_s; P :: (x_L : (\exists B_s. A_L))$  (this case)  
 $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} P :: (x_L : A_L)$  and  $\hat{B} \leq B_s$  (this case)  
 $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} [z_s/y_s] P :: (x_L : A_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*   
 $\hat{C} \leq B_s$  (by transitivity of  $\leq$ )  
 $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} \text{send } x_L z_s; [z_s/y_s] P :: (x_L : (\exists B_s. A_L))$  (by (T- $\exists$ R))
- $\Gamma, y_s : \hat{B}, y'_s : \hat{A}; \Delta \vdash_{\Sigma} \text{send } x_L y'_s; P :: (x_L : (\exists A_s. B_L))$  (this case)  
 $\Gamma, y_s : \hat{B}, y'_s : \hat{A}; \Delta \vdash_{\Sigma} P :: (x_L : B_L)$  and  $\hat{A} \leq A_s$  (this case)  
 $\Gamma, z_s : \hat{C}, y'_s : \hat{A}; \Delta \vdash_{\Sigma} [z_s/y_s] P :: (x_L : B_L)$  (by I.H.)  
*for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*

- $\Gamma, z_s : \hat{C}, y'_s : \hat{A}; \Delta \vdash_{\Sigma} \text{send } x_L y'_s; [z_s/y_s] P :: (x_L : (\exists A_s.B_L))$  (by (T- $\exists_R$ ))
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : A_L \multimap B_L, y_L : A_L \vdash_{\Sigma} \text{send } x'_L y_L; Q :: (x_L : A_L)$  (this case)
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : B_L \vdash_{\Sigma} Q :: (x_L : A_L)$  (this case)
- $\Gamma, z_s : \hat{C}; \Delta, x'_L : B_L \vdash_{\Sigma} [z_s/y_s] Q :: (x_L : A_L)$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\Gamma, z_s : \hat{C}; \Delta, x'_L : A_L \multimap B_L, y_L : A_L \vdash_{\Sigma} \text{send } x'_L y_L; [z_s/y_s] Q :: (x_L : A_L)$  (by (T- $\multimap_L$ ))
- $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} y_L \leftarrow \text{recv } x_L; P_{y_L} :: (x_L : A_L \multimap B_L)$  (this case)
- $\Gamma, y_s : \hat{B}; \Delta, y_L : A_L \vdash_{\Sigma} P_{y_L} :: (x_L : B_L)$  (this case)
- $\Gamma, z_s : \hat{C}; \Delta, y_L : A_L \vdash_{\Sigma} [z_s/y_s] P_{y_L} :: (x_L : B_L)$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} y_L \leftarrow \text{recv } x_L; [z_s/y_s] P_{y_L} :: (x_L : A_L \multimap B_L)$  (by (T- $\multimap_R$ ))
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : (\Pi B_s.C_L) \vdash_{\Sigma} \text{send } x'_L y_s; Q :: (x_L : A_L)$  (this case)
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : C_L \vdash_{\Sigma} Q :: (x_L : A_L)$  and  $\hat{B} \leq B_s$  (this case)
- $\Gamma, z_s : \hat{C}; \Delta, x'_L : C_L \vdash_{\Sigma} [z_s/y_s] Q :: (x_L : A_L)$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\hat{C} \leq B_s$  (by transitivity of  $\leq$ )
- $\Gamma, z_s : \hat{C}; \Delta, x'_L : (\Pi B_s.C_L) \vdash_{\Sigma} \text{send } x'_L z_s; [z_s/y_s] Q :: (x_L : A_L)$  (by (T- $\Pi_L$ ))
- $\Gamma, y_s : \hat{B}, y'_s : \hat{A}; \Delta, x'_L : (\Pi A_s.B_L) \vdash_{\Sigma} \text{send } x'_L y'_s; Q :: (x_L : A_L)$  (this case)
- $\Gamma, y_s : \hat{B}, y'_s : \hat{A}; \Delta, x'_L : B_L \vdash_{\Sigma} Q :: (x_L : A_L)$  and  $\hat{A} \leq A_s$  (this case)
- $\Gamma, z_s : \hat{C}, y'_s : \hat{A}; \Delta, x'_L : B_L \vdash_{\Sigma} [z_s/y_s] Q :: (x_L : A_L)$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\Gamma, z_s : \hat{C}, y'_s : \hat{A}; \Delta, x'_L : (\Pi A_s.B_L) \vdash_{\Sigma} \text{send } x'_L y'_s; [z_s/y_s] Q :: (x_L : A_L)$  (by (T- $\Pi_L$ ))
- $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} y'_s \leftarrow \text{recv } x_L; P_{y_s} :: (x_L : (\Pi A_s.B_L))$  (this case)
- $\Gamma, y_s : \hat{B}, y'_s : A_s; \Delta \vdash_{\Sigma} P_{y_s} :: (x_L : B_L)$  (this case)
- $\Gamma, z_s : \hat{C}, y'_s : A_s; \Delta \vdash_{\Sigma} [z_s/y_s] P_{y_s} :: (x_L : B_L)$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} y'_s \leftarrow \text{recv } x_L; [z_s/y_s] P_{y_s} :: (x_L : (\Pi A_s.B_L))$  (by (T- $\Pi_R$ ))
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : \oplus \{\overline{l : B_L}\} \vdash_{\Sigma} \text{case } x'_L \text{ of } \overline{l \Rightarrow Q} :: (x_L : A_L)$  (this case)
- $(\forall i) \Gamma, y_s : \hat{B}; \Delta, x'_L : B_{L_i} \vdash_{\Sigma} Q_i :: (x_L : A_L)$  (this case)
- $(\forall i) \Gamma, z_s : \hat{C}; \Delta, x'_L : B_{L_i} \vdash_{\Sigma} [z_s/y_s] Q_i :: (x_L : A_L)$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\Gamma, z_s : \hat{C}; \Delta, x'_L : \oplus \{\overline{l : B_L}\} \vdash_{\Sigma} \text{case } x'_L \text{ of } \overline{l \Rightarrow [z_s/y_s] Q} :: (x_L : A_L)$  (by (T- $\oplus_L$ ))
- $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} x_L.l_h; P :: (x_L : \oplus \{\overline{l : A_L}\})$  (this case)
- $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} P :: (x_L : A_{L_h})$  (this case)
- $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} [z_s/y_s] P :: (x_L : A_{L_h})$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} x_L.l_h; [z_s/y_s] P :: (x_L : \oplus \{\overline{l : A_L}\})$  (by (T- $\oplus_R$ ))
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : \& \{\overline{l : B_L}\} \vdash_{\Sigma} x'_L.l_h; Q :: (x_L : A_L)$  (this case)
- $\Gamma, y_s : \hat{B}; \Delta, x'_L : B_{L_h} \vdash_{\Sigma} Q :: (x_L : A_L)$  (this case)
- $\Gamma, z_s : \hat{C}; \Delta, x'_L : B_{L_h} \vdash_{\Sigma} [z_s/y_s] Q :: (x_L : A_L)$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$*
- $\Gamma, z_s : \hat{C}; \Delta, x'_L : \& \{\overline{l : B_L}\} \vdash_{\Sigma} x'_L.l_h; [z_s/y_s] Q :: (x_L : A_L)$  (by (T- $\&_L$ ))



- $\Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} \text{case } x_L \text{ of } \overline{l} \Rightarrow \overline{P} :: (x_L : \&\{\overline{l} : A_L\})$  (this case)
- $(\forall i) \Gamma, y_s : \hat{B}; \Delta \vdash_{\Sigma} P_i :: (x_L : A_{L_i})$  (this case)
- $(\forall i) \Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} [z_s/y_s] P_i :: (x_L : A_{L_i})$  (by I.H.)
- for some fresh  $z_s : \hat{C}$  such that  $\hat{C} \leq \hat{B}$
- $\Gamma, z_s : \hat{C}; \Delta \vdash_{\Sigma} \text{case } x_L \text{ of } \overline{l} \Rightarrow [z_s/y_s] \overline{P} :: (x_L : \&\{\overline{l} : A_L\})$  ((T-&R))

Lemma 1-4: By contraction.

Lemma 1-5: By  $\alpha$ -equivalence.

Lemma 1-6: By contraction.

Lemma 1-8: By contraction. □

**Lemma 2** (Configuration Substitution). *Writing  $Q\langle a \rangle$  for a process term  $Q$  with an occurrence of a channel  $a$  and given the partial order  $\perp \leq \uparrow_L^s C_L \leq \top$ , for any  $C_L$ , the following substitutions are type-preserving and thus admissible:*

1. If  $\Gamma \vDash_{\Sigma} \text{proc}(c_L, Q\langle a_L \rangle), \Theta :: \Delta$  and  $\Gamma \vDash_{\Sigma} \Theta :: (\Delta, \Delta_1, a_L : A_L)$ , then, for any  $\Theta'$  such that  $\Gamma \vDash_{\Sigma} \Theta' :: (\Delta, \Delta_1, b_L : A_L), \Gamma \vDash_{\Sigma} \text{proc}(c_L, [b_L/a_L] Q\langle a_L \rangle), \Theta' :: \Delta$ .
2. If  $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \Theta :: \Delta$  and  $\hat{B} \leq \hat{A}$ , then  $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} [b_s/a_s] \Theta :: \Delta$ .
3. If  $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \Lambda :: \Gamma_1$  and  $\text{proc}(a_s, -) \notin \Lambda$  and  $\text{unavail}(a_s) \notin \Lambda$  and  $\hat{B} \leq \hat{A}$ , then  $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} [b_s/a_s] \Lambda :: \Gamma_1$ .

*Proof.* We prove each case in turn:

1. By induction on  $\Gamma \vDash_{\Sigma} \text{proc}(c_L, Q\langle a_L \rangle), \Theta :: \Delta$ :
  - $\Gamma \vDash_{\Sigma} \text{proc}(c_L, Q\langle a_L \rangle), \Theta :: \Delta$  and  $\Gamma \vDash_{\Sigma} \Theta :: (\Delta, \Delta_1, a_L : A_L)$  (this case)
  - $(c_s : \hat{D}) \in \Gamma$  and  $\vdash_{\Sigma} (C_L, \hat{D}) \text{ esync}$  (this case)
  - $\Gamma; \Delta_1, a_L : A_L \vdash_{\Sigma} Q\langle a_L \rangle :: (c_L : C_L)$  (this case)
  - $\Gamma; \Delta_1, b_L : A_L \vdash_{\Sigma} [b_L/a_L] Q\langle a_L \rangle :: (c_L : C_L)$  (by Lemma 1-2)
  - $\Gamma \vDash_{\Sigma} \text{proc}(c_L, [b_L/a_L] Q\langle a_L \rangle), \Theta' :: \Delta$  (by (T- $\Omega_2$ ))
  - for some  $\Theta'$  such that  $\Gamma \vDash_{\Sigma} \Theta' :: (\Delta, \Delta_1, b_L : A_L)$
2. By induction on  $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \Theta :: \Delta$ :
  - $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} (\cdot) :: (\cdot)$  and  $\hat{B} \leq \hat{A}$  (this case)
  - $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} (\cdot) :: (\cdot)$  (by Definition 1)
  - $\Gamma, b_s : \hat{B}, c_s : \hat{C} \vDash_{\Sigma} \text{proc}(a_L, P_{a_L}), \Theta :: (\Delta, a_L : A_L)$  and  $\hat{C} \leq \hat{B}$  (this case)
  - $(a_s : \hat{B}) \in \Gamma$  and  $\vdash_{\Sigma} (A_L, \hat{B}) \text{ esync}$  (this case)
  - $\Gamma, b_s : \hat{B}, c_s : \hat{C}; \Delta' \vdash_{\Sigma} P_{a_L} :: (a_L : A_L)$  (this case)
  - $\Gamma, b_s : \hat{B}, c_s : \hat{C} \vDash_{\Sigma} \Theta : \Delta, \Delta'$  (this case)
  - $\Gamma, b_s : \hat{B}, c_s : \hat{C} \vDash_{\Sigma} [c_s/b_s] \Theta : \Delta, \Delta'$  (by I.H.)
  - $\Gamma, c'_s : \hat{C}, c_s : \hat{C}; \Delta' \vdash_{\Sigma} [c'_s/b_s] P_{a_L} :: (a_L : A_L)$  (by Lemma 1-3)
  - for some fresh  $c'_s : \hat{C}$
  - $\Gamma, b_s : \hat{B}, c_s : \hat{C}; \Delta' \vdash_{\Sigma} [c_s/c'_s] ([c'_s/b_s] P_{a_L}) :: (a_L : A_L)$  (by Lemma 1-4 and weakening)
  - $\Gamma, b_s : \hat{B}, c_s : \hat{C} \vDash_{\Sigma} \text{proc}(a_L, [c_s/b_s] P_{a_L}), [c_s/b_s] \Theta :: (\Delta, a_L : A_L)$  (by (T- $\Theta_2$ ))
3. By induction on  $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \Lambda :: \Gamma_1$ :
  - $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} (\cdot) :: (\cdot)$  and  $\text{proc}(a_s, -) \notin (\cdot)$  and  $\text{unavail}(a_s) \notin (\cdot)$  and  $\hat{B} \leq \hat{A}$  (this case)
  - $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} (\cdot) :: (\cdot)$  (by Definition 1)
  - $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \text{proc}(c_s, P_{c_s}) :: (c_s : \uparrow_L^s C_L)$  and  $\hat{B} \leq \hat{A}$  and  $a_s \neq c_s$  (this case)
  - $\vdash_{\Sigma} (\uparrow_L^s C_L, \top) \text{ esync}$  (this case)

- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vdash_{\Sigma} P_{a_s} :: (c_s : \uparrow_L^s C_L)$  (this case)
- $\Gamma, b'_s : \hat{B}, b_s : \hat{B} \vdash_{\Sigma} [b'_s/a_s] P_{a_s} :: (c_s : \uparrow_L^s C_L)$  (by Lemma 1-7)
- for some fresh  $b'_s : \hat{B}$
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vdash_{\Sigma} [b_s/b'_s] ([b'_s/a_s] P_{a_s}) :: (c_s : \uparrow_L^s C_L)$  (by Lemma 1-8 and weakening)
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vdash_{\Sigma} \text{proc}(c_s, [b_s/a_s] P_{a_s}) :: (c_s : \uparrow_L^s C_L)$  (by (T- $\Lambda_2$ ))
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \text{unavail}(c_s) :: (c_s : \hat{C})$  and  $\hat{B} \leq \hat{A}$  and  $a_s \neq c_s$  (this case)
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \text{unavail}(c_s) :: (c_s : \hat{C})$  (by Definition 1)
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \Lambda_1, \Lambda_2 :: \Gamma_1, \Gamma_2, b_s : \hat{B}$  and  $\text{proc}(a_s, -) \notin \Lambda_1, \Lambda_2$  and  $\text{unavail}(a_s) \notin \Lambda_1, \Lambda_2$  (this case)
- for some  $\Lambda_1, \Lambda_2, \Gamma_1,$  and  $\Gamma_2$
- $\hat{B} \leq \hat{A}$  (this case)
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \Lambda_1 :: \Gamma_1, b_s : \hat{B}$  (this case)
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} \Lambda_2 :: \Gamma_2$  (this case)
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} [b_s/a_s] \Lambda_1 :: \Gamma_1, b_s : \hat{B}$  (by I.H.)
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} [b_s/a_s] \Lambda_2 :: \Gamma_2$  (by I.H.)
- $\Gamma, a_s : \hat{A}, b_s : \hat{B} \vDash_{\Sigma} [b_s/a_s] (\Lambda_1, \Lambda_2) :: \Gamma_1, \Gamma_2, b_s : \hat{B}$  ((T- $\Lambda_4$ ))

□

**Lemma 3** (Update of  $\Gamma$ ). *Given the partial order  $\perp \leq \uparrow_L^s C_L \leq \top$ , for any  $C_L$ , the following substitutions are type-preserving and thus admissible:*

1. If  $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \Theta :: \Delta$ , then, for any  $C_L$  such that  $\vdash_{\Sigma} (C_L, \hat{A})$  esync and for any  $\hat{B}$  such that  $\hat{B} \leq \hat{A}$  and  $\vdash_{\Sigma} (C_L, \hat{B})$  esync,  $\Gamma, a_s : \hat{B} \vDash_{\Sigma} \Theta :: \Delta$ .
2. If  $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \Lambda :: \Gamma_1$  with  $\text{proc}(a_s, P) \notin \Lambda$  and  $\text{unavail}(a_s) \notin \Lambda$ , then, for any  $C_L$  such that  $\vdash_{\Sigma} (C_L, \hat{A})$  esync and for any  $\hat{B}$  such that  $\hat{B} \leq \hat{A}$  and  $\vdash_{\Sigma} (C_L, \hat{B})$  esync,  $\Gamma, a_s : \hat{B} \vDash_{\Sigma} \Lambda :: \Gamma_1$ .

*Proof.* We prove each case in turn:

1. By induction on  $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \Theta :: \Delta$ :
  - $\Gamma, a_s : \hat{A} \vDash_{\Sigma} (\cdot) :: (\cdot)$  (this case)
  - $\Gamma, a_s : \hat{B} \vDash_{\Sigma} (\cdot) :: (\cdot)$  (since  $a_s$  does not occur in the configuration)
  - for some  $C_L$  such that  $\vdash_{\Sigma} (C_L, \hat{A})$  esync and for any  $\hat{B}$  such that  $\hat{B} \leq \hat{A}$
  - $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \text{proc}(c_L, P_{c_L}), \Theta :: (\Delta, c_L : D_L)$  (this case)
  - $(c_s : \hat{D}) \in \Gamma$  and  $\vdash_{\Sigma} (D_L, \hat{D})$  esync (this case)
  - $\Gamma, a_s : \hat{A}; \Delta' \vdash_{\Sigma} P_{c_L} :: (c_L : D_L)$  (this case)
  - $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \Theta : \Delta, \Delta'$  (this case)
  - $\Gamma, a_s : \hat{B} \vDash_{\Sigma} \Theta : \Delta, \Delta'$  (by I.H.)
  - for some  $C_L$  such that  $\vdash_{\Sigma} (C_L, \hat{A})$  esync and for any  $\hat{B}$  such that  $\hat{B} \leq \hat{A}$
  - $\Gamma, a'_s : \hat{B}; \Delta' \vdash_{\Sigma} [a'_s/a_s] P_{c_L} :: (c_L : D_L)$  (by Lemma 1-3)
  - for some fresh  $a'_s : \hat{B}$
  - $\Gamma, a_s : \hat{B}; \Delta' \vdash_{\Sigma} [a_s/a'_s] ([a'_s/a_s] P_{c_L}) :: (c_L : D_L)$  (by Lemma 1-4 and weakening)
  - $\Gamma, a_s : \hat{B} \vDash_{\Sigma} \text{proc}(c_L, P_{c_L}), \Theta :: (\Delta, c_L : D_L)$  (by (T- $\Theta_2$ ))
2. By induction on  $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \Lambda :: \Gamma_1$ :
  - $\Gamma, a_s : \hat{A} \vDash_{\Sigma} (\cdot) :: (\cdot)$  and  $\text{proc}(a_s, -) \notin (\cdot)$  and  $\text{unavail}(a_s) \notin (\cdot)$  (this case)
  - $\Gamma, a_s : \hat{B} \vDash_{\Sigma} (\cdot) :: (\cdot)$  (since  $a_s$  does not occur in the configuration)
  - for some  $C_L$  such that  $\vdash_{\Sigma} (C_L, \hat{A})$  esync and for any  $\hat{B}$  such that  $\hat{B} \leq \hat{A}$
  - $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \text{proc}(c_s, P_{c_s}) :: (c_s : \uparrow_L^s D_L)$  and  $a_s \neq c_s$  (this case)
  - $\vdash_{\Sigma} (\uparrow_L^s D_L, \top)$  esync (this case)
  - $\Gamma, a_s : \hat{A} \vdash_{\Sigma} P_{a_s} :: (c_s : \uparrow_L^s D_L)$  (this case)
  - $\Gamma, b'_s : \hat{B} \vdash_{\Sigma} [b'_s/a_s] P_{a_s} :: (c_s : \uparrow_L^s D_L)$  (by Lemma 1-7)
  - for some fresh  $b'_s : \hat{B}$  and  $C_L$  such that  $\vdash_{\Sigma} (C_L, \hat{A})$  esync and  $\hat{B} \leq \hat{A}$
  - $\Gamma, a_s : \hat{B} \vdash_{\Sigma} [a_s/b'_s] ([b'_s/a_s] P_{a_s}) :: (c_s : \uparrow_L^s D_L)$  (by Lemma 1-8 and weakening)
  - $\Gamma, a_s : \hat{B} \vdash_{\Sigma} \text{proc}(c_s, P_{a_s}) :: (c_s : \uparrow_L^s D_L)$  (by (T- $\Lambda_2$ ))

- $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \text{unavail}(c_s) :: (c_s : \hat{C})$  and  $a_s \neq c_s$  (this case)  
 $\Gamma, a_s : \hat{B} \vDash_{\Sigma} \text{unavail}(c_s) :: (c_s : \hat{C})$  (since  $a_s$  does not occur in the configuration)
- $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \Lambda_1, \Lambda_2 :: \Gamma_1, \Gamma_2$  and  $\text{proc}(a_s, -) \notin \Lambda_1, \Lambda_2$  and  $\text{unavail}(a_s) \notin \Lambda_1, \Lambda_2$  (this case)  
for some  $\Lambda_1, \Lambda_2, \Gamma_1,$  and  $\Gamma_2$ 
  - $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \Lambda_1 :: \Gamma_1$  (this case)
  - $\Gamma, a_s : \hat{A} \vDash_{\Sigma} \Lambda_2 :: \Gamma_2$  (this case)
  - $\Gamma, a_s : \hat{B} \vDash_{\Sigma} \Lambda_1 :: \Gamma_1$  (by I.H.)
for some  $C_L$  such that  $\vdash_{\Sigma} (C_L, \hat{A}) \text{ esync}$  and for any  $\hat{B}$  such that  $\hat{B} \leq \hat{A}$ 
  - $\Gamma, a_s : \hat{B} \vDash_{\Sigma} \Lambda_2 :: \Gamma_2$  (by I.H.)
  - $\Gamma, a_s : \hat{B} \vDash_{\Sigma} \Lambda_1, \Lambda_2 :: \Gamma_1, \Gamma_2$  ((T- $\Lambda_4$ ))

□

**Lemma 4** (Equi-Synchronizing and  $\perp$ ). *If  $\vdash_{\Sigma} (C_L, \uparrow_L^s A_L)$  esync and  $\vdash_{\Sigma} (C_L, \uparrow_L^s B_L)$  esync, for any  $C_L, \uparrow_L^s A_L, \uparrow_L^s B_L$  such that  $A_L \neq B_L$ , then  $\vdash_{\Sigma} (C_L, \perp)$  esync.*

*Proof.* For the presentation of the proof, we switch to a set-based formulation of esync to allow for a more natural formulation of the coinductive proof. The assertion  $\vdash_{\Sigma} (D, \hat{D}) \text{ esync}$  then is expressed as  $(D, \hat{D}) \in \text{esync}$ . Given the monotone generating function  $\mathcal{F}$  arising from the rules defined in Figure 16, we obtain that  $\text{esync} \subseteq \mathcal{F}(\text{esync})$ , since esync is  $\mathcal{F}$ -consistent.

Next, we define the set  $\text{esync}'$  as

$$\text{esync}' \triangleq \text{esync} \cup \text{esync}_{\perp}$$

where  $\text{esync}_{\perp}$  is defined as:

$$\text{esync}_{\perp} \triangleq \{(C_L, \perp) \mid \exists A_L, B_L. (C_L, \uparrow_L^s A_L) \in \text{esync} \wedge (C_L, \uparrow_L^s B_L) \in \text{esync} \wedge A_L \neq B_L\}$$

To prove Lemma 4 it suffices to show that  $\text{esync}'$  is  $\mathcal{F}$ -consistent, i.e., that  $\text{esync}' \subseteq \mathcal{F}(\text{esync}')$ . Thus, we have to show the following two cases:

- (i)  $\text{esync} \subseteq \mathcal{F}(\text{esync}')$  and
- (ii)  $\text{esync}_{\perp} \subseteq \mathcal{F}(\text{esync}')$

We first show (i) and then (ii):

- (i)  $\text{esync} \subseteq \text{esync}'$  (by definition of  $\text{esync}'$ )  
 $\mathcal{F}(\text{esync}) \subseteq \mathcal{F}(\text{esync}')$  (by monotonicity of  $\mathcal{F}$ )  
 $\text{esync} \subseteq \mathcal{F}(\text{esync})$  (since  $\text{esync}$  is  $\mathcal{F}$ -consistent)  
 $\text{esync} \subseteq \mathcal{F}(\text{esync}')$  (by transitivity of  $\subseteq$ )

(ii) We consider each syntactic form of  $C_L$  in turn:

- $(\oplus\{\bar{l} : C'_L\}, \perp) \in \text{esync}_{\perp}$  (this case)  
 $(\oplus\{\bar{l} : C'_L\}, \uparrow_L^s A_L) \in \text{esync}$  and  $(\oplus\{\bar{l} : C'_L\}, \uparrow_L^s B_L) \in \text{esync}$  (this case)  
for some  $A_L$  and  $B_L$  such that  $A_L \neq B_L$   
 $(\forall i) (C'_{L_i}, \uparrow_L^s A_L) \in \text{esync}$  and  $(\forall i) (C'_{L_i}, \uparrow_L^s B_L) \in \text{esync}$  (by inversion on (T-ESYNC $_{\oplus}$ ))  
 $(\forall i) (C'_{L_i}, \perp) \in \text{esync}_{\perp}$  (by definition of  $\text{esync}_{\perp}$ )  
 $(\forall i) (C'_{L_i}, \perp) \in \text{esync}'$  (since  $\text{esync}_{\perp} \subseteq \text{esync}'$ )  
 $(\oplus\{\bar{l} : C'_L\}, \perp) \in \mathcal{F}(\text{esync}')$  (by (T-ESYNC $_{\oplus}$ ))
- $(\&\{\bar{l} : C'_L\}, \perp) \in \text{esync}_{\perp}$  (this case)  
 $(\&\{\bar{l} : C'_L\}, \uparrow_L^s A_L) \in \text{esync}$  and  $(\&\{\bar{l} : C'_L\}, \uparrow_L^s B_L) \in \text{esync}$  (this case)  
for some  $A_L$  and  $B_L$  such that  $A_L \neq B_L$   
 $(\forall i) (C'_{L_i}, \uparrow_L^s A_L) \in \text{esync}$  and  $(\forall i) (C'_{L_i}, \uparrow_L^s B_L) \in \text{esync}$  (by inversion on (T-ESYNC $_{\&}$ ))  
 $(\forall i) (C'_{L_i}, \perp) \in \text{esync}_{\perp}$  (by definition of  $\text{esync}_{\perp}$ )  
 $(\forall i) (C'_{L_i}, \perp) \in \text{esync}'$  (since  $\text{esync}_{\perp} \subseteq \text{esync}'$ )  
 $(\&\{\bar{l} : C'_L\}, \perp) \in \mathcal{F}(\text{esync}')$  (by (T-ESYNC $_{\&}$ ))

- $(C'_l \otimes C''_l, \perp) \in \text{esync}_\perp$  (this case)  
 $(C'_l \otimes C''_l, \uparrow^s A_l) \in \text{esync}$  and  $(C'_l \otimes C''_l, \uparrow^s B_l) \in \text{esync}$  (this case)  
*for some  $A_l$  and  $B_l$  such that  $A_l \neq B_l$*   
 $(C''_l, \uparrow^s A_l) \in \text{esync}$  and  $(C''_l, \uparrow^s B_l) \in \text{esync}$  (by inversion on (T-ESYNC $_\otimes$ ))  
 $(C''_l, \perp) \in \text{esync}_\perp$  (by definition of  $\text{esync}_\perp$ )  
 $(C''_l, \perp) \in \text{esync}'$  (since  $\text{esync}_\perp \subseteq \text{esync}'$ )  
 $(C'_l \otimes C''_l, \perp) \in \mathcal{F}(\text{esync}')$  (by (T-ESYNC $_\otimes$ ))
- $(C'_l \multimap C''_l, \perp) \in \text{esync}_\perp$  (this case)  
 $(C'_l \multimap C''_l, \uparrow^s A_l) \in \text{esync}$  and  $(C'_l \multimap C''_l, \uparrow^s B_l) \in \text{esync}$  (this case)  
*for some  $A_l$  and  $B_l$  such that  $A_l \neq B_l$*   
 $(C''_l, \uparrow^s A_l) \in \text{esync}$  and  $(C''_l, \uparrow^s B_l) \in \text{esync}$  (by inversion on (T-ESYNC $_{\multimap}$ ))  
 $(C''_l, \perp) \in \text{esync}_\perp$  (by definition of  $\text{esync}_\perp$ )  
 $(C''_l, \perp) \in \text{esync}'$  (since  $\text{esync}_\perp \subseteq \text{esync}'$ )  
 $(C'_l \multimap C''_l, \perp) \in \mathcal{F}(\text{esync}')$  (by (T-ESYNC $_{\multimap}$ ))
- $(\exists C_s.C'_l, \perp) \in \text{esync}_\perp$  (this case)  
 $(\exists C_s.C'_l, \uparrow^s A_l) \in \text{esync}$  and  $(\exists C_s.C'_l, \uparrow^s B_l) \in \text{esync}$  (this case)  
*for some  $A_l$  and  $B_l$  such that  $A_l \neq B_l$*   
 $(C'_l, \uparrow^s A_l) \in \text{esync}$  and  $(C'_l, \uparrow^s B_l) \in \text{esync}$  (by inversion on (T-ESYNC $_{\exists}$ ))  
 $(C'_l, \perp) \in \text{esync}_\perp$  (by definition of  $\text{esync}_\perp$ )  
 $(C'_l, \perp) \in \text{esync}'$  (since  $\text{esync}_\perp \subseteq \text{esync}'$ )  
 $(\exists C_s.C'_l, \perp) \in \mathcal{F}(\text{esync}')$  (by (T-ESYNC $_{\exists}$ ))
- $(\Pi C_s.C'_l, \perp) \in \text{esync}_\perp$  (this case)  
 $(\Pi C_s.C'_l, \uparrow^s A_l) \in \text{esync}$  and  $(\Pi C_s.C'_l, \uparrow^s B_l) \in \text{esync}$  (this case)  
*for some  $A_l$  and  $B_l$  such that  $A_l \neq B_l$*   
 $(C'_l, \uparrow^s A_l) \in \text{esync}$  and  $(C'_l, \uparrow^s B_l) \in \text{esync}$  (by inversion on (T-ESYNC $_{\Pi}$ ))  
 $(C'_l, \perp) \in \text{esync}_\perp$  (by definition of  $\text{esync}_\perp$ )  
 $(C'_l, \perp) \in \text{esync}'$  (since  $\text{esync}_\perp \subseteq \text{esync}'$ )  
 $(\Pi C_s.C'_l, \perp) \in \mathcal{F}(\text{esync}')$  (by (T-ESYNC $_{\Pi}$ ))
- $(\mathbf{1}, \perp) \in \text{esync}_\perp$  (this case)  
 Nothing to show.
- $(\downarrow^s C_s, \perp) \in \text{esync}_\perp$  (this case)  
 $(\downarrow^s C_s, \uparrow^s A_l) \in \text{esync}$  and  $(\downarrow^s C_s, \uparrow^s B_l) \in \text{esync}$  (this case)  
*for some  $A_l$  and  $B_l$  such that  $A_l \neq B_l$*   
 Contradiction. (since  $A_l = B_l$  or  $A_l = B_l = \top$  according to (T-ESYNC $_{\downarrow^s-1}$ ) or (T-ESYNC $_{\downarrow^s-2}$ ), resp.)

□

**Corollary 1** (Balance between  $\Lambda$  and  $\Theta$ ). *If the configuration  $\Lambda; \Theta$  is well-formed, then, for any  $\text{proc}(a_s, -)$  in  $\Lambda$ , there does not exist a  $\text{proc}(a_l, -)$  in  $\Theta$ .*

*Proof.* By well-formedness of  $\Lambda; \Theta$  (see Section B.3) it follows that  $\text{unavail}(a_s) \notin \Lambda$  and that  $\forall a. \text{proc}(a_l, -) \in \Theta \implies \text{unavail}(a_s) \in \Lambda$ . Then, by the contrapositive, it follows that  $\text{proc}(a_l, -) \notin \Theta$ . □

**Lemma 5** (Permutation of  $\Theta$ ). *Writing  $Q\langle a_l \rangle$  for a process term  $Q$  with an occurrence of a linear channel  $a_l$ , the following permutation is admissible:*

*If  $\Gamma \models_\Sigma \Theta_1, \text{proc}(c_l, Q\langle a_l \rangle), \Theta_2, \text{proc}(a_l, P_{a_l}), \Theta_3 :: \Delta$ , then  $\Gamma \models_\Sigma \Theta_1, \text{proc}(c_l, Q\langle a_l \rangle), \text{proc}(a_l, P_{a_l}), \Theta_2, \Theta_3 :: \Delta$ .*

*Proof.* In the given typing derivation,  $\text{proc}(a_l, P_{a_l})$  provides  $a_l : A_l$  for some  $A_l$  to  $\Theta_1, \text{proc}(c_l, Q\langle a_l \rangle), \Theta_2$ . Well-formedness of a configuration (see Section B.3) guarantees that  $a_l$  is unique within a configuration, and hence there can be no other process in  $\Theta_2$  that provides a service along  $a_l$ . Moreover, channel  $a_l$  cannot be consumed by  $\Theta_2$

because channels are linear and  $a_l$  occurs in  $\text{proc}(c_l, Q\langle a_l \rangle)$ . Any channels used by  $\text{proc}(a_l, P_{a_l})$  will continue to be available if we move the process to the left.  $\square$

**Lemma 6** (Truncate  $\Theta$ ). *If  $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(a_l, P_{a_l}), \Theta_2 :: \Delta$ , then there exists an  $A_l$  and  $\Delta'$  such that  $\Gamma \vDash_{\Sigma} \text{proc}(a_l, P_{a_l}), \Theta_2 :: (\Delta', a_l : A_l)$ .*

*Proof.* By induction on  $\Gamma \vDash_{\Sigma} \Theta :: \Delta$ :

- $\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)$  (this case)  
Holds vacuously.
- $\Gamma \vDash_{\Sigma} \text{proc}(c_l, P_{c_l}), \Theta :: \Delta$  (this case)  
 $\Gamma; \Delta_2 \vdash_{\Sigma} P_{c_l} :: (c : C_l)$  (this case)  
for some  $\Delta_2, \Delta_1$ , and  $C_l$  such that  $\Delta = (\Delta_1, c : C_l)$   
 $\Gamma \vDash_{\Sigma} \Theta :: \Delta_1, \Delta_2$  (this case)
  - (a)  $\Theta_1 = (\cdot)$  and  $\text{proc}(a_l, P_{a_l}), \Theta_2 = \text{proc}(c_l, P_{c_l}), \Theta$  (this subcase)  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, P_{c_l}), \Theta :: (\Delta_1, c : C_l)$
  - (b)  $\Theta_1 = \text{proc}(c_l, P_{c_l}), \Theta'_1$  and  $\text{proc}(a_l, P_{a_l}), \Theta_2 = \text{proc}(a_l, P_{a_l}), \Theta_2$  (this subcase)  
for some  $\Theta'_1$  such that  $\Theta = \Theta'_1, \text{proc}(a_l, P_{a_l}), \Theta_2$   
 $\Gamma \vDash_{\Sigma} \text{proc}(a_l, P_{a_l}), \Theta_2 :: (\Delta_3, a_l : A_l)$  (by I.H.)  
for some  $\Delta_3$  and  $A_l$

$\square$

**Lemma 7** (Linear Process Terms are Stable under Concretization). *If  $\Gamma_1, \Gamma_2; \Delta \vdash_{\Sigma} P :: (a_l : A_l)$  and  $\Gamma'_2 \triangleleft \Gamma_2$ , then  $\Gamma_1, \Gamma'_2; \Delta \vdash_{\Sigma} P :: (a_l : A_l)$ .*

*Proof.* By induction on  $\Gamma'_2 \triangleleft \Gamma_2$ :

- $\Gamma'_2 \triangleleft (\cdot)$  (this case)  
 $\Gamma_1; \Delta \vdash_{\Sigma} P :: (a_l : A_l)$  (by assumption)  
 $\Gamma_1, \Gamma'_2; \Delta \vdash_{\Sigma} P :: (a_l : A_l)$  (by weakening)
- $\Gamma'_3, x_s : \hat{A} \triangleleft \Gamma_3, x_s : \hat{B}$  and  $\Gamma'_3 \triangleleft \Gamma_3$  and  $\hat{A} \leq \hat{B}$  (this case)  
where  $\Gamma_2 = \Gamma_3, x_s : \hat{B}$   
 $\Gamma_1, \Gamma_3, x_s : \hat{B}; \Delta \vdash_{\Sigma} P :: (a_l : A_l)$  (by assumption)  
 $\Gamma_1, \Gamma'_3, x_s : \hat{B}; \Delta \vdash_{\Sigma} P :: (a_l : A_l)$  (by I.H.)  
 $\Gamma_1, \Gamma'_3, x'_s : \hat{A}; \Delta \vdash_{\Sigma} [x'_s/x_s] P :: (a_l : A_l)$  (by Lemma 1-3)  
where  $x'_s$  fresh  
 $\Gamma_1, \Gamma'_3, x_s : \hat{A}; \Delta \vdash_{\Sigma} [x_s/x'_s] ([x'_s/x_s] P) :: (a_l : A_l)$  (by Lemma 1-4 and weakening)

$\square$

**Lemma 8** (Invariant Sub-Configuration in  $\Theta$ ). *If  $\Gamma \vDash_{\Sigma} \Theta_1, \Theta_2 :: \Delta$  and  $\Gamma \vDash_{\Sigma} \Theta_2 :: \Delta'$ , then, for any  $\Gamma'$  and  $\Theta'_2$  such that  $\Gamma' \vDash_{\Sigma} \Theta'_2 :: \Delta'$  and  $\Gamma' \triangleleft \Gamma$ ,  $\Gamma' \vDash_{\Sigma} \Theta_1, \Theta'_2 :: \Delta$ .*

*Proof.* By induction on  $\Gamma \vDash_{\Sigma} \Theta :: \Delta$ :

- $\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)$  (this case)  
Holds vacuously.
- $\Gamma \vDash_{\Sigma} \text{proc}(c_l, P_{c_l}), \Theta :: \Delta$  (this case)  
 $\Gamma; \Delta_2 \vdash_{\Sigma} P_{c_l} :: (c : C_l)$  and  $(c_s : \hat{B}) \in \Gamma$  and  $\vdash_{\Sigma} (C_l, \hat{B}) \text{ esync}$  (this case)  
for some  $\Delta_2, \Delta_1, C_l$ , and  $\hat{B}$  such that  $\Delta = (\Delta_1, c : C_l)$   
 $\Gamma \vDash_{\Sigma} \Theta :: \Delta_1, \Delta_2$  (this case)

- (a)  $\Theta_1 = (\cdot)$  and  $\Theta_2 = \text{proc}(c_l, P_{c_l}), \Theta$  and  $\Gamma \vDash_{\Sigma} \text{proc}(c_l, P_{c_l}), \Theta :: \Delta$  (this subcase)  
 $\Gamma' \triangleleft \Gamma$  (by assumption)  
 $\Gamma' \vDash_{\Sigma} \Theta'_2 :: \Delta$  (by assumption)
- (b)  $\Theta_1 = \text{proc}(c_l, P_{c_l}), \Theta'_1$  and  $\Gamma \vDash_{\Sigma} \Theta_2 :: \Delta'$  (this subcase)  
*for some  $\Theta'_1$  such that  $\Theta = \Theta'_1, \Theta_2$*   
 $\Gamma' \vDash_{\Sigma} \Theta_2 :: \Delta'$  (by assumption)  
 $\Gamma' \triangleleft \Gamma$  (by assumption)  
 $\Gamma' \vDash_{\Sigma} \Theta'_1, \Theta'_2 :: \Delta_1, \Delta_2$  (by I.H.)  
 $\Gamma'; \Delta_2 \vdash_{\Sigma} P_{c_l} :: (c : C_l)$  (by Lemma 7)  
 $\Gamma' \vDash_{\Sigma} \text{proc}(c_l, P_{c_l}), \Theta'_1, \Theta'_2 :: \Delta$  (by (T- $\Theta_2$ ))

□

**Lemma 9** (Existence of Process for Offered Linear Channel). *If  $\Gamma \vDash_{\Sigma} \Theta :: \Delta$ , then, for all  $a_l \in \text{dom}(\Delta)$ , there exists exactly one  $\text{proc}(a_l, P_{a_l})$ , for some  $P$ , in  $\Theta$ .*

*Proof.* We prove Lemma 9 by induction  $\Gamma \vDash_{\Sigma} \Theta :: \Delta$ :

- $\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)$  (this case)  
 Holds vacuously.
- $\Gamma \vDash_{\Sigma} \text{proc}(a_l, P_{a_l}), \Theta :: (\Delta, a_l : A_l)$  (this case)  
 $\Gamma; \Delta' \vdash_{\Sigma} P_{a_l} :: (a_l : A_l)$  (this case)  
*for some  $\Delta'$*   
 $\Gamma \vDash_{\Sigma} \Theta :: (\Delta, \Delta')$  (this case)  
 For all  $b_l \in \text{dom}(\Delta, \Delta')$ , there exists exactly one  $\text{proc}(b_l, Q_{b_l})$ , for some  $Q$ , in  $\Theta$ . (by I.H.)  
 There exists exactly one  $\text{proc}(a_l, P_{a_l})$  in  $\text{proc}(a_l, P_{a_l}), \Theta$  that offers along  $a_l$ .  
 (by well-formedness of configuration)

□

**Lemma 10** (Existence of Provider). *Writing  $Q\langle a_m \rangle$  for a process term  $Q$  with an occurrence of a channel  $a_m$ , the following hold:*

1. *If  $\Gamma \vDash_{\Sigma} \Lambda; \text{proc}(c_l, Q\langle a_s \rangle), \Theta_1 :: \Gamma; \Delta$ , then either there exists a  $\text{proc}(a_s, P_{a_s})$  in  $\Lambda$ , for some  $P$ , or there exists an  $\text{unavail}(a_s)$  in  $\Lambda$ .*
2. *If  $\Gamma \vDash_{\Sigma} \Lambda; \text{proc}(c_l, Q\langle a_l \rangle), \Theta_1 :: \Gamma; \Delta$ , then there exists exactly one  $\text{proc}(a_l, P_{a_l})$ , for some  $P$ , in  $\Theta_1$ .*

*Proof.* We prove each case in turn:

1.  $\Gamma \vDash_{\Sigma} \Lambda; \text{proc}(c_l, Q\langle a_s \rangle), \Theta_1 :: \Gamma; \Delta$  (by assumption)  
 $\Gamma \vDash_{\Sigma} \Lambda :: \Gamma$  and  $\Gamma \vDash_{\Sigma} \text{proc}(c_l, Q\langle a_s \rangle), \Theta_1 :: \Delta$  (by inversion on (T- $\Omega$ ))  
 $(a_s : \hat{A}) \in \Gamma$  (by the meaning of the configuration and process typing judgment)  
*for some  $\hat{A}$*   
 $\Gamma \vDash_{\Sigma} \Lambda_1 :: (a_s : \hat{A})$  and  $\Gamma \vDash_{\Sigma} \Lambda_2 :: \Gamma_2$  (by inversion on (T- $\Lambda_4$ ))  
*for some  $\Lambda_1, \Lambda_2$ , and  $\Gamma_1$  such that  $\Gamma = \Gamma_2, a_s : \hat{A}$*   
 Either  $\Lambda_1 = \text{unavail}(a_s)$  or  $\Lambda_1 = \text{proc}(a_s, P_{a_s})$  (by inversion on (T- $\Lambda_2$ ) and (T- $\Lambda_2$ ))  
*for some  $P$*
2.  $\Gamma \vDash_{\Sigma} \Lambda; \text{proc}(c_l, Q\langle a_l \rangle), \Theta_1 :: \Gamma; \Delta$  (by assumption)  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, Q\langle a_l \rangle), \Theta_1 :: \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma; \Delta', a_l : A_l \vdash_{\Sigma} Q :: (c_l : C_l)$  and  $\Gamma \vDash_{\Sigma} \Theta_1 :: \Delta_1, \Delta', a_l : A_l$   
*for some  $\Delta', \Delta_1, A_l$ , and  $C_l$  such that  $\Delta = \Delta_1, c_l : C_l$*   
 (by inversion on (T- $\Theta_2$ ) and meaning of process typing judgment)  
 There exists exactly one  $\text{proc}(a_l, P_{a_l})$ , for some  $P$ , in  $\Theta_1$  (by Lemma 9)

□

## D.2 Preservation

The preservation theorem expresses that the types of the providing linear channels are maintained along transitions and that new shared channels may be allocated and the types of existing shared channels concretized.

**Theorem 3** (Preservation). *If  $\Gamma \vDash_{\Sigma} \Lambda; \Theta :: \Gamma; \Delta$  and  $\Lambda; \Theta \longrightarrow \Lambda'; \Theta'$ , then  $\Gamma' \vDash_{\Sigma} \Lambda'; \Theta' :: \Gamma'; \Delta$ , for some  $\Lambda'$ ,  $\Theta'$ , and  $\Gamma'$  such that  $\Gamma' \triangleleft \Gamma$ .*

*Proof.* We prove preservation by induction on the dynamics, constructing a derivation of a well-formed and well-typed configuration  $\Gamma' \vDash_{\Sigma} \Lambda''; \Theta'' :: \Gamma'; \Delta$ , where  $\Lambda''$  and  $\Theta''$  are permutations of  $\Lambda'$  and  $\Theta'$ , respectively:

**Case:**

$$\begin{array}{l} \text{proc}(a_{\perp}, \text{fwd } a_{\perp} b_{\perp}) \\ \longrightarrow a_{\perp} = b_{\perp}, a_{\text{S}} = b_{\text{S}} \end{array} \quad (\text{D-ID}_{\perp})$$

$$\begin{array}{l} \Gamma \vDash_{\Sigma} \text{unavail}(a_{\text{S}}), \text{unavail}(b_{\text{S}}), \Lambda_1; \Theta_1, \text{proc}(a_{\perp}, \text{fwd } a_{\perp} b_{\perp}), \Theta_2, \text{proc}(b_{\perp}, P), \Theta_3 :: \Gamma; \Delta \\ \text{for some } \Lambda_1, \Theta_1, \Theta_2, \Theta_3, \text{ and } \text{proc}(b_{\perp}, P) \quad (\text{by assumption and Lemma 10-2}) \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_{\text{S}}), \text{unavail}(b_{\text{S}}), \Lambda_1; \Theta_1, \text{proc}(a_{\perp}, \text{fwd } a_{\perp} b_{\perp}), \Theta_2, \text{proc}(b_{\perp}, P), \Theta_3 :: \Gamma; \Delta \text{ is well-formed} \quad (\text{by I.H.}) \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_{\text{S}}), \text{unavail}(b_{\text{S}}), \Lambda_1; \Theta_1, \text{proc}(a_{\perp}, \text{fwd } a_{\perp} b_{\perp}), \text{proc}(b_{\perp}, P), \Theta_2, \Theta_3 :: \Gamma; \Delta \quad (\text{by Lemma 5}) \\ \text{unavail}(a_{\text{S}}), \text{unavail}(b_{\text{S}}), \Lambda_1; \Theta_1, \text{proc}(a_{\perp}, \text{fwd } a_{\perp} b_{\perp}), \text{proc}(b_{\perp}, P), \Theta_2, \Theta_3 \quad (\text{this case}) \\ \longrightarrow \text{unavail}(a_{\text{S}}), \text{unavail}(b_{\text{S}}), [b_{\text{S}}/a_{\text{S}}] \Lambda_1; [b_{\text{S}}/a_{\text{S}}, b_{\perp}/a_{\perp}] \Theta_1, \text{proc}(b_{\perp}, P), [b_{\text{S}}/a_{\text{S}}] (\Theta_2, \Theta_3) \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_{\text{S}}), \text{unavail}(b_{\text{S}}), \Lambda_1 :: \Gamma \quad (\text{by inversion on (T-}\Omega\text{)}) \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_{\text{S}}) :: \Gamma_1 \text{ and } \Gamma \vDash_{\Sigma} \text{unavail}(b_{\text{S}}) :: \Gamma_2 \text{ and } \Gamma \vDash_{\Sigma} \Lambda_1 :: \Gamma_3 \quad (\text{by inversion on (T-}\Lambda_4\text{)}) \\ \text{for some } \Gamma_1, \Gamma_2, \text{ and } \Gamma_3 \text{ such that } \Gamma = \Gamma_1, \Gamma_2, \Gamma_3 \\ \Gamma_1 = a_{\text{S}} : \hat{A} \text{ and } \Gamma_2 = b_{\text{S}} : \hat{B} \quad (\text{by inversion on (T-}\Lambda_3\text{)}) \\ \text{for some } \hat{A} \text{ and } \hat{B} \\ \Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(a_{\perp}, \text{fwd } a_{\perp} b_{\perp}), \text{proc}(b_{\perp}, P), \Theta_2, \Theta_3 :: \Delta \quad (\text{by inversion on (T-}\Omega\text{)}) \\ \Gamma \vDash_{\Sigma} \text{proc}(a_{\perp}, \text{fwd } a_{\perp} b_{\perp}), \text{proc}(b_{\perp}, P), \Theta_2, \Theta_3 :: (\Delta_1, a_{\perp} : C_{\perp}) \quad (\text{by Lemma 6}) \\ \text{for some } \Delta_1 \text{ and } C_{\perp} \\ \Gamma; \Delta'_1 \vdash_{\Sigma} \text{fwd } a_{\perp} b_{\perp} :: (a_{\perp} : C_{\perp}) \text{ and } \vdash_{\Sigma} (C_{\perp}, \hat{A}) \text{ esync} \quad (\text{by inversion on (T-}\Theta_2\text{)}) \\ \text{for some } \Delta'_1 \text{ and since } (a_{\text{S}} : \hat{A}) \in \Gamma \\ \Delta'_1 = b_{\perp} : C_{\perp} \quad (\text{by inversion on (T-ID}_{\perp}\text{)}) \\ \Gamma \vDash_{\Sigma} \text{proc}(b_{\perp}, P), \Theta_2, \Theta_3 :: (\Delta_1, b_{\perp} : C_{\perp}) \quad (\text{by inversion on (T-}\Theta_2\text{)}) \\ \Gamma; \Delta''_1 \vdash_{\Sigma} P :: (b_{\perp} : C_{\perp}) \text{ and } \vdash_{\Sigma} (C_{\perp}, \hat{B}) \text{ esync} \quad (\text{by inversion on (T-}\Theta_2\text{)}) \\ \text{for some } \Delta''_1 \text{ and since } (b_{\text{S}} : \hat{B}) \in \Gamma \\ \Gamma \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta''_1) \quad (\text{by inversion on (T-}\Theta_2\text{)}) \end{array}$$

Since  $a_{\text{S}} = \hat{A}$  and  $b_{\text{S}} = \hat{B}$  there are 9 combinations of 2 out of  $\perp$ ,  $\top$ , and  $\uparrow_{\perp}^{\text{S}} A_{\perp} / \uparrow_{\perp}^{\text{S}} B_{\perp}$ , for some  $A_{\perp}$  and  $B_{\perp}$ . We consider each case, collating several cases that have the same proof:

**Subcases:**  $\hat{A} \geq \hat{B}$

$$\begin{array}{l} a_{\text{S}} : \quad \perp \quad \text{and} \quad b_{\text{S}} : \quad \perp \\ a_{\text{S}} : \quad \uparrow_{\perp}^{\text{S}} A_{\perp} \quad \text{and} \quad b_{\text{S}} : \quad \perp \\ a_{\text{S}} : \quad \uparrow_{\perp}^{\text{S}} A_{\perp} \quad \text{and} \quad b_{\text{S}} : \quad \uparrow_{\perp}^{\text{S}} B_{\perp} \quad \text{and } A_{\perp} = B_{\perp} \\ a_{\text{S}} : \quad \top \quad \text{and} \quad b_{\text{S}} : \quad \perp \\ a_{\text{S}} : \quad \top \quad \text{and} \quad b_{\text{S}} : \quad \uparrow_{\perp}^{\text{S}} B_{\perp} \\ a_{\text{S}} : \quad \top \quad \text{and} \quad b_{\text{S}} : \quad \top \end{array}$$

$$\begin{array}{l} \Gamma \vDash_{\Sigma} [b_{\text{S}}/a_{\text{S}}] (\Theta_2, \Theta_3) :: (\Delta_1, \Delta''_1) \quad (\text{by Lemma 2-2}) \\ \Gamma \vDash_{\Sigma} \text{proc}(b_{\perp}, P), [b_{\text{S}}/a_{\text{S}}] (\Theta_2, \Theta_3) :: (\Delta_1, b_{\perp} : C_{\perp}) \quad (\text{by (T-}\Theta_2\text{)}) \\ \Gamma \vDash_{\Sigma} [b_{\text{S}}/a_{\text{S}}, b_{\perp}/a_{\perp}] \Theta_1, \text{proc}(b_{\perp}, P), [b_{\text{S}}/a_{\text{S}}] (\Theta_2, \Theta_3) :: \Delta \quad (\text{by Lemma 8 and Lemma 2-1 and 2-2}) \end{array}$$

$\Gamma \vDash_{\Sigma} [b_s/a_s] \Lambda_1 :: \Gamma_3$  (by Lemma 2-3 since  $\text{proc}(a_s, -) \notin \Lambda_1$  and  $\text{unavail}(a_s) \notin \Lambda_1$ )  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s), \text{unavail}(b_s), [b_s/a_s] \Lambda_1 :: \Gamma$  (by (T- $\Lambda_4$ ))  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s), \text{unavail}(b_s), [b_s/a_s] \Lambda_1; [b_s/a_s, b_l/a_l] \Theta_1, \text{proc}(b_l, P), [b_s/a_s] (\Theta_2, \Theta_3) :: \Gamma; \Delta$   
 (by (T- $\Omega$ ) and well-formedness maintained)

**Subcase:**  $a_s : \uparrow_{\perp}^s A_L$  and  $b_s : \uparrow_{\perp}^s B_L$  and  $A_L \neq B_L$

$\vdash_{\Sigma} (C_L, \perp) \text{ esync}$  (by Lemma 4)  
 $\Gamma' \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_1'')$  (by Lemma 3-1 since  $\vdash_{\Sigma} (C_L, \uparrow_{\perp}^s B_L) \text{ esync}$ ,  $\vdash_{\Sigma} (C_L, \perp) \text{ esync}$ , and  $\perp \leq \uparrow_{\perp}^s B_L$ )  
 where  $\Gamma' = [b_s : \perp/b_s : \uparrow_{\perp}^s B_L] \Gamma$   
 $\Gamma' \triangleleft \Gamma$  (by Definition 2 since  $\triangleleft$  is reflexive)  
 $\Gamma' \vDash_{\Sigma} \Lambda_1 :: \Gamma_3$   
 (by Lemma 3-2 since  $\vdash_{\Sigma} (C_L, \uparrow_{\perp}^s B_L) \text{ esync}$ ,  $\vdash_{\Sigma} (C_L, \perp) \text{ esync}$ ,  $\perp \leq \uparrow_{\perp}^s B_L$ ,  $\text{proc}(b_s, -) \notin \Lambda_1$ , and  $\text{unavail}(b_s) \notin \Lambda_1$ )  
 $\Gamma' \vDash_{\Sigma} [b_s/a_s] (\Theta_2, \Theta_3) :: (\Delta_1, \Delta_1'')$  (by Lemma 2-2)  
 $[b'_s : \perp/b_s : \uparrow_{\perp}^s B_L] \Gamma; \Delta_1'' \vdash_{\Sigma} [b'_s/b_s] P :: (b_l : C_L)$  (by Lemma 1-3)  
 where  $b'_s$  fresh  
 $\Gamma'; \Delta_1'' \vdash_{\Sigma} [b_s/b'_s] [b'_s/b_s] P :: (b_l : C_L)$  (by Lemma 1-4 and weakening)  
 $\Gamma' \vDash_{\Sigma} \text{proc}(b_l, P), [b_s/a_s] (\Theta_2, \Theta_3) :: (\Delta_1, b_l : C_L)$  (by (T- $\Theta_2$ ))  
 $\Gamma' \vDash_{\Sigma} [b_s/a_s, b_l/a_l] \Theta_1, \text{proc}(b_l, P), [b_s/a_s] (\Theta_2, \Theta_3) :: \Delta$   
 (by Lemma 8, since  $\Gamma' \triangleleft \Gamma$ , and Lemma 2-1 and 2-2)  
 $\Gamma' \vDash_{\Sigma} [b_s/a_s] \Lambda_1 :: \Gamma_3$  (by Lemma 2-3 since  $\text{proc}(a_s, -) \notin \Lambda_1$  and  $\text{unavail}(a_s) \notin \Lambda_1$ )  
 $\Gamma' \vDash_{\Sigma} \text{unavail}(a_s), \text{unavail}(b_s), [b_s/a_s] \Lambda_1 :: \Gamma'$  (by (T- $\Lambda_4$ ))  
 $\Gamma' \vDash_{\Sigma} \text{unavail}(a_s), \text{unavail}(b_s), [b_s/a_s] \Lambda_1; [b_s/a_s, b_l/a_l] \Theta_1, \text{proc}(b_l, P), [b_s/a_s] (\Theta_2, \Theta_3) :: \Gamma'; \Delta$   
 (by (T- $\Omega$ ) and well-formedness maintained)

**Subcases:**  $\hat{A} \leq \hat{B}$

$a_s : \perp$  and  $b_s : \uparrow_{\perp}^s B_L$   
 $a_s : \perp$  and  $b_s : \top$   
 $a_s : \uparrow_{\perp}^s A_L$  and  $b_s : \top$

$\Gamma' \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_1'')$  (by Lemma 3-1 since  $\vdash_{\Sigma} (C_L, \hat{B}) \text{ esync}$ ,  $\vdash_{\Sigma} (C_L, \hat{A}) \text{ esync}$ , and  $\hat{A} \leq \hat{B}$ )  
 where  $\Gamma' = [b_s : \hat{A}/b_s : \hat{B}] \Gamma$   
 $\Gamma' \triangleleft \Gamma$  (by Definition 2 since  $\triangleleft$  is reflexive)  
 $\Gamma' \vDash_{\Sigma} \Lambda_1 :: \Gamma_3$   
 (by Lemma 3-2 since  $\vdash_{\Sigma} (C_L, \hat{B}) \text{ esync}$ ,  $\vdash_{\Sigma} (C_L, \hat{A}) \text{ esync}$ ,  $\hat{A} \leq \hat{B}$ ,  $\text{proc}(b_s, -) \notin \Lambda_1$ , and  $\text{unavail}(b_s) \notin \Lambda_1$ )  
 $\Gamma' \vDash_{\Sigma} [b_s/a_s] (\Theta_2, \Theta_3) :: (\Delta_1, \Delta_1'')$  (by Lemma 2-2)  
 $[b'_s : \hat{A}/b_s : \hat{B}] \Gamma; \Delta_1'' \vdash_{\Sigma} [b'_s/b_s] P :: (b_l : C_L)$  (by Lemma 1-3)  
 where  $b'_s$  fresh  
 $\Gamma'; \Delta_1'' \vdash_{\Sigma} [b_s/b'_s] [b'_s/b_s] P :: (b_l : C_L)$  (by Lemma 1-4 and weakening)  
 $\Gamma' \vDash_{\Sigma} \text{proc}(b_l, P), [b_s/a_s] (\Theta_2, \Theta_3) :: (\Delta_1, b_l : C_L)$  (by (T- $\Theta_2$ ))  
 $\Gamma' \vDash_{\Sigma} [b_s/a_s, b_l/a_l] \Theta_1, \text{proc}(b_l, P), [b_s/a_s] (\Theta_2, \Theta_3) :: \Delta$  (by Lemma 8, since  $\Gamma' \triangleleft \Gamma$ , and Lemma 2-1 and 2-2)  
 $\Gamma' \vDash_{\Sigma} [b_s/a_s] \Lambda_1 :: \Gamma_3$  (by Lemma 2-3 since  $\text{proc}(a_s, -) \notin \Lambda_1$  and  $\text{unavail}(a_s) \notin \Lambda_1$ )  
 $\Gamma' \vDash_{\Sigma} \text{unavail}(a_s), \text{unavail}(b_s), [b_s/a_s] \Lambda_1 :: \Gamma'$  (by (T- $\Lambda_4$ ))  
 $\Gamma' \vDash_{\Sigma} \text{unavail}(a_s), \text{unavail}(b_s), [b_s/a_s] \Lambda_1; [b_s/a_s, b_l/a_l] \Theta_1, \text{proc}(b_l, P), [b_s/a_s] (\Theta_2, \Theta_3) :: \Gamma'; \Delta$   
 (by (T- $\Omega$ ) and well-formedness maintained)

**Case:**

$\text{proc}(a_s, \text{fwd } a_s b_s)$  (D-ID<sub>5</sub>)  
 $\longrightarrow \text{unavail}(a_s), a_s = b_s$



$\Gamma \vDash_{\Sigma} \text{proc}(a_s, \text{fwd } a_s b_s), \Lambda_1; \Theta :: \Gamma; \Delta$  (by assumption)  
*for some*  $\Lambda_1$   
 $\Gamma \vDash_{\Sigma} \text{proc}(a_s, \text{fwd } a_s b_s), \Lambda_1; \Theta :: \Gamma; \Delta$  is well-formed (by I.H.)  
 $\text{proc}(a_s, \text{fwd } a_s b_s), \Lambda_1; \Theta$  (this case)  
 $\longrightarrow \text{unavail}(a_s), [b_s/a_s] \Lambda_1; [b_s/a_s] \Theta$   
 $\Gamma \vDash_{\Sigma} \text{proc}(a_s, \text{fwd } a_s b_s), \Lambda_1 :: \Gamma$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(a_s, \text{fwd } a_s b_s) :: \Gamma_1$  and  $\Gamma \vDash_{\Sigma} \Lambda_1 :: \Gamma_2$  (by inversion on (T- $\Lambda_4$ ))  
*for some*  $\Gamma_1$  and  $\Gamma_2$  such that  $\Gamma = \Gamma_1, \Gamma_2$   
 $\Gamma_1 = a_s : \uparrow_L^s A_L$  and  $\Gamma \vdash_{\Sigma} \text{fwd } a_s b_s :: (a_s : \uparrow_L^s A_L)$  and  $\vdash_{\Sigma} (\uparrow_L^s A_L, \top)$  esync (by inversion on (T- $\Lambda_2$ ))  
*for some*  $A_L$   
 $(b_s : \hat{B}) \in \Gamma$  and  $\hat{B} \leq \uparrow_L^s A_L$  (by inversion on (T-ID $_5$ ))  
*for some*  $\hat{B}$

**Subcase:**  $b_s : \perp$

$\Gamma \vDash_{\Sigma} [b_s/a_s] \Lambda_1 :: \Gamma_2$  (by Lemma 2-3 since  $\perp \leq \uparrow_L^s A_L$  and  $\text{proc}(a_s, -) \notin \Lambda_1$  and  $\text{unavail}(a_s) \notin \Lambda_1$ )  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s) :: (a_s : \uparrow_L^s A_L)$  (by (T- $\Lambda_3$ ))  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s), [b_s/a_s] \Lambda_1 :: \Gamma$  (by (T- $\Lambda_4$ ))  
 $\Gamma \vDash_{\Sigma} \Theta :: \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} [b_s/a_s] \Theta :: \Delta$  (by Lemma 2-2 since  $\perp \leq \uparrow_L^s A_L$ )  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s), [b_s/a_s] \Lambda_1; [b_s/a_s] \Theta :: \Gamma; \Delta$  (by (T- $\Omega$ ) and well-formedness maintained)

**Subcase:**  $b_s : \uparrow_L^s A_L$

$\Gamma \vDash_{\Sigma} [b_s/a_s] \Lambda_1 :: \Gamma_2$  (by Lemma 2-3 since  $\uparrow_L^s A_L \leq \uparrow_L^s A_L$  and  $\text{proc}(a_s, -) \notin \Lambda_1$  and  $\text{unavail}(a_s) \notin \Lambda_1$ )  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s) :: (a_s : \uparrow_L^s A_L)$  (by (T- $\Lambda_3$ ))  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s), [b_s/a_s] \Lambda_1 :: \Gamma$  (by (T- $\Lambda_4$ ))  
 $\Gamma \vDash_{\Sigma} \Theta :: \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} [b_s/a_s] \Theta :: \Delta$  (by Lemma 2-2 since  $\uparrow_L^s A_L \leq \uparrow_L^s A_L$ )  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s), [b_s/a_s] \Lambda_1; [b_s/a_s] \Theta :: \Gamma; \Delta$  (by (T- $\Omega$ ) and well-formedness maintained)

**Case:**

$\text{proc}(a_L, x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L}), \text{!def}(x'_L : A_L \leftarrow X_L \leftarrow \overline{y_L} : \overline{B_L}, \overline{y_S} : \overline{B_S} = P_{x'_L, \overline{y_L}, \overline{y_S}})$  (D-SPAWN $_{LL}$ )  
 $\longrightarrow \text{proc}(a_L, [b_L/x_L] Q_{x_L}), \text{proc}(b_L, [b_L/x'_L, \overline{c_L}/\overline{y_L}, \overline{c_S}/\overline{y_S}] P_{x'_L, \overline{y_L}, \overline{y_S}}), \text{unavail}(b_S)$  (*b fresh*)

$\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(a_L, x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L}), \Theta_2 :: \Gamma; \Delta$  (by assumption)  
*for some*  $\Theta_1$  and  $\Theta_2$ , and where  $\text{!def}(x'_L : A_L \leftarrow X_L \leftarrow \overline{y_L} : \overline{B_L}, \overline{y_S} : \overline{B_S} = P_{x'_L, \overline{y_L}, \overline{y_S}})$   
 $\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(a_L, x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L}), \Theta_2 :: \Gamma; \Delta$  is well-formed (by I.H.)  
 $\Lambda; \Theta_1, \text{proc}(a_L, x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L}), \Theta_2$  (this case)  
 $\longrightarrow \text{unavail}(b_S), \Lambda; \Theta_1, \text{proc}(a_L, [b_L/x_L] Q_{x_L}), \text{proc}(b_L, [b_L/x'_L, \overline{c_L}/\overline{y_L}, \overline{c_S}/\overline{y_S}] P_{x'_L, \overline{y_L}, \overline{y_S}}), \Theta_2$   
 $\Gamma \vDash_{\Sigma} \Lambda :: \Gamma$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(a_L, x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L}), \Theta_2 :: \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(a_L, x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L}), \Theta_2 :: (\Delta_1, a_L : C_L)$  (by Lemma 6)  
*for some*  $\Delta_1$  and  $C_L$   
 $\Gamma; \Delta'_1 \vdash_{\Sigma} x_L \leftarrow X_L \leftarrow \overline{c_L}, \overline{c_S}; Q_{x_L} :: (a_L : C_L)$  (by inversion on (T- $\Theta_2$ ))  
*for some*  $\Delta'_1$   
 $(a_S : \hat{D}) \in \Gamma$  and  $\vdash_{\Sigma} (C_L, \hat{D})$  esync (by inversion on (T- $\Theta_2$ ))

$$\begin{array}{l}
\text{for some } \hat{D} \\
(x'_l : A_l \leftarrow X_l \leftarrow \overline{y_l : B_l}, \overline{y_s : B_s} = P_{x'_l, \overline{y_l, y_s}}) \in \Sigma \\
\quad \text{(by inversion on (T-SPAWN}_{LL}) \text{ and since !def}(x'_l : A_l \leftarrow X_l \leftarrow \overline{y_l : B_l}, \overline{y_s : B_s} = P_{x'_l, \overline{y_l, y_s}})) \\
\Gamma; \Delta_2, x_l : A_l \vdash_{\Sigma} Q_{x_l} :: (a_l : C_l) \quad \text{(by inversion on (T-SPAWN}_{LL}) \\
\quad \text{where } \Delta_2 = \Delta'_1 - \overline{c_l : B_l} \text{ and } \overline{c_s : B_s} : \overline{B} \subseteq \Gamma \text{ for some } \overline{B} \text{ such that } \overline{B} \leq \overline{B_s} \\
\Gamma \vDash_{\Sigma} \Theta_2 :: \Delta_1, \Delta'_1 \quad \text{(by inversion on (T-}\Theta_2)) \\
\vdash_{\Sigma} (A_l, \top) \text{ esync} \quad \text{(by inversion on (T-}\Sigma_2)) \\
\overline{y_s : B_s}; \overline{y_l : B_l} \vdash_{\Sigma} P_{x'_l, \overline{y_l, y_s}} :: (x'_l : A_l) \quad \text{(by inversion on (T-}\Sigma_2)) \\
\overline{y_s : B_s}; \overline{c_l : B_l} \vdash_{\Sigma} [b_l/x'_l, \overline{c_l/\overline{y_l}}] P_{x'_l, \overline{y_l, y_s}} :: (b_l : A_l) \quad \text{(by Lemma 1-1 and 1-2)} \\
\overline{c'_s : \overline{B}}; \overline{c_l : B_l} \vdash_{\Sigma} [\overline{c'_s/\overline{y_s}}, b_l/x'_l, \overline{c_l/\overline{y_l}}] P_{x'_l, \overline{y_l, y_s}} :: (b_l : A_l) \quad \text{(by Lemma 1-3)} \\
\quad \text{where } \overline{c'_s} \text{ fresh} \\
\Gamma; \overline{c_l : B_l} \vdash_{\Sigma} [\overline{c_s/\overline{c'_s}}, \overline{c_s/\overline{y_s}}, b_l/x'_l, \overline{c_l/\overline{y_l}}] P_{x'_l, \overline{y_l, y_s}} :: (b_l : A_l) \quad \text{(by Lemma 1-4 and weakening and since } \overline{c_s : B_s} \subseteq \Gamma) \\
\Gamma' \vDash_{\Sigma} \text{proc}(b_l, [b_l/x'_l, \overline{c_l/\overline{y_l}}, \overline{c_s/\overline{y_s}}] P_{x'_l, \overline{y_l, y_s}}), \Theta_2 :: (\Delta_1, \Delta_2, b_l : A_l) \quad \text{(by (T-}\Theta_2) \text{ and weakening)} \\
\quad \text{where } \Gamma' = \Gamma, b_s : \top \\
\Gamma' \triangleleft \Gamma \quad \text{(by Definition 2 since } \triangleleft \text{ is reflexive)} \\
\Gamma'; \Delta_2, b_l : A_l \vdash_{\Sigma} [b_l/x_l] Q_{x_l} :: (a_l : C_l) \quad \text{(by Lemma 1-2 and weakening)} \\
\Gamma' \vDash_{\Sigma} \text{proc}(a_l, [b_l/x_l] Q_{x_l}), \text{proc}(b_l, [b_l/x'_l, \overline{c_l/\overline{y_l}}, \overline{c_s/\overline{y_s}}] P_{x'_l, \overline{y_l, y_s}}), \Theta_2 :: (\Delta_1, a_l : C_l) \quad \text{(by (T-}\Theta_2)) \\
\Gamma' \vDash_{\Sigma} \Theta_1, \text{proc}(a_l, [b_l/x_l] Q_{x_l}), \text{proc}(b_l, [b_l/x'_l, \overline{c_l/\overline{y_l}}, \overline{c_s/\overline{y_s}}] P_{x'_l, \overline{y_l, y_s}}), \Theta_2 :: \Delta \quad \text{(by Lemma 8, since } \Gamma' \triangleleft \Gamma) \\
\Gamma' \vDash_{\Sigma} \text{unavail}(b_s) :: (b_s : \top) \quad \text{(by (T-}\Lambda_3)) \\
\Gamma' \vDash_{\Sigma} \text{unavail}(b_s), \Lambda :: \Gamma' \quad \text{(by (T-}\Lambda_4) \text{ and weakening)} \\
\Gamma' \vDash_{\Sigma} \text{unavail}(b_s), \Lambda; \Theta_1, \text{proc}(a_l, [b_l/x_l] Q_{x_l}), \text{proc}(b_l, [b_l/x'_l, \overline{c_l/\overline{y_l}}, \overline{c_s/\overline{y_s}}] P_{x'_l, \overline{y_l, y_s}}), \Theta_2 :: \Gamma'; \Delta \\
\quad \text{(by (T-}\Omega) \text{ and well-formedness maintained)}
\end{array}$$

**Case:**

$$\begin{array}{l}
\text{proc}(a_l, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \text{!def}(x'_s : A_s \leftarrow X_s \leftarrow \overline{y_s : B_s} = P_{x'_s, \overline{y_s}}) \quad \text{(D-SPAWN}_{LS}) \\
\longrightarrow \text{proc}(a_l, [b_s/x'_s] Q_{x_s}), \text{proc}(b_s, [b_s/x'_s, \overline{c_s/\overline{y_s}}] P_{x'_s, \overline{y_s}}) \quad (b \text{ fresh}) \\
\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(a_l, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Theta_2 :: \Gamma; \Delta \quad \text{(by assumption)} \\
\quad \text{for some } \Theta_1 \text{ and } \Theta_2 \text{ and where !def}(x'_s : A_s \leftarrow X_s \leftarrow \overline{y : B_i} = P_{x'_s, \overline{y_s}}) \\
\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(a_l, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Theta_2 :: \Gamma; \Delta \text{ is well-formed} \quad \text{(by I.H.)} \\
\Lambda; \Theta_1, \text{proc}(a_l, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Theta_2 \\
\longrightarrow \text{proc}(b_s, [b_s/x'_s, \overline{c_s/\overline{y_s}}] P_{x'_s, \overline{y_s}}), \Lambda; \Theta_1, \text{proc}(a_l, [b_s/x_s] Q_{x_s}), \Theta_2 \quad \text{(this case)} \\
\Gamma \vDash_{\Sigma} \Lambda :: \Gamma \quad \text{(by inversion on (T-}\Omega)) \\
\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(a_l, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Theta_2 :: \Delta \quad \text{(by inversion on (T-}\Omega)) \\
\Gamma \vDash_{\Sigma} \text{proc}(a_l, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Theta_2 :: (\Delta_1, a_l : C_l) \quad \text{(by Lemma 6)} \\
\quad \text{for some } \Delta_1 \text{ and } C_l \\
\Gamma; \Delta'_1 \vdash_{\Sigma} x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s} :: (a_l : C_l) \quad \text{(by inversion on (T-}\Theta_2)) \\
\quad \text{for some } \Delta'_1 \\
(a_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_l, \hat{D}) \text{ esync} \quad \text{(by inversion on (T-}\Theta_2)) \\
\quad \text{for some } \hat{D} \\
(x'_s : A_s \leftarrow X_s \leftarrow \overline{y_s : B_s} = P_{x'_s, \overline{y_s}}) \in \Sigma \\
\quad \text{(by inversion on (T-SPAWN}_{LS}) \text{ and since !def}(x'_s : A_s \leftarrow X_s \leftarrow \overline{y_s : B_s} = P_{x'_s, \overline{y_s}})) \\
\Gamma, x_s : A_s; \Delta'_1 \vdash_{\Sigma} Q_{x_s} :: (a_l : C_l) \text{ and } \overline{c_s : B_s} : \overline{B} \subseteq \Gamma \text{ for some } \overline{B} \text{ such that } \overline{B} \leq \overline{B_s} \quad \text{(by inversion on (T-SPAWN}_{LS}) \\
\Gamma \vDash_{\Sigma} \Theta_2 :: \Delta_1, \Delta'_1 \quad \text{(by inversion on (T-}\Theta_2))
\end{array}$$

$$\begin{array}{l}
\vdash_{\Sigma} (A_s, \top) \text{ esync} \quad \text{(by inversion on (T-}\Sigma_2)) \\
\overline{y_s : B_s} \vdash_{\Sigma} P_{x'_s, \overline{y_s}} :: (x'_s : A_s) \quad \text{(by inversion on (T-}\Sigma_2)) \\
\overline{c'_s : \hat{B}}, b_s : A_s \vdash_{\Sigma} [b_s/x'_s, \overline{c'_s}/\overline{y_s}] P_{x'_s, \overline{y_s}} :: (b_s : A_s) \quad \text{(by Lemma 1-5 and 1-7)} \\
\quad \text{where } \overline{c'_s} \text{ fresh} \\
\Gamma, b_s : A_s \vdash_{\Sigma} [\overline{c_s}/\overline{c'_s}] ([b_s/x'_s, \overline{c'_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}) :: (b_s : A_s) \quad \text{(by Lemma 1-8 and weakening and since } \overline{c_s} : \overline{B} \subseteq \Gamma) \\
\Gamma, b_s : A_s; \Delta'_1 \vdash_{\Sigma} [b_s/x_s] Q_{x_s} :: (a_l : C_l) \quad \text{(by Lemma 1-3)} \\
\Gamma' \vDash_{\Sigma} \text{proc}(a_l, [b_s/x_s] Q_{x_s}), \Theta_2 :: (\Delta_1, a_l : C_l) \quad \text{(by (T-}\Theta_2) \text{ and weakening)} \\
\quad \text{where } \Gamma' = \Gamma, b_s : A_s \\
\Gamma' \triangleleft \Gamma \quad \text{(by Definition 2 since } \triangleleft \text{ is reflexive)} \\
\Gamma' \vDash_{\Sigma} \Theta_1, \text{proc}(a_l, [b_s/x_s] Q_{x_s}), \Theta_2 :: \Delta \quad \text{(by Lemma 8, since } \Gamma' \triangleleft \Gamma) \\
\Gamma' \vDash_{\Sigma} \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}) :: (b_s : A_s) \quad \text{(by (T-}\Lambda_2)) \\
\Gamma' \vDash_{\Sigma} \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}), \Lambda :: \Gamma' \quad \text{(by (T-}\Lambda_4) \text{ and weakening)} \\
\Gamma' \vDash_{\Sigma} \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}), \Lambda; \Theta_1, \text{proc}(a_l, [b_s/x_s] Q_{x_s}), \Theta_2 :: \Gamma'; \Delta \quad \text{(by (T-}\Omega) \text{ and well-formedness maintained)}
\end{array}$$

**Case:**

$$\begin{array}{l}
\text{proc}(a_s, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), !\text{def}(x'_s : A_s \leftarrow X_s \leftarrow \overline{y_s : B_s} = P_{x'_s, \overline{y_s}}) \quad \text{(D-SPAWN}_{SS}) \\
\longrightarrow \text{proc}(a_s, [b_s/x_s] Q_{x_s}), \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}) \quad (b \text{ fresh}) \\
\Gamma \vDash_{\Sigma} \text{proc}(a_s, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Lambda_1; \Theta :: \Gamma; \Delta \quad \text{(by assumption)} \\
\quad \text{for some } \Lambda_1 \text{ and where } !\text{def}(x'_s : A_s \leftarrow X_s \leftarrow \overline{y_s : B_s} = P_{x'_s, \overline{y_s}}) \\
\Gamma \vDash_{\Sigma} \text{proc}(a_s, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Lambda_1; \Theta :: \Gamma; \Delta \text{ is well-formed} \quad \text{(by I.H.)} \\
\Gamma \vDash_{\Sigma} \text{proc}(a_s, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Lambda_1; \Theta :: \Gamma; \Delta \\
\longrightarrow \text{proc}(a_s, [b_s/x_s] Q_{x_s}), \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}), \Lambda_1; \Theta \quad \text{(this case)} \\
\Gamma \vDash_{\Sigma} \text{proc}(a_s, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}), \Lambda_1 :: \Gamma \quad \text{(by inversion on (T-}\Omega)) \\
\Gamma \vDash_{\Sigma} \Theta :: \Delta \quad \text{(by inversion on (T-}\Omega)) \\
\Gamma \vDash_{\Sigma} \text{proc}(a_s, x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s}) :: \Gamma_1 \text{ and } \Gamma \vDash_{\Sigma} \Lambda_1 :: \Gamma_2 \quad \text{(by inversion on (T-}\Lambda_4)) \\
\quad \text{for some } \Gamma_1 \text{ and } \Gamma_2 \text{ such that } \Gamma = \Gamma_1, \Gamma_2 \\
\Gamma_1 = a_s : \uparrow_l^s C_l \text{ and } \Gamma \vdash_{\Sigma} x_s \leftarrow X_s \leftarrow \overline{c_s}; Q_{x_s} :: (a_s : \uparrow_l^s C_l) \text{ and } \vdash_{\Sigma} (\uparrow_l^s C_l, \top) \text{ esync} \quad \text{(by inversion on (T-}\Lambda_2)) \\
\quad \text{for some } C_l \\
(x'_s : A_s \leftarrow X_s \leftarrow \overline{y_s : B_s} = P_{x'_s, \overline{y_s}}) \in \Sigma \\
\quad \text{(by inversion on (T-SPAWN}_{SS}) \text{ and since } !\text{def}(x'_s : A_s \leftarrow X_s \leftarrow \overline{y_s : B_s} = P_{x'_s, \overline{y_s}})) \\
\Gamma, x_s : A_s \vdash_{\Sigma} Q_{x_s} :: (a_s : \uparrow_l^s C_l) \text{ and } \overline{c_s} : \overline{B} \subseteq \Gamma \text{ for some } \overline{B} \text{ such that } \overline{B} \leq \overline{B_s} \quad \text{(by inversion on (T-SPAWN}_{SS}) \\
\vdash_{\Sigma} (A_s, \top) \text{ esync} \quad \text{(by inversion on (T-}\Sigma_2)) \\
\overline{y_s : B_s} \vdash_{\Sigma} P_{x'_s, \overline{y_s}} :: (x'_s : A_s) \quad \text{(by inversion on (T-}\Sigma_2)) \\
\overline{c'_s : \hat{B}}, b_s : A_s \vdash_{\Sigma} [b_s/x'_s, \overline{c'_s}/\overline{y_s}] P_{x'_s, \overline{y_s}} :: (b_s : A_s) \quad \text{(by Lemma 1-5 and 1-7)} \\
\quad \text{where } \overline{c'_s} \text{ fresh} \\
\Gamma, b_s : A_s \vdash_{\Sigma} [\overline{c_s}/\overline{c'_s}] ([b_s/x'_s, \overline{c'_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}) :: (b_s : A_s) \quad \text{(by Lemma 1-8 and weakening and since } \overline{c_s} : \overline{B} \subseteq \Gamma) \\
\Gamma, b_s : A_s \vdash_{\Sigma} [b_s/x_s] Q_{x_s} :: (a_s : \uparrow_l^s C_l) \quad \text{(by Lemma 1-7)} \\
\Gamma' \vDash_{\Sigma} \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}) :: (b_s : A_s) \quad \text{(by (T-}\Lambda_2)) \\
\quad \text{where } \Gamma' = \Gamma, b_s : A_s \\
\Gamma' \triangleleft \Gamma \quad \text{(by Definition 2 since } \triangleleft \text{ is reflexive)} \\
\Gamma' \vDash_{\Sigma} \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}), \Lambda_1 :: (\Gamma_2, b_s : A_s) \quad \text{(by (T-}\Lambda_4) \text{ and weakening)} \\
\Gamma' \vDash_{\Sigma} \text{proc}(a_s, [b_s/x_s] Q_{x_s}) :: (a_s : \uparrow_l^s C_l) \quad \text{(by (T-}\Lambda_2))
\end{array}$$

$$\begin{aligned} \Gamma' \vDash_{\Sigma} \text{proc}(a_s, [b_s/x_s] Q_{x_s}), \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}), \Lambda_1 :: \Gamma' & \quad (\text{by (T-}\Lambda_4)) \\ \Gamma' \vDash_{\Sigma} \text{proc}(a_s, [b_s/x_s] Q_{x_s}), \text{proc}(b_s, [b_s/x'_s, \overline{c_s}/\overline{y_s}] P_{x'_s, \overline{y_s}}), \Lambda_1; \Theta :: \Gamma'; \Delta & \\ & \quad (\text{by (T-}\Omega) \text{ and weakening and well-formedness maintained}) \end{aligned}$$

**Case:**

$$\begin{aligned} & \text{proc}(c_l, x_s \leftarrow \text{release } a_l; Q_{x_s}), \text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \text{unavail}(a_s) & \quad (\text{D-}\downarrow_l^s - \text{release/detach}) \\ & \longrightarrow \text{proc}(c_l, [a_s/x_s] Q_{x_s}), \text{proc}(a_s, [a_s/x_s] P_{x_s}) \\ \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_s), \Lambda_1; \Theta_1, \text{proc}(c_l, x_s \leftarrow \text{release } a_l; Q_{x_s}), \Theta_2, \text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \Theta_3 :: \Gamma, \Delta & \\ & \quad \text{for some } \Lambda_1, \Theta_1, \Theta_2, \text{ and } \Theta_3 & \quad (\text{by assumption}) \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_s), \Lambda_1; \Theta_1, \text{proc}(c_l, x_s \leftarrow \text{release } a_l; Q_{x_s}), \Theta_2, \text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \Theta_3 :: \Gamma, \Delta & \text{ is well-formed} \\ & & \quad (\text{by I.H.}) \\ \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_s), \Lambda_1; \Theta_1, \text{proc}(c_l, x_s \leftarrow \text{release } a_l; Q_{x_s}), \text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \Theta_2, \Theta_3 :: \Gamma, \Delta & \\ & & \quad (\text{by Lemma 5}) \\ \\ \text{unavail}(a_s), \Lambda_1; \Theta_1, \text{proc}(c_l, x_s \leftarrow \text{release } a_l; Q_{x_s}), \text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \Theta_2, \Theta_3 & \\ \longrightarrow \text{proc}(a_s, [a_s/x_s] P_{x_s}), \Lambda_1; \Theta_1, \text{proc}(c_l, [a_s/x_s] Q_{x_s}), \Theta_2, \Theta_3 & \quad (\text{this case}) \\ \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_s), \Lambda_1 :: \Gamma & \quad (\text{by inversion on (T-}\Omega)) \\ \Gamma \vDash_{\Sigma} \text{unavail}(a_s) :: \Gamma_1 \text{ and } \Gamma \vDash_{\Sigma} \Lambda_1 :: \Gamma_2 & \quad (\text{by inversion on (T-}\Lambda_4)) \\ & \quad \text{for some } \Gamma_1 \text{ and } \Gamma_2 \text{ such that } \Gamma = \Gamma_1, \Gamma_2 \\ \\ \Gamma_1 = a_s : \hat{B} & \quad (\text{by inversion on (T-}\Lambda_3)) \\ & \quad \text{for some } \hat{B} \\ \\ \Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, x_s \leftarrow \text{release } a_l; Q_{x_s}), \text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \Theta_2, \Theta_3 :: \Delta & \quad (\text{by inversion on (T-}\Omega)) \\ \Gamma \vDash_{\Sigma} \text{proc}(c_l, x_s \leftarrow \text{release } a_l; Q_{x_s}), \text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_l) & \quad (\text{by Lemma 6}) \\ & \quad \text{for some } \Delta_1 \text{ and } C_l \\ \\ \Gamma; \Delta'_1 \vdash_{\Sigma} x_s \leftarrow \text{release } a_l; Q_{x_s} :: (c_l : C_l) \text{ and } (c_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_l, \hat{D}) \text{ esync} & \quad (\text{by inversion on (T-}\Theta_2)) \\ & \quad \text{for some } \Delta'_1 \text{ and } \hat{D} \\ \\ \Delta'_1 = \Delta_2, a_l : \downarrow_l^s A_s \text{ and } \Gamma, x_s : A_s; \Delta_2 \vdash_{\Sigma} Q_{x_s} :: (c_l : C_l) & \quad (\text{by inversion on (T-}\downarrow_l^s)) \\ & \quad \text{for some } \Delta_2 \text{ and } A_s \\ \\ \Gamma \vDash_{\Sigma} \text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_l : \downarrow_l^s A_s) & \quad (\text{by inversion on (T-}\Theta_2)) \\ \\ \Gamma; \Delta''_1 \vdash_{\Sigma} x_s \leftarrow \text{detach } a_l; P_{x_s} :: (a_l : \downarrow_l^s A_s) \text{ and } \vdash_{\Sigma} (\downarrow_l^s A_s, \hat{B}) \text{ esync} & \quad (\text{by inversion on (T-}\Theta_2) \text{ and since } (a_s : \hat{B}) \in \Gamma) \\ & \quad \text{for some } \Delta''_1 \\ \\ \Delta''_1 = \cdot \text{ and } \Gamma \vdash_{\Sigma} P_{x_s} :: (x_s : A_s) & \quad (\text{by inversion on (T-}\downarrow_l^s \text{R)}) \\ \\ \Gamma \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2) & \quad (\text{by inversion on (T-}\Theta_2)) \\ \\ \text{Either } \hat{B} = \uparrow_l^s B_l, \text{ for some } B_l, \text{ or } \hat{B} = \top & \\ & \quad (\text{because } \vdash_{\Sigma} (\downarrow_l^s A_s, \perp) \text{ esync is impossible according to the rules in Figure 16}) \end{aligned}$$

**Subcase:**  $\hat{B} = \uparrow_l^s B_l$ , for some  $B_l$

Then,  $\vdash_{\Sigma} (\downarrow_l^s A_s, \uparrow_l^s B_l) \text{ esync}$ . Consequently, the type  $A_s$  of the continuation  $P_{x_s}$  must be  $\uparrow_l^s B_l$ .

$$\begin{aligned} A_s &= \uparrow_l^s B_l & \quad (\text{by } \vdash_{\Sigma} (\downarrow_l^s A_s, \hat{B}) \text{ esync}) \\ \vdash_{\Sigma} (\uparrow_l^s B_l, \top) \text{ esync} & & \quad (\text{by inversion on (T-ESYNC}_{\downarrow_l^s} \text{-1) since } \vdash_{\Sigma} (\downarrow_l^s \uparrow_l^s B_l, \uparrow_l^s B_l) \text{ esync}) \\ \Gamma, a'_s : \uparrow_l^s B_l \vdash_{\Sigma} [a'_s/x_s] P_{x_s} :: (a'_s : \uparrow_l^s B_l) & & \quad (\text{by Lemma 1-5}) \\ & \quad \text{where } a'_s \text{ fresh} \\ \\ \Gamma \vdash_{\Sigma} [a_s/a'_s] ([a'_s/x_s] P_{x_s}) :: (a_s : \uparrow_l^s B_l) & & \quad (\text{by Lemma 1-6 since } (a_s : \uparrow_l^s B_l) \in \Gamma) \\ \Gamma \vDash_{\Sigma} \text{proc}(a_s, [a_s/x_s] P_{x_s}) :: (a_s : \uparrow_l^s B_l) & & \quad (\text{by (T-}\Lambda_2)) \\ \Gamma \vDash_{\Sigma} \text{proc}(a_s, [a_s/x_s] P_{x_s}), \Lambda_1 :: \Gamma & & \quad (\text{by (T-}\Lambda_4) \text{ and since } \text{proc}(a_s, \cdot) \notin \Lambda_1 \text{ by well-formedness of } \Lambda) \end{aligned}$$

$$\begin{aligned}
& \Gamma, a'_s : \uparrow^s B_L; \Delta_2 \vdash_{\Sigma} [a'_s/x_s] Q_{x_s} :: (c_l : C_L) && \text{(by Lemma 1-7)} \\
& \text{where } a'_s \text{ fresh} \\
& \Gamma; \Delta_2 \vdash_{\Sigma} [a_s/a'_s] ([a'_s/x_s] Q_{x_s}) :: (c_l : C_L) && \text{(by Lemma 1-8 since } (a_s : \uparrow^s B_L) \in \Gamma) \\
& \Gamma \vDash_{\Sigma} \text{proc}(c_l, [a_s/x_s] Q_{x_s}), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_L) && \text{(by (T-}\Theta_2)) \\
& \Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, [a_s/x_s] Q_{x_s}), \Theta_2, \Theta_3 :: \Delta && \text{(by Lemma 8)} \\
& \Gamma \vDash_{\Sigma} \text{proc}(a_s, [a_s/x_s] P_{x_s}), \Lambda_1; \Theta_1, \text{proc}(c_l, [a_s/x_s] Q_{x_s}), \Theta_2, \Theta_3 :: \Gamma; \Delta && \text{(by (T-}\Omega) \text{ and well-formedness maintained)}
\end{aligned}$$

**Subcase:**  $\hat{B} = \top$

Then,  $\vdash_{\Sigma} (\downarrow^s A_s, \top)$  *esync*. Consequently,  $\Gamma$  must be updated as follows:  $[a_s : A_s/a_s : \top] \Gamma$ .

$$\begin{aligned}
& \vdash_{\Sigma} (\downarrow^s A_s, A_s) \text{ esync} && \text{(by coinduction on rules in Figure 16, rule (T-ESYNC}_{\downarrow^s} \text{-1) in particular)} \\
& \Gamma' \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2) \text{ and } \Gamma' \vDash_{\Sigma} \Lambda_1 :: \Gamma_2 && \text{(by Lemma 3-1 and 3-2 since } \vdash_{\Sigma} (\downarrow^s A_s, \top) \text{ esync and } \vdash_{\Sigma} (\downarrow^s A_s, A_s) \text{ esync)} \\
& \text{where } \Gamma' = [a_s : A_s/a_s : \top] \Gamma && \text{(and } A_s \leq \top \text{ and } \text{proc}(a_s, \_) \notin \Lambda_1 \text{ and } \text{unavail}(a_s) \notin \Lambda_1 \text{ by well-formedness of } \Lambda) \\
& \Gamma' \triangleleft \Gamma && \text{(by Definition 2 since } \triangleleft \text{ is reflexive)} \\
& [a'_s : A_s/a_s : \top] \Gamma \vdash_{\Sigma} [a'_s/a_s] P_{x_s} :: (x_s : A_s) && \text{(by Lemma 1-7)} \\
& \text{where } a'_s \text{ fresh} \\
& \Gamma' \vdash_{\Sigma} [a_s/a'_s] ([a'_s/a_s] P_{x_s}) :: (x_s : A_s) && \text{(by Lemma 1-8 and weakening)} \\
& \Gamma', a''_s : A_s \vdash_{\Sigma} [a''_s/x_s] P_{x_s} :: (a''_s : A_s) && \text{(by Lemma 1-5)} \\
& \text{where } a''_s \text{ fresh} \\
& \Gamma' \vdash_{\Sigma} [a_s/a''_s] ([a''_s/x_s] P_{x_s}) :: (a_s : A_s) && \text{(by Lemma 1-6 since } (a_s : A_s) \in \Gamma') \\
& \vdash_{\Sigma} (A_s, \top) \text{ esync} && \text{(by inversion on (T-ESYNC}_{\downarrow^s} \text{-2) since } \vdash_{\Sigma} (\downarrow^s A_s, \top) \text{ esync)} \\
& \Gamma' \vDash_{\Sigma} \text{proc}(a_s, [a_s/x_s] P_{x_s}) :: (a_s : A_s) && \text{(by (T-}\Lambda_2)) \\
& \Gamma' \vDash_{\Sigma} \text{proc}(a_s, [a_s/x_s] P_{x_s}), \Lambda_1 :: \Gamma' && \text{(by (T-}\Lambda_4) \text{ and since } \text{proc}(a_s, \_) \notin \Lambda_1 \text{ by well-formedness of } \Lambda) \\
& [a'''_s : A_s/a_s : \top] \Gamma, x_s : A_s \vdash_{\Sigma} [a'''_s/a_s] Q_{x_s} :: (c_l : C_L) && \text{(by Lemma 1-7)} \\
& \text{where } a'''_s \text{ fresh} \\
& \Gamma', x_s : A_s \vdash_{\Sigma} [a_s/a'''_s] ([a'''_s/a_s] Q_{x_s}) :: (c_l : C_L) && \text{(by Lemma 1-8 and weakening)} \\
& \Gamma'; \Delta_2 \vdash_{\Sigma} [a_s/x_s] Q_{x_s} :: (c_l : C_L) && \text{(by Lemma 1-8)} \\
& \Gamma' \vDash_{\Sigma} \text{proc}(c_l, [a_s/x_s] Q_{x_s}), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_L) && \text{(by (T-}\Theta_2) \text{ and weakening)} \\
& \Gamma' \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, [a_s/x_s] Q_{x_s}), \Theta_2, \Theta_3 :: \Delta && \text{(by Lemma 8, since } \Gamma' \triangleleft \Gamma) \\
& \Gamma' \vDash_{\Sigma} \text{proc}(a_s, [a_s/x_s] P_{x_s}), \Lambda_1; \Theta_1, \text{proc}(c_l, [a_s/x_s] Q_{x_s}), \Theta_2, \Theta_3 :: \Gamma'; \Delta && \text{(by (T-}\Omega) \text{ and well-formedness maintained)}
\end{aligned}$$

**Case:**

$$\begin{aligned}
& \text{proc}(c_l, x_l \leftarrow \text{acquire } a_s; Q_{x_l}), \text{proc}(a_s, x_l \leftarrow \text{accept } a_s; P_{x_l}) && \text{(D-}\uparrow^s \text{- acquire/accept)} \\
& \longrightarrow \text{proc}(c_l, [a_l/x_l] Q_{x_l}), \text{proc}(a_l, [a_l/x_l] P_{x_l}), \text{unavail}(a_s)
\end{aligned}$$

$$\begin{aligned}
& \Gamma \vDash_{\Sigma} \text{proc}(a_s, x_l \leftarrow \text{accept } a_s; P_{x_l}), \Lambda_1; \Theta_1, \text{proc}(c_l, x_l \leftarrow \text{acquire } a_s; Q_{x_l}), \Theta_2 :: \Gamma; \Delta && \text{(by assumption)} \\
& \text{for some } \Lambda_1, \Theta_1, \text{ and } \Theta_2 \\
& \Gamma \vDash_{\Sigma} \text{proc}(a_s, x_l \leftarrow \text{accept } a_s; P_{x_l}), \Lambda_1; \Theta_1, \text{proc}(c_l, x_l \leftarrow \text{acquire } a_s; Q_{x_l}), \Theta_2 :: \Gamma; \Delta \text{ is well-formed} && \text{(by I.H.)} \\
& \text{proc}(a_s, x_l \leftarrow \text{accept } a_s; P_{x_l}), \Lambda_1; \Theta_1, \text{proc}(c_l, x_l \leftarrow \text{acquire } a_s; Q_{x_l}), \Theta_2 && \text{(this case)} \\
& \longrightarrow \text{unavail}(a_s), \Lambda_1; \Theta_1, \text{proc}(c_l, [a_l/x_l] Q_{x_l}), \text{proc}(a_l, [a_l/x_l] P_{x_l}), \Theta_2 \\
& \Gamma \vDash_{\Sigma} \text{proc}(a_s, x_l \leftarrow \text{accept } a_s; P_{x_l}), \Lambda_1 :: \Gamma && \text{(by inversion on (T-}\Omega)) \\
& \Gamma \vDash_{\Sigma} \text{proc}(a_s, x_l \leftarrow \text{accept } a_s; P_{x_l}) :: \Gamma_1 \text{ and } \Gamma \vDash_{\Sigma} \Lambda_1 :: \Gamma_2 && \text{(by inversion on (T-}\Lambda_4)) \\
& \text{for some } \Gamma_1 \text{ and } \Gamma_2 \text{ such that } \Gamma = \Gamma_1, \Gamma_2 \\
& \Gamma_1 = a_s : \uparrow^s A_l \text{ and } \Gamma \vdash_{\Sigma} x_l \leftarrow \text{accept } a_s; P_{x_l} :: (a_s : \uparrow^s A_l) \text{ and } \vdash_{\Sigma} (\uparrow^s A_l, \top) \text{ esync} && \text{(by inversion on (T-}\Lambda_2))
\end{aligned}$$

*for some  $A_l$*   
 $\Gamma; \cdot \vdash_{\Sigma} P_{x_l} :: (x_l : A_l)$  (by inversion on (T- $\uparrow_{lR}^s$ ))  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s) :: (a_s : \uparrow_l^s A_l)$  (by (T- $\Lambda_3$ ))  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s), \Lambda_1 :: \Gamma$  (by (T- $\Lambda_4$ ) and since  $\text{unavail}(a_s) \notin \Lambda_1$  by well-formedness of  $\Lambda$ )  
 $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, x_l \leftarrow \text{acquire } a_s; Q_{x_l}), \Theta_2 :: \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, x_l \leftarrow \text{acquire } a_s; Q_{x_l}), \Theta_2 :: (\Delta_1, c_l : C_l)$  (by Lemma 6)  
*for some  $\Delta_1$  and  $C_l$*   
 $\Gamma; \Delta'_1 \vdash_{\Sigma} x_l \leftarrow \text{acquire } a_s; Q_{x_l} :: (c_l : C_l) \text{ and } (c_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_l, \hat{D}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta'_1$  and  $\hat{D}$*   
 $\Gamma \vDash_{\Sigma} \Theta_2 :: (\Delta_1, \Delta'_1)$  (by inversion on (T- $\Theta_2$ ))  
 $\Gamma; \Delta'_1, x_l : A_l \vdash_{\Sigma} Q_{x_l} :: (c_l : C_l)$  (by inversion on (T- $\uparrow_{lL}^s$ ) and since  $(a_s : \uparrow_l^s A_l) \in \Gamma$ )  
 $\vdash_{\Sigma} (A_l, \uparrow_l^s A_l) \text{ esync}$  (by inversion on (T-ESYNC $\uparrow_l^s$ ) since  $\vdash_{\Sigma} (\uparrow_l^s A_l, \top) \text{ esync}$ )  
 $\Gamma; \cdot \vdash_{\Sigma} [a_l/x_l] P_{x_l} :: (a_l : A_l)$  (by Lemma 1-1 and Corollary 1)  
 $\Gamma \vDash_{\Sigma} \text{proc}(a_l, [a_l/x_l] P_{x_l}), \Theta_2 :: (\Delta_1, \Delta'_1, a_l : A_l)$  (by (T- $\Theta_2$ ) and Corollary 1)  
 $\Gamma; \Delta'_1, a_l : A_l \vdash_{\Sigma} [a_l/x_l] Q_{x_l} :: (c_l : C_l)$  (by Lemma 1-2 and Corollary 1)  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, [a_l/x_l] Q_{x_l}), \text{proc}(a_l, [a_l/x_l] P_{x_l}), \Theta_2 :: (\Delta_1, c_l : C_l)$  (by (T- $\Theta_2$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, [a_l/x_l] Q_{x_l}), \text{proc}(a_l, [a_l/x_l] P_{x_l}), \Theta_2 :: \Delta$  (by Lemma 8)  
 $\Gamma \vDash_{\Sigma} \text{unavail}(a_s), \Lambda_1; \Theta_1, \text{proc}(c_l, [a_l/x_l] Q_{x_l}), \text{proc}(a_l, [a_l/x_l] P_{x_l}), \Theta_2 :: \Gamma; \Delta$  (by (T- $\Omega$ ) and well-formedness is maintained)

**Case:**

$$\begin{aligned} & \text{proc}(c_l, \text{wait } a_l; Q), \text{proc}(a_l, \text{close } a_l) && \text{(D-1)} \\ & \longrightarrow \text{proc}(c_l, Q) \end{aligned}$$

$\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, \text{wait } a_l; Q), \Theta_2, \text{proc}(a_l, \text{close } a_l), \Theta_3 :: \Gamma; \Delta$  (by assumption)  
*for some  $\Theta_1, \Theta_2$ , and  $\Theta_3$*   
 $\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, \text{wait } a_l; Q), \Theta_2, \text{proc}(a_l, \text{close } a_l), \Theta_3 :: \Gamma; \Delta$  is well-formed (by I.H.)  
 $\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, \text{wait } a_l; Q), \text{proc}(a_l, \text{close } a_l), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by Lemma 5)  
 $\Lambda; \Theta_1, \text{proc}(c_l, \text{wait } a_l; Q), \text{proc}(a_l, \text{close } a_l), \Theta_2, \Theta_3$   
 $\longrightarrow \Lambda; \Theta_1, \text{proc}(c_l, Q), \Theta_2, \Theta_3$  (this case)  
 $\Gamma \vDash_{\Sigma} \Lambda :: \Gamma$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, \text{wait } a_l; Q), \text{proc}(a_l, \text{close } a_l), \Theta_2, \Theta_3 :: \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, \text{wait } a_l; Q), \text{proc}(a_l, \text{close } a_l), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_l)$  (by Lemma 6)  
*for some  $\Delta_1$  and  $C_l$*   
 $\Gamma; \Delta'_1 \vdash_{\Sigma} \text{wait } a_l; Q :: (c_l : C_l) \text{ and } (c_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_l, \hat{D}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta'_1$  and  $\hat{D}$*   
 $\Gamma; \Delta_2 \vdash_{\Sigma} Q :: (c_l : C_l)$  (by inversion on (T- $\mathbf{1}_L$ ))  
*for some  $\Delta_2$  such that  $\Delta'_1 = \Delta_2, a_l : \mathbf{1}$*   
 $\Gamma \vDash_{\Sigma} \text{proc}(a_l, \text{close } a_l), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_l : \mathbf{1})$  (by inversion on (T- $\Theta_2$ ))  
 $\Gamma; \Delta_3 \vdash_{\Sigma} \text{close } a_l :: (a_l : \mathbf{1}) \text{ and } (a_s : \hat{B}) \in \Gamma \text{ and } \vdash_{\Sigma} (\mathbf{1}, \hat{B}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta_3$  and  $\hat{B}$*   
 $\Delta_3 = (\cdot)$  (by inversion on (T- $\mathbf{1}_R$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2)$  (by inversion on (T- $\Theta_2$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, Q), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_l)$  (by (T- $\Theta_2$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, Q), \Theta_2, \Theta_3 :: \Delta$  (by Lemma 8)  
 $\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, Q), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by (T- $\Omega$ ) and well-formedness is maintained)

**Case:**

$$\begin{aligned} & \text{proc}(c_L, y_L \leftarrow \text{recv } a_L; Q_{y_L}), \text{proc}(a_L, \text{send } a_L b_L; P) && \text{(D-}\otimes\text{)} \\ & \longrightarrow \text{proc}(c_L, [b_L/y_L] Q_{y_L}), \text{proc}(a_L, P) \end{aligned}$$

$$\begin{aligned} & \Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, y_L \leftarrow \text{recv } a_L; Q_{y_L}), \Theta_2, \text{proc}(a_L, \text{send } a_L b_L; P), \Theta_3 :: \Gamma; \Delta && \text{(by assumption)} \\ & \quad \text{for some } \Theta_1, \Theta_2, \text{ and } \Theta_3 \\ & \Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, y_L \leftarrow \text{recv } a_L; Q_{y_L}), \Theta_2, \text{proc}(a_L, \text{send } a_L b_L; P), \Theta_3 :: \Gamma; \Delta \text{ is well-formed} && \text{(by I.H.)} \\ & \Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, y_L \leftarrow \text{recv } a_L; Q_{y_L}), \text{proc}(a_L, \text{send } a_L b_L; P), \Theta_2, \Theta_3 :: \Gamma; \Delta && \text{(by Lemma 5)} \\ & \Lambda; \Theta_1, \text{proc}(c_L, y_L \leftarrow \text{recv } a_L; Q_{y_L}), \text{proc}(a_L, \text{send } a_L b_L; P), \Theta_2, \Theta_3 \\ & \quad \longrightarrow \Lambda; \Theta_1, \text{proc}(c_L, [b_L/y_L] Q_{y_L}), \text{proc}(a_L, P), \Theta_2, \Theta_3 && \text{(this case)} \\ & \Gamma \models_{\Sigma} \Lambda :: \Gamma && \text{(by inversion on (T-}\Omega\text{))} \\ & \Gamma \models_{\Sigma} \Theta_1, \text{proc}(c_L, y_L \leftarrow \text{recv } a_L; Q_{y_L}), \text{proc}(a_L, \text{send } a_L b_L; P), \Theta_2, \Theta_3 :: \Delta && \text{(by inversion on (T-}\Omega\text{))} \\ & \Gamma \models_{\Sigma} \text{proc}(c_L, y_L \leftarrow \text{recv } a_L; Q_{y_L}), \text{proc}(a_L, \text{send } a_L b_L; P), \Theta_2, \Theta_3 :: (\Delta_1, c_L : C_L) && \text{(by Lemma 6)} \\ & \quad \text{for some } \Delta_1 \text{ and } C_L \\ & \Gamma; \Delta'_1 \vdash_{\Sigma} y_L \leftarrow \text{recv } a_L; Q_{y_L} :: (c_L : C_L) \text{ and } (c_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_L, \hat{D}) \text{ esync} && \text{(by inversion on (T-}\Theta_2\text{))} \\ & \quad \text{for some } \Delta'_1 \text{ and } \hat{D} \\ & \Gamma; \Delta_2, a_L : B_L, y_L : A_L \vdash_{\Sigma} Q_{y_L} :: (c_L : C_L) && \text{(by inversion on (T-}\otimes_L\text{))} \\ & \quad \text{for some } \Delta_2, A_L, \text{ and } B_L \text{ such that } \Delta'_1 = \Delta_2, a_L : A_L \otimes B_L \\ & \Gamma \models_{\Sigma} \text{proc}(a_L, \text{send } a_L b_L; P), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_L : A_L \otimes B_L) && \text{(by inversion on (T-}\Theta_2\text{))} \\ & \Gamma; \Delta_3 \vdash_{\Sigma} \text{send } a_L b_L; P :: (a_L : A_L \otimes B_L) \text{ and } (a_s : \hat{A}) \in \Gamma \text{ and } \vdash_{\Sigma} (A_L \otimes B_L, \hat{A}) \text{ esync} && \text{(by inversion on (T-}\Theta_2\text{))} \\ & \quad \text{for some } \Delta_3 \text{ and } \hat{A} \\ & \Gamma; \Delta_4 \vdash_{\Sigma} P :: (a_L : B_L) && \text{(by inversion on (T-}\otimes_R\text{))} \\ & \quad \text{for some } \Delta_4 \text{ such that } \Delta_3 = \Delta_4, b_L : A_L \\ & \Gamma \models_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, \Delta_4, b_L : A_L) && \text{(by inversion on (T-}\Theta_2\text{))} \\ & \vdash_{\Sigma} (B_L, \hat{A}) \text{ esync} && \text{(by inversion on (T-ESYNC}_{\otimes}\text{))} \\ & \Gamma \models_{\Sigma} \text{proc}(a_L, P), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, b_L : A_L, a_L : B_L) && \text{(by (T-}\Theta_2\text{))} \\ & \Gamma; \Delta_2, a_L : B_L, b_L : A_L \vdash_{\Sigma} [b_L/y_L] Q_{y_L} :: (c_L : C_L) && \text{(by Lemma 1-2)} \\ & \Gamma \models_{\Sigma} \text{proc}(c_L, [b_L/y_L] Q_{y_L}), \text{proc}(a_L, P), \Theta_2, \Theta_3 :: (\Delta_1, c_L : C_L) && \text{(by (T-}\Theta_2\text{))} \\ & \Gamma \models_{\Sigma} \Theta_1, \text{proc}(c_L, [b_L/y_L] Q_{y_L}), \text{proc}(a_L, P), \Theta_2, \Theta_3 :: \Delta && \text{(by Lemma 8)} \\ & \Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, [b_L/y_L] Q_{y_L}), \text{proc}(a_L, P), \Theta_2, \Theta_3 :: \Gamma; \Delta && \text{(by (T-}\Omega\text{) and well-formedness is maintained)} \end{aligned}$$

**Case:**

$$\begin{aligned} & \text{proc}(c_L, y_S \leftarrow \text{recv } a_L; Q_{y_S}), \text{proc}(a_L, \text{send } a_L b_S; P) && \text{(D-}\exists\text{)} \\ & \longrightarrow \text{proc}(c_L, [b_S/y_S] Q_{y_S}), \text{proc}(a_L, P) \end{aligned}$$

$$\begin{aligned} & \Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, y_S \leftarrow \text{recv } a_L; Q_{y_S}), \Theta_2, \text{proc}(a_L, \text{send } a_L b_S; P), \Theta_3 :: \Gamma; \Delta && \text{(by assumption)} \\ & \quad \text{for some } \Theta_1, \Theta_2, \text{ and } \Theta_3 \\ & \Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, y_S \leftarrow \text{recv } a_L; Q_{y_S}), \Theta_2, \text{proc}(a_L, \text{send } a_L b_S; P), \Theta_3 :: \Gamma; \Delta \text{ is well-formed} && \text{(by I.H.)} \\ & \Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, y_S \leftarrow \text{recv } a_L; Q_{y_S}), \text{proc}(a_L, \text{send } a_L b_S; P), \Theta_2, \Theta_3 :: \Gamma; \Delta && \text{(by Lemma 5)} \\ & \Lambda; \Theta_1, \text{proc}(c_L, y_S \leftarrow \text{recv } a_L; Q_{y_S}), \text{proc}(a_L, \text{send } a_L b_S; P), \Theta_2, \Theta_3 \\ & \quad \longrightarrow \Lambda; \Theta_1, \text{proc}(c_L, [b_S/y_S] Q_{y_S}), \text{proc}(a_L, P), \Theta_2, \Theta_3 && \text{(this case)} \\ & \Gamma \models_{\Sigma} \Lambda :: \Gamma && \text{(by inversion on (T-}\Omega\text{))} \\ & \Gamma \models_{\Sigma} \Theta_1, \text{proc}(c_L, y_S \leftarrow \text{recv } a_L; Q_{y_S}), \text{proc}(a_L, \text{send } a_L b_S; P), \Theta_2, \Theta_3 :: \Delta && \text{(by inversion on (T-}\Omega\text{))} \\ & \Gamma \models_{\Sigma} \text{proc}(c_L, y_S \leftarrow \text{recv } a_L; Q_{y_S}), \text{proc}(a_L, \text{send } a_L b_S; P), \Theta_2, \Theta_3 :: (\Delta_1, c_L : C_L) && \text{(by Lemma 6)} \end{aligned}$$

*for some  $\Delta_1$  and  $C_L$*   
 $\Gamma; \Delta'_1 \vdash_{\Sigma} y_s \leftarrow \text{recv } a_L; Q_{y_s} :: (c_L : C_L) \text{ and } (c_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_L, \hat{D}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta'_1$  and  $\hat{D}$*   
 $\Gamma, y_s : A_s; \Delta_2, a_L : B_L \vdash_{\Sigma} Q_{y_s} :: (c_L : C_L)$  (by inversion on (T- $\exists_L$ ))  
*for some  $\Delta_2, A_s$ , and  $B_L$  such that  $\Delta'_1 = \Delta_2, a_L : \exists A_s.B_L$*   
 $\Gamma \models_{\Sigma} \text{proc}(a_L, \text{send } a_L b_s; P), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_L : \exists A_s.B_L)$  (by inversion on (T- $\Theta_2$ ))  
 $\Gamma; \Delta_3 \vdash_{\Sigma} \text{send } a_L b_s; P :: (a_L : \exists A_s.B_L) \text{ and } (a_s : \hat{A}) \in \Gamma \text{ and } \vdash_{\Sigma} (\exists A_s.B_L, \hat{A}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta_3$  and  $\hat{A}$*   
 $\Gamma; \Delta_3 \vdash_{\Sigma} P :: (a_L : B_L) \text{ and } (b_s : \hat{B}) \in \Gamma \text{ and } \hat{B} \leq A_s$  (by inversion on (T- $\exists_R$ ))  
*for some  $\hat{B}$*   
 $\Gamma \models_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, \Delta_3)$  (by inversion on (T- $\Theta_2$ ))  
 $\vdash_{\Sigma} (B_L, \hat{A}) \text{ esync}$  (by inversion on (T-ESYNC $\exists$ ))  
 $\Gamma \models_{\Sigma} \text{proc}(a_L, P), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_L : B_L)$  (by (T- $\Theta_2$ ))  
 $\Gamma, b'_s : \hat{B}; \Delta_2, a_L : B_L \vdash_{\Sigma} [b'_s/y_s] Q_{y_s} :: (c_L : C_L)$  (by Lemma 1-3)  
*where  $b'_s$  fresh*  
 $\Gamma; \Delta_2, a_L : B_L \vdash_{\Sigma} [b_s/b'_s] ([b'_s/y_s] Q_{y_s}) :: (c_L : C_L)$  (by Lemma 1-4 since  $(b_s : \hat{B}) \in \Gamma$ )  
 $\Gamma \models_{\Sigma} \text{proc}(c_L, [b_s/y_s] Q_{y_s}), \text{proc}(a_L, P), \Theta_2, \Theta_3 :: (\Delta_1, c_L : C_L)$  (by (T- $\Theta_2$ ))  
 $\Gamma \models_{\Sigma} \Theta_1, \text{proc}(c_L, [b_s/y_s] Q_{y_s}), \text{proc}(a_L, P), \Theta_2, \Theta_3 :: \Delta$  (by Lemma 8)  
 $\Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, [b_s/y_s] Q_{y_s}), \text{proc}(a_L, P), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by (T- $\Omega$ ) and well-formedness is maintained)

**Case:**

$\text{proc}(c_L, \text{send } a_L b_L; Q), \text{proc}(a_L, y_L \leftarrow \text{recv } a_L; P_{y_L})$  (D- $\rightarrow$ )  
 $\rightarrow \text{proc}(c_L, Q), \text{proc}(a_L, [b_L/y_L] P_{y_L})$   
 $\Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, \text{send } a_L b_L; Q), \Theta_2, \text{proc}(a_L, y_L \leftarrow \text{recv } a_L; P_{y_L}), \Theta_3 :: \Gamma; \Delta$  (by assumption)  
*for some  $\Theta_1, \Theta_2$ , and  $\Theta_3$*   
 $\Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, \text{send } a_L b_L; Q), \Theta_2, \text{proc}(a_L, y_L \leftarrow \text{recv } a_L; P_{y_L}), \Theta_3 :: \Gamma; \Delta$  is well-formed (by I.H.)  
 $\Gamma \models_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, \text{send } a_L b_L; Q), \text{proc}(a_L, y_L \leftarrow \text{recv } a_L; P_{y_L}), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by Lemma 5)  
 $\Lambda; \Theta_1, \text{proc}(c_L, \text{send } a_L b_L; Q), \text{proc}(a_L, y_L \leftarrow \text{recv } a_L; P_{y_L}), \Theta_2, \Theta_3$   
 $\rightarrow \Lambda; \Theta_1, \text{proc}(c_L, Q), \text{proc}(a_L, [b_L/y_L] P_{y_L}), \Theta_2, \Theta_3$  (this case)  
 $\Gamma \models_{\Sigma} \Lambda :: \Gamma$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \models_{\Sigma} \Theta_1, \text{proc}(c_L, \text{send } a_L b_L; Q), \text{proc}(a_L, y_L \leftarrow \text{recv } a_L; P_{y_L}), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \models_{\Sigma} \text{proc}(c_L, \text{send } a_L b_L; Q), \text{proc}(a_L, y_L \leftarrow \text{recv } a_L; P_{y_L}), \Theta_2, \Theta_3 :: (\Delta_1, c_L : C_L)$  (by Lemma 6)  
*for some  $\Delta_1$  and  $C_L$*   
 $\Gamma; \Delta'_1 \vdash_{\Sigma} \text{send } a_L b_L; Q :: (c_L : C_L) \text{ and } (c_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_L, \hat{D}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta'_1$  and  $\hat{D}$*   
 $\Gamma; \Delta_2, a_L : B_L \vdash_{\Sigma} Q :: (c_L : C_L)$  (by inversion on (T- $\rightarrow_L$ ))  
*for some  $\Delta_2, A_L$ , and  $B_L$  such that  $\Delta'_1 = \Delta_2, a_L : A_L \multimap B_L, b_L : A_L$*   
 $\Gamma \models_{\Sigma} \text{proc}(a_L, y_L \leftarrow \text{recv } a_L; P_{y_L}), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_L : A_L \multimap B_L, b_L : A_L)$  (by inversion on (T- $\Theta_2$ ))  
 $\Gamma; \Delta_3 \vdash_{\Sigma} y_L \leftarrow \text{recv } a_L; P_{y_L} :: (a_L : A_L \multimap B_L) \text{ and } (a_s : \hat{A}) \in \Gamma \text{ and } \vdash_{\Sigma} (A_L \multimap B_L, \hat{A}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta_3$  and  $\hat{A}$*   
 $\Gamma; \Delta_3, y_L : A_L \vdash_{\Sigma} P_{y_L} :: (a_L : B_L)$  (by inversion on (T- $\rightarrow_R$ ))  
 $\Gamma \models_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, \Delta_3, b_L : A_L)$  (by inversion on (T- $\Theta_2$ ))  
 $\Gamma; \Delta_3, b_L : A_L \vdash_{\Sigma} [b_L/y_L] P_{y_L} :: (a_L : B_L)$  (by Lemma 1-2)  
 $\vdash_{\Sigma} (B_L, \hat{A}) \text{ esync}$  (by inversion on (T-ESYNC $\rightarrow$ ))



$$\begin{aligned}
& \Gamma \vDash_{\Sigma} \text{proc}(a_L, [b_L/y_L]P_{y_L}), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_L : B_L) && \text{(by (T-}\Theta_2\text{))} \\
& \Gamma \vDash_{\Sigma} \text{proc}(c_L, Q), \text{proc}(a_L, [b_L/y_L]P_{y_L}), \Theta_2, \Theta_3 :: (\Delta_1, c_L : C_L) && \text{(by (T-}\Theta_2\text{))} \\
& \Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_L, Q), \text{proc}(a_L, [b_L/y_L]P_{y_L}), \Theta_2, \Theta_3 :: \Delta && \text{(by Lemma 8)} \\
& \Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, Q), \text{proc}(a_L, [b_L/y_L]P_{y_L}), \Theta_2, \Theta_3 :: \Gamma; \Delta && \text{(by (T-}\Omega\text{) and well-formedness is maintained)}
\end{aligned}$$

**Case:**

$$\begin{aligned}
& \text{proc}(c_L, \text{send } a_L b_S; Q), \text{proc}(a_L, y_S \leftarrow \text{recv } a_L; P_{y_S}) && \text{(D-II)} \\
& \longrightarrow \text{proc}(c_L, Q), \text{proc}(a_L, [b_S/y_S]P_{y_S})
\end{aligned}$$

$$\begin{aligned}
& \Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, \text{send } a_L b_S; Q), \Theta_2, \text{proc}(a_L, y_S \leftarrow \text{recv } a_L; P_{y_S}), \Theta_3 :: \Gamma; \Delta && \text{(by assumption)} \\
& \quad \text{for some } \Theta_1, \Theta_2, \text{ and } \Theta_3 \\
& \Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, \text{send } a_L b_S; Q), \Theta_2, \text{proc}(a_L, y_S \leftarrow \text{recv } a_L; P_{y_S}), \Theta_3 :: \Gamma; \Delta \text{ is well-formed} && \text{(by I.H.)} \\
& \Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, \text{send } a_L b_S; Q), \text{proc}(a_L, y_S \leftarrow \text{recv } a_L; P_{y_S}), \Theta_2, \Theta_3 :: \Gamma; \Delta && \text{(by Lemma 5)} \\
& \Lambda; \Theta_1, \text{proc}(c_L, \text{send } a_L b_S; Q), \text{proc}(a_L, y_S \leftarrow \text{recv } a_L; P_{y_S}), \Theta_2, \Theta_3 && \\
& \quad \longrightarrow \Lambda; \Theta_1, \text{proc}(c_L, Q), \text{proc}(a_L, [b_S/y_S]P_{y_S}), \Theta_2, \Theta_3 && \text{(this case)} \\
& \Gamma \vDash_{\Sigma} \Lambda :: \Gamma && \text{(by inversion on (T-}\Omega\text{))} \\
& \Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_L, \text{send } a_L b_S; Q), \text{proc}(a_L, y_S \leftarrow \text{recv } a_L; P_{y_S}), \Theta_2, \Theta_3 :: \Gamma; \Delta && \text{(by inversion on (T-}\Omega\text{))} \\
& \Gamma \vDash_{\Sigma} \text{proc}(c_L, \text{send } a_L b_S; Q), \text{proc}(a_L, y_S \leftarrow \text{recv } a_L; P_{y_S}), \Theta_2, \Theta_3 :: (\Delta_1, c_L : C_L) && \text{(by Lemma 6)} \\
& \quad \text{for some } \Delta_1 \text{ and } C_L \\
& \Gamma; \Delta'_1 \vdash_{\Sigma} \text{send } a_L b_S; Q :: (c_L : C_L) \text{ and } (c_S : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_L, \hat{D}) \text{ esync} && \text{(by inversion on (T-}\Theta_2\text{))} \\
& \quad \text{for some } \Delta'_1 \text{ and } \hat{D} \\
& \Gamma; \Delta_2, a_L : B_L \vdash_{\Sigma} Q :: (c_L : C_L) \text{ and } (b_S : \hat{B}) \in \Gamma \text{ and } \hat{B} \leq A_S && \text{(by inversion on (T-}\Pi_L\text{))} \\
& \quad \text{for some } \Delta_2, \hat{B}, A_S, \text{ and } B_L \text{ such that } \Delta'_1 = \Delta_2, a_L : \Pi A_S.B_L \\
& \Gamma \vDash_{\Sigma} \text{proc}(a_L, y_S \leftarrow \text{recv } a_L; P_{y_S}), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_L : \Pi A_S.B_L) && \text{(by inversion on (T-}\Theta_2\text{))} \\
& \Gamma; \Delta_3 \vdash_{\Sigma} y_S \leftarrow \text{recv } a_L; P_{y_S} :: (a_L : \Pi A_S.B_L) \text{ and } (a_S : \hat{A}) \in \Gamma \text{ and } \vdash_{\Sigma} (\Pi A_S.B_L, \hat{A}) \text{ esync} && \text{(by inversion on (T-}\Theta_2\text{))} \\
& \quad \text{for some } \Delta_3 \text{ and } \hat{A} \\
& \Gamma, y_S : A_S; \Delta_3 \vdash_{\Sigma} P_{y_S} :: (a_L : B_L) && \text{(by inversion on (T-}\Pi_R\text{))} \\
& \Gamma \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, \Delta_3) && \text{(by inversion on (T-}\Theta_2\text{))} \\
& \Gamma, b'_S : \hat{B}; \Delta_3 \vdash_{\Sigma} [b'_S/y_S]P_{y_S} :: (a_L : B_L) && \text{(by Lemma 1-3)} \\
& \quad \text{where } b'_S \text{ fresh} \\
& \Gamma; \Delta_3 \vdash_{\Sigma} [b_S/b'_S]([b'_S/y_S]P_{y_S}) :: (a_L : B_L) && \text{(by Lemma 1-4 since } (b_S : \hat{B}) \in \Gamma\text{)} \\
& \vdash_{\Sigma} (B_L, \hat{A}) \text{ esync} && \text{(by inversion on (T-ESYNC}_{\Pi}\text{))} \\
& \Gamma \vDash_{\Sigma} \text{proc}(a_L, [b_S/y_S]P_{y_S}), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_L : B_L) && \text{(by (T-}\Theta_2\text{))} \\
& \Gamma \vDash_{\Sigma} \text{proc}(c_L, Q), \text{proc}(a_L, [b_S/y_S]P_{y_S}), \Theta_2, \Theta_3 :: (\Delta_1, c_L : C_L) && \text{(by (T-}\Theta_2\text{))} \\
& \Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_L, Q), \text{proc}(a_L, [b_S/y_S]P_{y_S}), \Theta_2, \Theta_3 :: \Delta && \text{(by Lemma 8)} \\
& \Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, Q), \text{proc}(a_L, [b_S/y_S]P_{y_S}), \Theta_2, \Theta_3 :: \Gamma; \Delta && \text{(by (T-}\Omega\text{) and well-formedness is maintained)}
\end{aligned}$$

**Case:**

$$\begin{aligned}
& \text{proc}(c_L, \text{case } a_L \text{ of } \overline{l \Rightarrow Q}), \text{proc}(a_L, a_L.l_h; P) && \text{(D-}\oplus\text{)} \\
& \longrightarrow \text{proc}(c_L, Q_h), \text{proc}(a_L, P)
\end{aligned}$$

$$\begin{aligned}
& \Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, \text{case } a_L \text{ of } \overline{l \Rightarrow Q}), \Theta_2, \text{proc}(a_L, a_L.l_h; P), \Theta_3 :: \Gamma; \Delta && \text{(by assumption)} \\
& \quad \text{for some } \Theta_1, \Theta_2, \text{ and } \Theta_3 \\
& \Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_L, \text{case } a_L \text{ of } \overline{l \Rightarrow Q}), \Theta_2, \text{proc}(a_L, a_L.l_h; P), \Theta_3 :: \Gamma; \Delta \text{ is well-formed} && \text{(by I.H.)}
\end{aligned}$$

$\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, \text{case } a_l \text{ of } \overline{l \Rightarrow Q}), \text{proc}(a_l, a_l.l_h; P), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by Lemma 5)  
 $\Lambda; \Theta_1, \text{proc}(c_l, \text{case } a_l \text{ of } \overline{l \Rightarrow Q}), \text{proc}(a_l, a_l.l_h; P), \Theta_2, \Theta_3$   
 $\longrightarrow \Lambda; \Theta_1, \text{proc}(c_l, Q_h), \text{proc}(a_l, P), \Theta_2, \Theta_3$  (this case)  
 $\Gamma \vDash_{\Sigma} \Lambda :: \Gamma$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, \text{case } a_l \text{ of } \overline{l \Rightarrow Q}), \text{proc}(a_l, a_l.l_h; P), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, \text{case } a_l \text{ of } \overline{l \Rightarrow Q}), \text{proc}(a_l, a_l.l_h; P), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_l)$  (by Lemma 6)  
*for some  $\Delta_1$  and  $C_l$*   
 $\Gamma; \Delta'_1 \vdash_{\Sigma} \text{case } a_l \text{ of } \overline{l \Rightarrow Q} :: (c_l : C_l) \text{ and } (c_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_l, \hat{D}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta'_1$  and  $\hat{D}$*   
 $(\forall i) \Gamma; \Delta_2, a_l : A_{L_i} \vdash_{\Sigma} Q_i :: (c_l : C_l)$  (by inversion on (T- $\oplus_L$ ))  
*for some  $\Delta_2$  and  $\overline{A_l}$  such that  $\Delta'_1 = \Delta_2, a_l : \oplus\{\overline{l : A_l}\}$*   
 $\Gamma \vDash_{\Sigma} \text{proc}(a_l, a_l.l_h; P), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_l : \oplus\{\overline{l : A_l}\})$  (by inversion on (T- $\Theta_2$ ))  
 $\Gamma; \Delta_3 \vdash_{\Sigma} a_l.l_h; P :: (a_l : \oplus\{\overline{l : A_l}\}) \text{ and } (a_s : \hat{A}) \in \Gamma \text{ and } \vdash_{\Sigma} (\oplus\{\overline{l : A_l}\}, \hat{A}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta_3$  and  $\hat{A}$*   
 $\Gamma; \Delta_3 \vdash_{\Sigma} P :: (a_l : A_{L_h})$  (by inversion on (T- $\oplus_R$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, \Delta_3)$  (by inversion on (T- $\Theta_2$ ))  
 $(\forall i) \vdash_{\Sigma} (A_{L_i}, \hat{A}) \text{ esync}$  (by inversion on (T-ESYNC $_{\oplus}$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(a_l, P), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_l : A_{L_h})$  (by (T- $\Theta_2$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, Q_h), \text{proc}(a_l, P), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_l)$  (by (T- $\Theta_2$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, Q_h), \text{proc}(a_l, P), \Theta_2, \Theta_3 :: \Delta$  (by Lemma 8)  
 $\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, Q_h), \text{proc}(a_l, P), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by (T- $\Omega$ ) and well-formedness is maintained)

**Case:**

$\text{proc}(c_l, a_l.l_h; Q), \text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P})$  (D- $\&$ )  
 $\longrightarrow \text{proc}(c_l, Q), \text{proc}(a_l, P_h)$

$\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, a_l.l_h; Q), \Theta_2, \text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P}), \Theta_3 :: \Gamma; \Delta$  (by assumption)  
*for some  $\Theta_1, \Theta_2$ , and  $\Theta_3$*   
 $\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, a_l.l_h; Q), \Theta_2, \text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P}), \Theta_3 :: \Gamma; \Delta$  is well-formed (by I.H.)  
 $\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, a_l.l_h; Q), \text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P}), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by Lemma 5)  
 $\Lambda; \Theta_1, \text{proc}(c_l, a_l.l_h; Q), \text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P}), \Theta_2, \Theta_3$   
 $\longrightarrow \Lambda; \Theta_1, \text{proc}(c_l, Q), \text{proc}(a_l, P_h), \Theta_2, \Theta_3$  (this case)  
 $\Gamma \vDash_{\Sigma} \Lambda :: \Gamma$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, a_l.l_h; Q), \text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P}), \Theta_2, \Theta_3 :: \Gamma; \Delta$  (by inversion on (T- $\Omega$ ))  
 $\Gamma \vDash_{\Sigma} \text{proc}(c_l, a_l.l_h; Q), \text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P}), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_l)$  (by Lemma 6)  
*for some  $\Delta_1$  and  $C_l$*   
 $\Gamma; \Delta'_1 \vdash_{\Sigma} a_l.l_h; Q :: (c_l : C_l) \text{ and } (c_s : \hat{D}) \in \Gamma \text{ and } \vdash_{\Sigma} (C_l, \hat{D}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta'_1$  and  $\hat{D}$*   
 $\Gamma; \Delta_2, a_l : A_{L_h} \vdash_{\Sigma} Q :: (c_l : C_l)$  (by inversion on (T- $\&_L$ ))  
*for some  $\Delta_2$  and  $\overline{A_l}$  such that  $\Delta'_1 = \Delta_2, a_l : \&\{\overline{l : A_l}\}$*   
 $\Gamma \vDash_{\Sigma} \text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P}), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_l : \&\{\overline{l : A_l}\})$  (by inversion on (T- $\Theta_2$ ))  
 $\Gamma; \Delta_3 \vdash_{\Sigma} \text{case } a_l \text{ of } \overline{l \Rightarrow P} :: (a_l : \&\{\overline{l : A_l}\}) \text{ and } (a_s : \hat{A}) \in \Gamma \text{ and } \vdash_{\Sigma} (\&\{\overline{l : A_l}\}, \hat{A}) \text{ esync}$  (by inversion on (T- $\Theta_2$ ))  
*for some  $\Delta_3$  and  $\hat{A}$*   
 $(\forall i) \Gamma; \Delta_3 \vdash_{\Sigma} P_i :: (a_l : A_{L_i})$  (by inversion on (T- $\&_R$ ))  
 $\Gamma \vDash_{\Sigma} \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, \Delta_3)$  (by inversion on (T- $\Theta_2$ ))

$$\begin{array}{ll}
(\forall i) \vdash_{\Sigma} (A_{L_i}, \hat{A}) \text{ esync} & \text{(by inversion on (T-ESYNC}_{\&}) \\
\Gamma \vDash_{\Sigma} \text{proc}(a_l, P_h), \Theta_2, \Theta_3 :: (\Delta_1, \Delta_2, a_l : A_{L_h}) & \text{(by (T-}\Theta_2)) \\
\Gamma \vDash_{\Sigma} \text{proc}(c_l, Q), \text{proc}(a_l, P_h), \Theta_2, \Theta_3 :: (\Delta_1, c_l : C_L) & \text{(by (T-}\Theta_2)) \\
\Gamma \vDash_{\Sigma} \Theta_1, \text{proc}(c_l, Q), \text{proc}(a_l, P_h), \Theta_2, \Theta_3 :: \Delta & \text{(by Lemma 8)} \\
\Gamma \vDash_{\Sigma} \Lambda; \Theta_1, \text{proc}(c_l, Q), \text{proc}(a_l, P_h), \Theta_2, \Theta_3 :: \Gamma; \Delta & \text{(by (T-}\Omega) \text{ and well-formedness is maintained)}
\end{array}$$

□

### D.3 Progress

The progress theorem relies on the notions of a *poised* and *blocked* process (see Definition 3 and Definition 4) and expresses that being blocked is the *only* way the whole configuration may be stuck [27]. Case (2-c) captures the scenario where a blocked process cannot proceed because the shared channel is unavailable. A successful acquire, on the other hand, is represented as part of case (2-a).

**Theorem 4** (Progress). *If  $\Gamma \vDash_{\Sigma} \Lambda; \Theta :: \Gamma; \Delta$ , then either*

1.  $\Lambda \longrightarrow \Lambda'$ , for some  $\Lambda'$ , or
2.  $\Lambda$  is *poised* and
  - (a)  $\Lambda; \Theta \longrightarrow \Lambda'; \Theta'$ , for some  $\Lambda'$  and  $\Theta'$ , or
  - (b)  $\Theta$  is *poised*, or
  - (c) some process in  $\Theta$  is *blocked* along  $a_s$  and  $\text{unavail}(a_s) \in \Lambda$ .

*Proof.*

$$\begin{array}{ll}
\Gamma \vDash_{\Sigma} \Lambda; \Theta :: \Gamma; \Delta & \text{(by assumption)} \\
\Gamma \vDash_{\Sigma} \Lambda :: \Gamma \text{ and } \Gamma \vDash_{\Sigma} \Theta :: \Delta & \text{(by inversion on (T-}\Omega))
\end{array}$$

We first show that either  $\Lambda \longrightarrow \Lambda'$ , for some  $\Lambda'$ , or that  $\Lambda$  is poised. We proceed by induction on  $\Gamma \vDash_{\Sigma} \Lambda :: \Gamma_1$ , where  $\Gamma_1 \subseteq \Gamma$ :

**Case:**  $\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)$

$(\cdot)$  is poised (by Definition 3)

**Case:**  $\Gamma \vDash_{\Sigma} \text{proc}(a_s, P_{a_s}) :: (a_s : \uparrow_L^s A_L)$ , for some  $a_s, P_{a_s}$ , and  $A_L$

We proceed by case analysis on  $\Gamma \vdash_{\Sigma} P_{a_s} :: (a_s : \uparrow_L^s A_L)$ :

**Subcase:**  $\Gamma \vdash_{\Sigma} \text{fwd } a_s b_s :: (a_s : \uparrow_L^s A_L)$ , for some  $(b_s : \hat{A}) \in \Gamma$  such that  $\hat{A} \leq \uparrow_L^s A_L$

$\text{proc}(a_s, \text{fwd } a_s b_s), \Lambda_1 \longrightarrow \text{unavail}(a_s), [b_s/a_s] \Lambda_1$  (by D-ID<sub>s</sub>)

**Subcase:**  $\Gamma \vdash_{\Sigma} x_s \leftarrow X_s \leftarrow \bar{y}_s; Q_{x_s} :: (a_s : \uparrow_L^s A_L)$

$\text{proc}(a_s, x_s \leftarrow X_s \leftarrow \bar{b}; Q_{x_s}), \Lambda_1 \longrightarrow \text{proc}(a_s, [b_s/x_s] Q_{x_s}), \text{proc}(b_s, [b_s/x'_s, \bar{b}/\bar{y}] P_{x'_s, \bar{y}}), \Lambda_1$   
*where  $b$  fresh* (by D-SPAWN<sub>SS</sub>)

**Subcase:**  $\Gamma \vdash_{\Sigma} x_l \leftarrow \text{accept } a_s; P_{x_l} :: (a_s : \uparrow_L^s A_L)$

$\text{proc}(a_s, x_l \leftarrow \text{accept } a_s; P_{x_l}), \Lambda_1$  is poised (by Definition 3)

**Case:**  $\Gamma \vDash_{\Sigma} \text{unavail}(a_s) :: (a_s : \hat{A})$ , for some  $a_s$  and  $\hat{A}$

unavail( $a_s$ ) is poised

(by Definition 3)

**Case:**  $\Gamma \vDash_{\Sigma} \Lambda_1, \Lambda_2 :: \Gamma_1, \Gamma_2$ , for some  $\Lambda_1, \Lambda_2, \Gamma_1$ , and  $\Gamma_2$ , such that  $\Gamma = \Gamma_1, \Gamma_2$

Either  $\Lambda_1 \longrightarrow \Lambda'_1$ , for some  $\Lambda'_1$ , or  $\Lambda_1$  is poised, or  $\Lambda_2 \longrightarrow \Lambda'_2$ , for some  $\Lambda'_2$ , or  $\Lambda_2$  is poised.

(by I.H.)

**Subcase:**  $\Lambda_1 \longrightarrow \Lambda'_1$ , for some  $\Lambda'_1$  and  $\Lambda_2$  is poised

$\Lambda_1, \Lambda_2 \longrightarrow \Lambda'_1, \Lambda_2$

**Subcase:**  $\Lambda_1 \longrightarrow \Lambda'_1$ , for some  $\Lambda'_1$  and  $\Lambda_2 \longrightarrow \Lambda'_2$ , for some  $\Lambda'_2$

$\Lambda_1, \Lambda_2 \longrightarrow \Lambda'_1, \Lambda'_2$

**Subcase:**  $\Lambda_1$  is poised and  $\Lambda_2 \longrightarrow \Lambda'_2$ , for some  $\Lambda'_2$

$\Lambda_1, \Lambda_2 \longrightarrow \Lambda_1, \Lambda'_2$

**Subcase:**  $\Lambda_1$  is poised and  $\Lambda_2$  is poised

$\Lambda_1, \Lambda_2$  is poised

Having proved that either  $\Lambda \longrightarrow \Lambda'$ , for some  $\Lambda'$ , or that  $\Lambda$  is poised, we assume that  $\Lambda$  is poised and proceed by induction on  $\Gamma \vDash_{\Sigma} \Theta :: \Delta$ :

**Case:**  $\Gamma \vDash_{\Sigma} (\cdot) :: (\cdot)$

$(\cdot)$  is poised

(by Definition 3)

**Case:**  $\Gamma \vDash_{\Sigma} \text{proc}(a_l, P_{a_l}), \Theta_1 :: (\Delta_1, a_l : A_l)$ , for some  $a_l, P_{a_s}, \Theta_1, \Delta_1$ , and  $A_l$

$\Gamma; \Delta'_1 \vdash_{\Sigma} P_{a_l} :: (a_l : A_l)$  and  $\Gamma \vDash_{\Sigma} \Theta_1 :: \Delta_1, \Delta'_1$   
for some  $\Delta'_1$

(by inversion on (T- $\Theta_2$ ))

Either  $\Lambda; \Theta_1 \longrightarrow \Lambda'; \Theta'_1$ , for some  $\Lambda'$  and  $\Theta'$ , or  $\Theta_1$  is poised or some process in  $\Theta_1$  is blocked along  $a_s$  and  $\text{unavail}(a_s) \in \Lambda$ .

(by I.H.)

**Subcase:**  $\Lambda; \Theta_1 \longrightarrow \Lambda'; \Theta'_1$

$\Lambda; \text{proc}(a_l, P_{a_l}), \Theta_1 \longrightarrow \Lambda'; \text{proc}(a_l, P_{a_l}), \Theta'_1$

**Subcase:**  $\Theta_1$  is poised

We proceed by case analysis on  $\Gamma; \Delta'_1 \vdash_{\Sigma} P_{a_l} :: (a_l : A_l)$ :

**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_{\Sigma} \text{fwd } a_l b_l :: (a_l : A_l)$ , for some  $b_l : A_l$  such that  $\Delta'_1 = a_l : A_l$

$\Lambda; \text{proc}(a_l, \text{fwd } a_l b_l), \Theta_1 \longrightarrow [b_s/a_s] \Lambda; [b_s/a_s] \Theta_1$

(by D-ID<sub>l</sub>)

**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_{\Sigma} x_l \leftarrow X_l \leftarrow \bar{b}; Q_{x_l} :: (a_l : A_l)$

$\Lambda; \text{proc}(a_l, x_l \leftarrow X_l \leftarrow \bar{b}; Q_{x_l}), \Theta_1 \longrightarrow \text{unavail}(b_s), \Lambda; \text{proc}(a_l, [b_l/x_l] Q_{x_l}), \text{proc}(b_l, [b_l/x'_l, \bar{b}/\bar{y}] P_{x'_l, \bar{y}}), \Theta_1$   
where  $b$  fresh

(by D-SPAWN<sub>LL</sub>)

**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_{\Sigma} x_s \leftarrow X_s \leftarrow \bar{b}; Q_{x_s} :: (a_l : A_l)$

$\Lambda; \text{proc}(a_l, x_s \leftarrow X_s \leftarrow \bar{b}; Q_{x_s}), \Theta_1 \longrightarrow \Lambda; \text{proc}(a_l, [b_s/x_s] Q_{x_s}), \text{proc}(b_s, [b_s/x'_s, \bar{b}/\bar{y}] P_{x'_s, \bar{y}}), \Theta_1$   
where  $b$  fresh

(by D-SPAWN<sub>LS</sub>)

**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_{\Sigma} x_l \leftarrow \text{acquire } c_s; Q_{x_l} :: (a_l : A_l)$ , for some  $(c_s : \hat{C}) \in \Gamma$  such that  $\hat{C} \leq \uparrow_l^s B_l$ , for some  $B_l$

Either there exists a  $\text{proc}(c_s, P_{c_s})$  in  $\Lambda$  or an  $\text{unavail}(c_s)$  in  $\Lambda$ .

(by Lemma 10-1)

**Subsubsubcase:** There exists a  $\Lambda_1$  such that  $\Lambda = \text{proc}(c_s, P_{c_s}), \Lambda_1$ :

$\text{proc}(c_s, P_{c_s})$  is poised (by Definition 3 since  $\Lambda$  is poised)  
 $\text{proc}(c_s, P_{c_s}) = \text{proc}(c_s, x_l \leftarrow \text{accept } c_s; P'_{x_l})$  (by Definition 3)  
 $\text{proc}(c_s, x_l \leftarrow \text{accept } c_s; P'_{x_l}), \Lambda_1; \text{proc}(a_l, x_l \leftarrow \text{acquire } c_s; Q_{a_l}), \Theta_1$   
 $\longrightarrow \text{unavail}(c_s), \Lambda_1; \text{proc}(a_l, [c_l/x_l] Q_{a_l}), \text{proc}(c_l, [c_l/x_l] P'_{x_l}), \Theta_1$  (by D- $\uparrow_l^s$  – acquire/accept)  
**Subsubsubcase:** There exists a  $\Lambda_1$  such that  $\Lambda = \text{unavail}(c_s), \Lambda_1$ :  
 $\text{proc}(a_l, x_l \leftarrow \text{acquire } c_s)$  in  $\Theta$  is blocked along  $c_s$  and  $\text{unavail}(c_s) \in \Lambda$  (by Definition 4)  
**Subsubcase:**  $\Gamma; \Delta_2, c_l : \downarrow_l^s C_s \vdash_\Sigma x_s \leftarrow \text{release } c_l; Q_{x_s} :: (a_l : A_l)$ , for some  $C_s$  and  $\Delta_2$  such that  $\Delta'_1 = \Delta_2, c_l : \downarrow_l^s C_s$   
There exist  $\Theta_2, \Theta'_2$ , and  $\text{proc}(c_l, P_{c_l})$  such that  $\Theta_1 = \Theta_2, \text{proc}(c_l, P_{c_l}), \Theta'_2$  and (by Lemma 10-2)  
there exists a  $\Lambda_1$  and  $\text{unavail}(c_s)$  such that  $\Lambda = \text{unavail}(c_s), \Lambda_1$ . (by well-formedness of configuration)  
 $\text{proc}(c_l, P_{c_l})$  is poised (by Definition 3 since  $\Theta_1$  is poised)  
 $\text{proc}(c_l, P_{c_l}) = \text{proc}(c_l, x_s \leftarrow \text{detach } c_l; P'_{x_s})$  (by Definition 3 and inversion on process typing)  
 $\text{unavail}(c_s), \Lambda_1; \text{proc}(a_l, x_s \leftarrow \text{release } c_l; Q_{x_s}), \Theta_2, \text{proc}(c_l, x_s \leftarrow \text{detach } c_l; P'_{x_s}), \Theta'_2$   
 $\longrightarrow \text{proc}(c_s, [c_s/x_s] P'_{x_s}), \Lambda_1; \text{proc}(a_l, [c_s/x_s] Q_{x_s}), \Theta_2, \Theta'_2$  (by D- $\downarrow_l^s$  – release/detach)  
**Subsubcase:**  $\Gamma; \cdot \vdash_\Sigma x_s \leftarrow \text{detach } a_l; P_{x_s} :: (a_l : A_l)$   
 $\text{proc}(a_l, x_s \leftarrow \text{detach } a_l; P_{x_s}), \Theta_1$  is poised (by Definition 3 since  $\Theta_1$  is poised)  
**Subsubcase:**  $\Gamma; \Delta_2, c_l : \mathbf{1} \vdash_\Sigma \text{wait } c_l; Q :: (a_l : A_l)$ , for some  $\Delta_2$  such that  $\Delta'_1 = \Delta_2, c_l : \mathbf{1}$   
There exist  $\Theta_2, \Theta'_2$ , and  $\text{proc}(c_l, P_{c_l})$  such that  $\Theta_1 = \Theta_2, \text{proc}(c_l, P_{c_l}), \Theta'_2$ . (by Lemma 10-2)  
 $\text{proc}(c_l, P_{c_l})$  is poised (by Definition 3 since  $\Theta_1$  is poised)  
 $\text{proc}(c_l, P_{c_l}) = \text{proc}(c_l, \text{close } c_l; P'_{x_s})$  (by Definition 3 and inversion on process typing)  
 $\Lambda; \text{proc}(a_l, \text{wait } c_l; Q), \Theta_2, \text{proc}(c_l, \text{close } c_l), \Theta'_2$   
 $\longrightarrow \Lambda; \text{proc}(a_l, Q), \Theta_2, \Theta'_2$  (by D-1)  
**Subsubcase:**  $\Gamma; \cdot \vdash_\Sigma \text{close } a_l :: (a_l : \mathbf{1})$   
 $\text{proc}(a_l, \text{close } a_l), \Theta_1$  is poised (by Definition 3 since  $\Theta_1$  is poised)  
**Subsubcase:**  $\Gamma; \Delta_2, c_l : B_l \otimes C_l \vdash_\Sigma y_l \leftarrow \text{recv } c_l; Q_{y_l} :: (a_l : A_l)$ ,  
for some  $B_l, C_l$ , and  $\Delta_2$  such that  $\Delta'_1 = \Delta_2, c_l : B_l \otimes C_l$   
There exist  $\Theta_2, \Theta'_2$ , and  $\text{proc}(c_l, P_{c_l})$  such that  $\Theta_1 = \Theta_2, \text{proc}(c_l, P_{c_l}), \Theta'_2$ . (by Lemma 10-2)  
 $\text{proc}(c_l, P_{c_l})$  is poised (by Definition 3 since  $\Theta_1$  is poised)  
 $\text{proc}(c_l, P_{c_l}) = \text{proc}(c_l, \text{send } c_l b_l; P')$ ,  
for some  $b_l : B_l$  (by Definition 3 and inversion on process typing)  
 $\Lambda; \text{proc}(a_l, y_l \leftarrow \text{recv } c_l; Q_{y_l}), \Theta_2, \text{proc}(c_l, \text{send } c_l b_l; P'), \Theta'_2$   
 $\longrightarrow \Lambda; \text{proc}(a_l, [b_l/y_l] Q_{y_l}), \Theta_2, \text{proc}(c_l, P'), \Theta'_2$  (by D- $\otimes/\exists$ )  
**Subsubcase:**  $\Gamma; \Delta_2, b_l : B_l \vdash_\Sigma \text{send } a_l b_l; P :: (a_l : B_l \otimes C_l)$ ,  
for some  $B_l, C_l$ , and  $\Delta_2$  such that  $A_l = B_l \otimes C_l$  and  $\Delta'_1 = \Delta_2, b_l : B_l$   
 $\text{proc}(a_l, \text{send } a_l b_l; P), \Theta_1$  is poised (by Definition 3 since  $\Theta_1$  is poised)  
**Subsubcase:**  $\Gamma; \Delta_2, c_l : (\exists B_s, C_l) \vdash_\Sigma y_s \leftarrow \text{recv } c_l; Q_{y_s} :: (a_l : A_l)$ ,  
for some  $B_s, C_l$ , and  $\Delta_2$  such that  $\Delta'_1 = \Delta_2, c_l : \exists B_s, C_l$   
There exist  $\Theta_2, \Theta'_2$ , and  $\text{proc}(c_l, P_{c_l})$  such that  $\Theta_1 = \Theta_2, \text{proc}(c_l, P_{c_l}), \Theta'_2$ . (by Lemma 10-2)  
 $\text{proc}(c_l, P_{c_l})$  is poised (by Definition 3 since  $\Theta_1$  is poised)  
 $\text{proc}(c_l, P_{c_l}) = \text{proc}(c_l, \text{send } c_l b_s; P')$ ,  
for some  $b_s : B_s$  (by Definition 3 and inversion on process typing)  
 $\Lambda; \text{proc}(a_l, y_s \leftarrow \text{recv } c_l; Q_{y_s}), \Theta_2, \text{proc}(c_l, \text{send } c_l b_s; P'), \Theta'_2$   
 $\longrightarrow \Lambda; \text{proc}(a_l, [b_s/y_s] Q_{y_s}), \Theta_2, \text{proc}(c_l, P'), \Theta'_2$  (by D- $\otimes/\exists$ )  
**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_\Sigma \text{send } a_l b_s; P :: (a_l : (\exists B_s, C_l))$ ,  
for some  $B_s, C_l$ , and  $(b_s : \hat{B} \in \Gamma)$  such that  $A_l = \exists B_s, C_l$  and  $\hat{B} \leq B_s$   
 $\text{proc}(a_l, \text{send } a_l b_s; P), \Theta_1$  is poised (by Definition 3 since  $\Theta_1$  is poised)  
**Subsubcase:**  $\Gamma; \Delta_2, c_l : B_l \multimap C_l, b_l : B_l \vdash_\Sigma \text{send } c_l b_l; Q :: (a_l : A_l)$ ,  
for some  $B_l, C_l$ , and  $\Delta_2$  such that  $\Delta'_1 = \Delta_2, c_l : B_l \otimes C_l, b_l : B_l$

There exist  $\Theta_2, \Theta'_2$ , and  $\text{proc}(c_l, P_{c_l})$  such that  $\Theta_1 = \Theta_2, \text{proc}(c_l, P_{c_l}), \Theta'_2$ . (by Lemma 10-2)

$\text{proc}(c_l, P_{c_l})$  is poised (by Definition 3 since  $\Theta_1$  is poised)

$\text{proc}(c_l, P_{c_l}) = \text{proc}(c_l, y_l \leftarrow \text{recv } c_l; P'_{y_l})$  (by Definition 3 and inversion on process typing)

$\Lambda; \text{proc}(a_l, \text{send } c_l b_l; Q), \Theta_2, \text{proc}(c_l, y_l \leftarrow \text{recv } c_l; P'_{y_l}), \Theta'_2$   
 $\longrightarrow \Lambda; \text{proc}(a_l, Q), \Theta_2, \text{proc}(c_l, [b_l/y_l] P'_{y_l}), \Theta'_2$  (by D- $\rightarrow$  /  $\Pi$ )

**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_\Sigma y_l \leftarrow \text{recv } a_l; P_{y_l} :: (a_l : B_l \multimap C_l)$ ,  
for some  $B_l, C_l$  such that  $A_l = B_l \multimap C_l$

$\text{proc}(a_l, y_l \leftarrow \text{recv } a_l; P_{y_l}), \Theta_1$  is poised (by Definition 3 since  $\Theta_1$  is poised)

**Subsubcase:**  $\Gamma; \Delta_2, c_l : (\Pi B_s.C_l) \vdash_\Sigma \text{send } c_l b_s; Q :: (a_l : A_l)$ ,  
for some  $B_s, C_l, (b_s : \hat{B}) \in \Gamma$ , and  $\Delta_2$  such that  $\hat{B} \leq B_s$  and  $\Delta'_1 = \Delta_2, c_l : (\Pi B_s.C_l)$

There exist  $\Theta_2, \Theta'_2$ , and  $\text{proc}(c_l, P_{c_l})$  such that  $\Theta_1 = \Theta_2, \text{proc}(c_l, P_{c_l}), \Theta'_2$ . (by Lemma 10-2)

$\text{proc}(c_l, P_{c_l})$  is poised (by Definition 3 since  $\Theta_1$  is poised)

$\text{proc}(c_l, P_{c_l}) = \text{proc}(c_l, y_s \leftarrow \text{recv } c_l; P'_{y_s})$  (by Definition 3 and inversion on process typing)

$\Lambda; \text{proc}(a_l, \text{send } c_l b_s; Q), \Theta_2, \text{proc}(c_l, y_s \leftarrow \text{recv } c_l; P'_{y_s}), \Theta'_2$   
 $\longrightarrow \Lambda; \text{proc}(a_l, Q), \Theta_2, \text{proc}(c_l, [b_s/y_s] P'_{y_s}), \Theta'_2$  (by D- $\rightarrow$  /  $\Pi$ )

**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_\Sigma y_s \leftarrow \text{recv } a_l; P_{y_s} :: (a_l : (\Pi B_s.C_l))$ ,  
for some  $B_s, C_l$  such that  $A_l = \Pi B_s.C_l$

$\text{proc}(a_l, y_s \leftarrow \text{recv } a_l; P_{y_s}), \Theta_1$  is poised (by Definition 3 since  $\Theta_1$  is poised)

**Subsubcase:**  $\Gamma; \Delta_2, c_l : \oplus \{\overline{l : B_l}\} \vdash_\Sigma \text{case } c_l \text{ of } \overline{l \Rightarrow Q} :: (a_l : A_l)$ ,  
for some  $B_l$  and  $\Delta_2$  such that  $\Delta'_1 = \Delta_2, c_l : \oplus \{\overline{l : B_l}\}$

There exist  $\Theta_2, \Theta'_2$ , and  $\text{proc}(c_l, P_{c_l})$  such that  $\Theta_1 = \Theta_2, \text{proc}(c_l, P_{c_l}), \Theta'_2$ . (by Lemma 10-2)

$\text{proc}(c_l, P_{c_l})$  is poised (by Definition 3 since  $\Theta_1$  is poised)

$\text{proc}(c_l, P_{c_l}) = \text{proc}(c_l, c_l.l_h; P')$  (by Definition 3 and inversion on process typing)

$\Lambda; \text{proc}(a_l, \text{case } c_l \text{ of } \overline{l \Rightarrow Q}), \Theta_2, \text{proc}(c_l, c_l.l_h; P'), \Theta'_2$   
 $\longrightarrow \Lambda; \text{proc}(a_l, Q_h), \Theta_2, \text{proc}(a_l, P'), \Theta'_2$  (by D- $\oplus$ )

**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_\Sigma a_l.l_h; P :: (a_l : \oplus \{\overline{l : B_l}\})$ ,  
for some  $B_l$  such that  $A_l = \oplus \{\overline{l : B_l}\}$

$\text{proc}(a_l, a_l.l_h; P), \Theta_1$  is poised (by Definition 3 since  $\Theta_1$  is poised)

**Subsubcase:**  $\Gamma; \Delta_2, c_l : \& \{\overline{l : B_l}\} \vdash_\Sigma c_l.l_h; Q :: (a_l : A_l)$ ,  
for some  $B_l$  and  $\Delta_2$  such that  $\Delta'_1 = \Delta_2, c_l : \& \{\overline{l : B_l}\}$

There exist  $\Theta_2, \Theta'_2$ , and  $\text{proc}(c_l, P_{c_l})$  such that  $\Theta_1 = \Theta_2, \text{proc}(c_l, P_{c_l}), \Theta'_2$ . (by Lemma 10-2)

$\text{proc}(c_l, P_{c_l})$  is poised (by Definition 3 since  $\Theta_1$  is poised)

$\text{proc}(c_l, P_{c_l}) = \text{proc}(c_l, \text{case } c_l \text{ of } \overline{l \Rightarrow P'})$  (by Definition 3 and inversion on process typing)

$\Lambda; \text{proc}(a_l, c_l.l_h; Q), \Theta_2, \text{proc}(c_l, \text{case } c_l \text{ of } \overline{l \Rightarrow P'}), \Theta'_2$   
 $\longrightarrow \Lambda; \text{proc}(a_l, Q), \Theta_2, \text{proc}(c_l, P'_h), \Theta'_2$  (by D- $\&$ )

**Subsubcase:**  $\Gamma; \Delta'_1 \vdash_\Sigma \text{case } a_l \text{ of } \overline{l \Rightarrow P} :: (a_l : \& \{\overline{l : B_l}\})$ ,  
for some  $B_l$  such that  $A_l = \& \{\overline{l : B_l}\}$

$\text{proc}(a_l, \text{case } a_l \text{ of } \overline{l \Rightarrow P}), \Theta_1$  is poised (by Definition 3 since  $\Theta_1$  is poised)

**Subcase:** Some process in  $\Theta_1$  is blocked along  $a_s$  and  $\text{unavail}(a_s) \in \Lambda$

Some process in  $\text{proc}(a_l, P_{a_l}), \Theta_1$  is blocked along  $a_s$  and  $\text{unavail}(a_s) \in \Lambda$

□

## E Examples

### E.1 “Imperative” Queue

Linearity prohibits a more “imperative” style of a queue implementation that maintains a reference to the back of the queue for direct insertion of an element. In this section, we give an implementation of a queue with direct access to both the front and back of the queue; the result is shown in Figure 21.

$$\begin{array}{l}
 \mathbf{list\ } A_s = \uparrow_L^s \&\{\text{ins} : \Pi A_s . \downarrow_L^s \mathbf{list\ } A_s \\
 \text{del} : \oplus \{\text{none} : \downarrow_L^s \mathbf{list\ } A_s, \\
 \text{some} : \exists A_s . \downarrow_L^s \mathbf{list\ } A_s\} \\
 \\
 \mathbf{queue\ } A_s = \uparrow_L^s \&\{\text{enq} : \Pi A_s . \downarrow_L^s \mathbf{queue\ } A_s, \\
 \text{deq} : \oplus \{\text{none} : \downarrow_L^s \mathbf{queue\ } A_s, \\
 \text{some} : \exists A_s . \downarrow_L^s \mathbf{queue\ } A_s\} \\
 \\
 \mathbf{empty} : \{\mathbf{list\ } A_s\} \\
 \mathbf{c} \leftarrow \mathbf{empty} = \\
 \mathbf{c}' \leftarrow \text{accept } \mathbf{c}; \\
 \text{case } \mathbf{c}' \text{ of} \\
 | \text{ins} \rightarrow \mathbf{x} \leftarrow \text{recv } \mathbf{c}'; \\
 \quad \mathbf{e} \leftarrow \mathbf{empty}; \\
 \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \\
 \quad \mathbf{c} \leftarrow \mathbf{elem} \leftarrow \mathbf{x}, \mathbf{e} \\
 | \text{del} \rightarrow \mathbf{c}'.\text{none}; \\
 \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \\
 \quad \mathbf{c} \leftarrow \mathbf{empty} \\
 \\
 \mathbf{elem} : \{\mathbf{list\ } A_s \leftarrow A_s, \mathbf{list\ } A_s\} \\
 \mathbf{c} \leftarrow \mathbf{elem} \leftarrow \mathbf{x}, \mathbf{next} = \\
 \mathbf{c}' \leftarrow \text{accept } \mathbf{c}; \\
 \text{case } \mathbf{c}' \text{ of} \\
 | \text{ins} \rightarrow \mathbf{y} \leftarrow \text{recv } \mathbf{c}'; \\
 \quad \mathbf{n} \leftarrow \mathbf{elem} \leftarrow \mathbf{y}, \mathbf{next}; \\
 \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \\
 \quad \mathbf{c} \leftarrow \mathbf{elem} \leftarrow \mathbf{x}, \mathbf{n} \\
 | \text{del} \rightarrow \mathbf{c}'.\text{some}; \\
 \quad \text{send } \mathbf{c}' \mathbf{x}; \\
 \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \\
 \quad \text{fwd } \mathbf{c} \mathbf{next} \\
 \\
 \mathbf{head} : \{\mathbf{queue\ } A_s \leftarrow \mathbf{list\ } A_s, \mathbf{list\ } A_s\} \\
 \mathbf{c} \leftarrow \mathbf{head} \leftarrow \mathbf{front}, \mathbf{back} = \\
 \mathbf{c}' \leftarrow \text{accept } \mathbf{c}; \\
 \text{case } \mathbf{c}' \text{ of} \\
 | \text{enq} \rightarrow \mathbf{x} \leftarrow \text{recv } \mathbf{c}'; \\
 \quad \mathbf{back}' \leftarrow \text{acquire } \mathbf{back}; \\
 \quad \mathbf{back}'.\text{ins}; \\
 \quad \text{send } \mathbf{back}' \mathbf{x}; \\
 \quad \mathbf{back} \leftarrow \text{release } \mathbf{back}'; \\
 \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \\
 \quad \mathbf{c} \leftarrow \mathbf{head} \leftarrow \mathbf{front}, \mathbf{back} \\
 | \text{deq} \rightarrow \mathbf{front}' \leftarrow \text{acquire } \mathbf{front}; \\
 \quad \mathbf{front}'.\text{del}; \\
 \quad (\text{case } \mathbf{front}' \text{ of} \\
 | \text{none} \rightarrow \mathbf{front} \leftarrow \text{release } \mathbf{front}'; \\
 \quad \mathbf{c}'.\text{none}; \\
 \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \\
 \quad \mathbf{c} \leftarrow \mathbf{head} \leftarrow \mathbf{front}, \mathbf{back} \\
 | \text{some} \rightarrow \mathbf{x} \leftarrow \text{recv } \mathbf{front}'; \\
 \quad \mathbf{front} \leftarrow \text{release } \mathbf{front}'; \\
 \quad \mathbf{c}'.\text{some}; \text{send } \mathbf{c}' \mathbf{x}; \\
 \quad \mathbf{c} \leftarrow \text{detach } \mathbf{c}'; \\
 \quad \mathbf{c} \leftarrow \mathbf{head} \leftarrow \mathbf{front}, \mathbf{back})
 \end{array}$$

Figure 21: Imperative queue: has access to both the front and the end of the queue.

As is common for imperative queue implementations, the queue consists of a head that maintains a reference to the front and the back of a linked list that comprises the elements of the queue. The session type  $\mathbf{queue\ } A_s$  defines the protocol of the queue, which is the protocol we introduced earlier for the queue in the producer-consumer example. For convenience, we repeat the protocol definition in Figure 21. Process  $\mathbf{head}$  implements this protocol. The session type  $\mathbf{list\ } A_s$  defines the protocol of the linked list that is used internally by process  $\mathbf{head}$  to store the elements of the queue. Session type  $\mathbf{list\ } A_s$  is implemented by the processes  $\mathbf{empty}$  and  $\mathbf{elem}$ . Process  $\mathbf{empty}$  represents an empty list, process  $\mathbf{elem}$  represents a non-empty list. The list thus consists of a sequence of  $\mathbf{elem}$  processes, ended by an  $\mathbf{empty}$  process. An empty queue is created by the following lines of code

$$\begin{array}{l}
 \mathbf{e} \leftarrow \mathbf{empty}; \\
 \mathbf{q} \leftarrow \mathbf{head} \leftarrow \mathbf{e}, \mathbf{e};
 \end{array}$$

These lines illustrate that sharing arises in case of an empty queue between the  $\mathbf{front}$  and the  $\mathbf{back}$  channel because they both point to the same list element. For this reason, process  $\mathbf{head}$  defines the channels  $\mathbf{front}$  and  $\mathbf{back}$  to be of the shared session type  $\mathbf{list\ } A_s$ . Sharing also arises in case of a non-empty queue between a list’s  $\mathbf{next}$  channel

and the head's *back* channel because both point to the same element in case of the last element in the list. For this reason, process *elem* defines the channel *next* to be of the shared session type list  $A_s$ . Process *head* uses the acquire-release primitives to communicate with the list. It acquires channel *back* upon receiving an *enq* request and channel *front* upon receiving a *deq* request and then releases both channels upon successful insertion into or deletion from the list.

## E.2 Cycles and Deadlocks

In the dining philosophers example (Figure 7), the circular dependency among shared channels is created by shared channel process arguments. Alternatively, the circularity can be introduced by shared channel input and output. Figure 22 gives an example that leads to a classic deadlock. The cycle is created by process *client*, which spawns a new shared process *deadlock* and then sends a “self-reference” to that very process after acquiring it. The deadlock occurs once the newly spawned and now linear process attempts to acquire itself.

```

cycle = { $\uparrow_L^s \Pi$ cycle.1}      client : {1}      deadlock : {cycle}
c  $\leftarrow$  client =          c  $\leftarrow$  deadlock =
  d  $\leftarrow$  deadlock ;      c'  $\leftarrow$  accept c ;
  d'  $\leftarrow$  acquire d ;      self  $\leftarrow$  recv c' ;
  send d' d ;                self'  $\leftarrow$  acquire self ; (* deadlocks here *)
  wait d' ;                  send self' self ;
  close c ;                   wait self' ;
                               close c' ;

```

Figure 22: Processes creating a cycle along channel *self*, causing a deadlock.

## E.3 Cycles and Blocking

Another form of blocking can arise from circularity that is caused by forwarding rather than by a classic deadlock. Figure 23 shows a variation of the previous example where the offering process *terminate* forwards itself to itself, resulting in a configuration in which no process exists anymore along either channel. As a result, the client will block when attempting to re-acquire the process.

```

cycle = { $\uparrow_L^s \&$ {input :  $\Pi$ cycle. $\downarrow_L^s$ cycle,      client : {1}      terminate : {cycle}
  dealloc : 1}}      c  $\leftarrow$  client =          c  $\leftarrow$  terminate =
  d  $\leftarrow$  terminate ;      d  $\leftarrow$  accept c ;
  d'  $\leftarrow$  acquire d ;      c'  $\leftarrow$  input  $\rightarrow$  self  $\leftarrow$  recv c' ;
  d'.input ; send d' d ;      c  $\leftarrow$  detach c' ;
  d  $\leftarrow$  release d' ;       fwd c self
  d'  $\leftarrow$  acquire d ; (* blocks here *)      | dealloc  $\rightarrow$  close c'
  d'.dealloc ;
  wait d' ;
  close c ;

```

Figure 23: Client-side blocking caused by circular forwarding.

## E.4 Linear Forwarding

In the linear forwarding case of the preservation proof, the following 9 subcases for the types of  $a_s$  and  $b_s$  are considered, categorized as shown:

$$\hat{B} \leq \hat{A} : \quad \begin{array}{ll} a_s : \top & \text{and } b_s : \top \\ a_s : \top & \text{and } b_s : \uparrow_L^s B_L \end{array}$$



$$\begin{array}{l}
a_s : \top \quad \text{and} \quad b_s : \perp \\
a_s : \uparrow_L^s A_L \quad \text{and} \quad b_s : \uparrow_L^s B_L \quad \text{and} \quad A_L = B_L \\
a_s : \uparrow_L^s A_L \quad \text{and} \quad b_s : \perp \\
a_s : \perp \quad \text{and} \quad b_s : \perp \\
\uparrow_L^s A_L \neq \uparrow_L^s B_L : \quad a_s : \uparrow_L^s A_L \quad \text{and} \quad b_s : \uparrow_L^s B_L \quad \text{and} \quad A_L \neq B_L \\
\hat{B} \geq \hat{A} : \quad a_s : \uparrow_L^s A_L \quad \text{and} \quad b_s : \top \\
\quad \quad \quad a_s : \perp \quad \text{and} \quad b_s : \uparrow_L^s B_L \\
\quad \quad \quad a_s : \perp \quad \text{and} \quad b_s : \perp
\end{array}$$

Next, we will provide examples for some of the cases above. As in Section 4.2, we use the connective  $\supset$  [26, 58] to support value input. The type “int  $\supset$  int  $\supset$  1”, for example, describes as session that receives a value of type int and then continues as a session that expects to receive a value of type int before terminating. As a reminder, the process predicate with the forwarding term is  $\text{proc}(a_L, \text{fwd } a_L b_L)$ .

In the first 3 cases,  $a_s : \top$  guarantees that the channel  $a_s$  does not yet occur in any process terms in the configuration. In that case, substitution does actually not have any effect. An example for  $a_s : \top$  and  $b_s : \top$  is:

$$\begin{array}{llll}
\text{supertype} = \text{int} \supset \text{int} \supset \mathbf{1} & \text{main} : \{\mathbf{1}\} & \text{super} : \{\text{supertype}\} & \text{sub} : \{\text{subtype}\} \\
\text{subtype} = \text{int} \supset \mathbf{1} & c \leftarrow \text{main} = & a \leftarrow \text{super} = & b \leftarrow \text{sub} = \\
& s \leftarrow \text{super} ; & x \leftarrow \text{recv } a ; & x \leftarrow \text{recv } b ; \\
& \text{send } s \ 6 ; \text{send } s \ 6 ; & b \leftarrow \text{sub} ; & \text{close } b \\
& \text{wait } s ; \text{close } c & \text{fwd } a \ b &
\end{array}$$

An example for  $a_s : \top$  and  $b_s : \uparrow_L^s B_L$  is:

$$\begin{array}{llll}
\text{supertype} = \text{int} \supset \text{int} \supset \mathbf{1} & \text{main} : \{\mathbf{1}\} & \text{super} : \{\text{supertype}\} & \text{sub} : \{\text{subtype}\} \\
\text{subtype} = \uparrow_L^s(\text{int} \supset \mathbf{1}) & c \leftarrow \text{main} = & a \leftarrow \text{super} = & \mathbf{b} \leftarrow \text{sub} = \\
& s \leftarrow \text{super} ; & x \leftarrow \text{recv } a ; & b' \leftarrow \text{accept } \mathbf{b} ; \\
& \text{send } s \ 6 ; \text{send } s \ 6 ; & \mathbf{b} \leftarrow \text{sub} ; & x \leftarrow \text{recv } b' ; \\
& \text{wait } s ; \text{close } c & b' \leftarrow \text{acquire } \mathbf{b} ; & \text{close } b' \\
& & \text{fwd } a \ b' &
\end{array}$$

An example for  $a_s : \uparrow_L^s A_L$  and  $b_s : \uparrow_L^s B_L$  and  $A_L \neq B_L$  is:

$$\begin{array}{llll}
\text{supertype} = \uparrow_L^s(\text{int} \supset \text{int} \supset \mathbf{1}) & \text{main} : \{\mathbf{1}\} & \text{super} : \{\text{supertype}\} & \text{sub} : \{\text{subtype}\} \\
\text{subtype} = \uparrow_L^s(\text{int} \supset \mathbf{1}) & c \leftarrow \text{main} = & \mathbf{a} \leftarrow \text{super} = & \mathbf{b} \leftarrow \text{sub} = \\
& \mathbf{s} \leftarrow \text{super} ; & a' \leftarrow \text{accept } \mathbf{a} ; & b' \leftarrow \text{accept } \mathbf{b} ; \\
& s' \leftarrow \text{acquire } \mathbf{s} ; & x \leftarrow \text{recv } a' ; & x \leftarrow \text{recv } b' ; \\
& \text{send } s' \ 6 ; \text{send } s' \ 6 ; & \mathbf{b} \leftarrow \text{sub} ; & \text{close } b' \\
& \text{wait } s' ; \text{close } c & b' \leftarrow \text{acquire } \mathbf{b} ; & \\
& & \text{fwd } a' \ b' &
\end{array}$$

An example for  $a_s : \top$  and  $b_s : \perp$  is:

$$\begin{array}{llll}
\text{supertype} = \uparrow_L^s(\text{int} \supset \text{int} \supset \mathbf{1}) & \text{main} : \{\mathbf{1}\} & \text{super} : \{\text{supertype}\} & \text{sub} : \{\text{subtype}\} \\
\text{subtype} = \uparrow_L^s(\text{int} \supset \mathbf{1}) & a \leftarrow \text{main} = & \mathbf{b} \leftarrow \text{super} = & \mathbf{c} \leftarrow \text{sub} = \\
& \mathbf{b} \leftarrow \text{super} ; & b' \leftarrow \text{accept } \mathbf{b} ; & c' \leftarrow \text{accept } \mathbf{c} ; \\
& b' \leftarrow \text{acquire } \mathbf{b} ; & x \leftarrow \text{recv } b' ; & x \leftarrow \text{recv } c' ; \\
& \text{send } b' \ 6 ; \text{send } b' \ 6 ; & \mathbf{s} \leftarrow \text{sub} ; & \text{close } c' \\
& \text{fwd } a \ b' & s' \leftarrow \text{acquire } \mathbf{s} ; & \\
& & \text{fwd } b' \ s' &
\end{array}$$

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## References

- [1] R. Arnold.  $C_0$ , an imperative programming language for novice computer scientists. Master's thesis, Department of Computer Science, Carnegie Mellon University, Dec. 2010. Available as Technical Report CMU-CS-10-145.
- [2] R. Atkey, S. Lindley, and J. G. Morris. Conflation confers concurrency. In S. L. et al., editor, *Wadler Festschrift*, pages 32–55. Springer LNCS 9600, 2016.
- [3] R. Beauxis, C. Palamidessi, and F. D. Valencia. On the asynchronous nature of the asynchronous  $\pi$ -calculus. In *Concurrency, Graphs and Models*, volume 5065 of *Lecture Notes in Computer Science*, pages 473–492. Springer, 2008.
- [4] P. N. Benton. A mixed linear and non-linear logic: Proofs, terms and models. In *8th International Workshop on Computer Science Logic (CSL)*, volume 933 of *Lecture Notes in Computer Science*, pages 121–135. Springer, 1994. An extended version appeared as Technical Report UCAM-CL-TR-352, University of Cambridge.
- [5] K. Bierhoff and J. Aldrich. Modular typestate checking of aliased objects. In *22nd Annual ACM SIGPLAN Conference on Object-Oriented Programming, Systems, Languages, and Applications (OOPSLA '07)*, pages 301–320. ACM, 2007.
- [6] J. Boyland. Checking interference with fractional permissions. In *10th International Symposium on Static Analysis (SAS)*, pages 55–72, 2003.
- [7] S. D. Brookes. A semantics for concurrent separation logic. In *15th International Conference on Concurrency Theory (CONCUR)*, volume 3170 of *Lecture Notes in Computer Science*, pages 16–34. Springer, 2004.
- [8] L. Caires and F. Pfenning. Session types as intuitionistic linear propositions. In *21st International Conference on Concurrency Theory (CONCUR)*, volume 6269 of *Lecture Notes in Computer Science*, pages 222–236. Springer, 2010.
- [9] L. Caires, F. Pfenning, and B. Toninho. Linear logic propositions as session types. *Mathematical Structures in Computer Science*, 26(3):367–423, 2016.
- [10] E. Castegren and T. Wrigstad. Lolcat: Relaxed linear references for lock-free programming. Technical Report 2016-013, Uppsala University, July 2016.
- [11] I. Cervesato and A. Scedrov. Relating state-based and process-based concurrency through linear logic. *Information and Computation*, 207(10):1044–1077, 2009.
- [12] I. Cervesato, F. Pfenning, D. Walker, and K. Watkins. A concurrent logical framework ii: Examples and applications. Technical Report CMU-CS-02-102, Computer Science Department, Carnegie Mellon University, 2002. Revised May 2003.
- [13] B.-Y. E. Chang, K. Chaudhuri, and F. Pfenning. A judgmental analysis of linear logic. Technical Report CMU-CS-03-131R, School of Computer Science, Carnegie Mellon University, December 2003.
- [14] K. Crary, R. Harper, and S. Puri. What is a recursive module? In *ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI)*, pages 50–63, 1999.
- [15] R. DeLine and M. Fähndrich. Typestates for objects. In *18th European Conference on Object-Oriented Programming (ECOOP)*, volume 3086 of *Lecture Notes in Computer Science*, pages 465–490. Springer, 2004.
- [16] Y. Deng, R. J. Simmons, and I. Cervesato. Relating reasoning methodologies in linear logic and process algebra. *Mathematical Structure in Computer Science*, 26(5):868–906, Jan. 2016.
- [17] H. DeYoung, L. Caires, F. Pfenning, and B. Toninho. Cut reduction in linear logic as asynchronous session-typed communication. In P. Cégielski and A. Durand, editors, *Proceedings of the 21st Conference on Computer Science Logic, CSL 2012*, pages 228–242, Fontainebleau, France, Sept. 2012. Leibniz International Proceedings in Informatics.

- [18] M. Dezani-Ciancaglini, D. Mostrous, N. Yoshida, and S. Drossopoulou. Session types for object-oriented languages. In *20th European Conference on Object-Oriented Programming (ECOOP)*, volume 4067 of *Lecture Notes in Computer Science*, pages 328–352. Springer, 2006.
- [19] E. W. Dijkstra. Hierarchical ordering of sequential processes. EWD Manuscript 310, 1971–1973.
- [20] M. Fähndrich and R. DeLine. Adoption and focus: Practical linear types for imperative programming. In *ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI)*, pages 13–24. ACM, 2002.
- [21] C. Flanagan and S. Qadeer. A type and effect system for atomicity. In *ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI)*, pages 338–349. ACM, 2003.
- [22] S. J. Gay and M. Hole. Subtyping for session types in the  $\pi$ -calculus. *Acta Informatica*, 42(2–3):191–225, 2005.
- [23] D. K. Gifford and J. M. Lucassen. Integrating functional and imperative programming. In *LISP and Functional Programming*, pages 28–38, 1986.
- [24] J.-Y. Girard. Linear logic. *Theoretical Computer Science*, 50:1–102, 1987.
- [25] D. Griffith. *Polarized Substructural Session Types*. PhD thesis, University of Illinois at Urbana-Champaign, 2016.
- [26] D. Griffith and F. Pfenning. SILL. <https://github.com/ISANobody/sill>, 2015.
- [27] R. Harper. *Practical Foundations for Programming Languages*. Cambridge University Press, 2013.
- [28] S. Heule, K. R. M. Leino, P. Müller, and A. J. Summers. Abstract read permissions: Fractional permissions without the fractions. In *14th International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI)*, volume 7737 of *Lecture Notes in Computer Science*, pages 315–334. Springer, 2013.
- [29] K. Honda. Types for dyadic interaction. In *4th International Conference on Concurrency Theory (CONCUR)*, volume 715 of *Lecture Notes in Computer Science*, pages 509–523. Springer, 1993.
- [30] K. Honda, V. T. Vasconcelos, and M. Kubo. Language primitives and type discipline for structured communication-based programming. In *7th European Symposium on Programming (ESOP)*, volume 1381 of *Lecture Notes in Computer Science*, pages 122–138. Springer, 1998.
- [31] K. Honda, N. Yoshida, and M. Carbone. Multiparty asynchronous session types. In *35th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL)*, pages 273–284. ACM, 2008.
- [32] W. A. Howard. The formulae-as-types notion of construction. Unpublished note. An annotated version appeared in: *To H.B. Curry: Essays on Combinatory Logic, Lambda Calculus and Formalism*, 479–490, Academic Press (1980), 1969.
- [33] R. Hu, N. Yoshida, and K. Honda. Session-based distributed programming in Java. In *22nd European Conference on Object-Oriented Programming (ECOOP)*, volume 5142 of *Lecture Notes in Computer Science*, pages 516–541. Springer, 2008.
- [34] T. B. L. Jespersen, P. Munksgaard, and K. F. Larsen. Session types for Rust. In *11th ACM SIGPLAN Workshop on Generic Programming (WGP)*, 2015.
- [35] L. Jia, H. Gommerstadt, and F. Pfenning. Monitors and blame assignment for higher-order session types. In *43rd ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL)*, pages 582–594, 2016.
- [36] R. Jung, D. Swasey, F. Sieczkowski, K. Svendsen, A. Turon, L. Birkedal, and D. Dreyer. Iris: Monoids and invariants as an orthogonal basis for concurrent reasoning. In *42nd ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL)*, pages 637–650. ACM, 2015.

- [37] N. Kobayashi and C. Laneve. Deadlock analysis of unbounded process networks. *Information and Computation*, 252:48–70, 2017.
- [38] J. Lange, N. Ng, B. Toninho, and N. Yoshida. Fencing off go: Liveness and safety for channel-based programming. In *44th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL)*, 2017. To appear.
- [39] K. R. M. Leino and P. Müller. A basis for verifying multi-threaded programs. In *18th European Symposium on Programming (ESOP)*, pages 378–393, 2009.
- [40] F. Militão, J. Aldrich, and L. Caires. Rely-guarantee protocols. In *28th European Conference on Object-Oriented Programming (ECOOP)*, volume 8586 of *Lecture Notes in Computer Science*, pages 334–359. Springer, 2014.
- [41] R. Milner. *Communicating and Mobile Systems: the  $\pi$ -Calculus*. Cambridge University Press, 1999.
- [42] MozillaResearch. The Rust programming language. <https://doc.rust-lang.org/stable/book>, November 2016.
- [43] K. Naden, R. Bocchino, J. Aldrich, and K. Bierhoff. A type system for borrowing permissions. In *39th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL)*, pages 557–570. ACM, 2012.
- [44] R. Neykova and N. Yoshida. Multiparty session actors. In *16th International Conference on Coordination Models and Languages (COORDINATION)*, volume 8459 of *Lecture Notes in Computer Science*, pages 131–146. Springer, 2014.
- [45] L. Nistor, J. Aldrich, S. Balzer, and H. Mehnert. Object propositions. In *19th International Symposium on Formal Methods (FM'14)*, *Lecture Notes in Computer Science*. Springer, 2014. To appear.
- [46] P. W. O’Hearn. Resources, concurrency and local reasoning. In *15th International Conference on Concurrency Theory (CONCUR)*, volume 3170 of *Lecture Notes in Computer Science*, pages 49–67. Springer, 2004.
- [47] J. A. Pérez, L. Caires, F. Pfenning, and B. Toninho. Linear logical relations and observational equivalences for session-based concurrency. *Information and Computation*, 239:254–302, 2014.
- [48] F. Pfenning. C0 language. <http://c0.typesafety.net>, 2010.
- [49] F. Pfenning and D. Griffith. Polarized substructural session types. In *18th International Conference on Foundations of Software Science and Computation Structures (FoSSaCS)*, volume 9034 of *Lecture Notes in Computer Science*, pages 3–22. Springer, 2015.
- [50] F. Pfenning, T. J. Cortina, and W. Lovas. Teaching imperative programming with contracts at the freshmen level. Unpublished note, 2011.
- [51] J. Reed. A judgmental deconstruction of modal logic. Unpublished manuscript, January 2009. URL <http://www.cs.cmu.edu/~jcreed/papers/jdml.pdf>.
- [52] D. Sangiorgi and D. Walker. *The  $\pi$ -Calculus - A Theory of Mobile Processes*. Cambridge University Press, 2001.
- [53] A. Scalas and N. Yoshida. Lightweight session programming in Scala. In *30th European Conference on Object-Oriented Programming (ECOOP)*, volume 56 of *LIPICs*, pages 21:1–21:28. Schloss Dagstuhl - Leibniz-Zentrum fuer Informatik, 2016.
- [54] J. Smans, B. Jacobs, and F. Piessens. Implicit dynamic frames: Combining dynamic frames and separation logic. In *23rd European Conference on Object-Oriented Programming (ECOOP’09)*, volume 5653 of *Lecture Notes in Computer Science*, pages 148–172. Springer, 2009.
- [55] F. Smith, D. Walker, and J. G. Morrisett. Alias types. In *9th European Symposium on Programming (ESOP)*, pages 366–381, 2000.

- [56] R. E. Strom and S. Yemini. Typestate: A programming language concept for enhancing software reliability. *IEEE Transactions on Software Engineering (TSE)*, 12(1):157–171, 1986.
- [57] B. Toninho. *A Logical Foundation for Session-based Concurrent Computation*. PhD thesis, Carnegie Mellon University and New University of Lisbon, 2015.
- [58] B. Toninho, L. Caires, and F. Pfenning. Higher-order processes, functions, and sessions: a monadic integration. In *22nd European Symposium on Programming (ESOP)*, volume 7792 of *Lecture Notes in Computer Science*, pages 350–369. Springer, 2013.
- [59] A. Turon, D. Dreyer, and L. Birkedal. Unifying refinement and hoare-style reasoning in a logic for higher-order concurrency. In *18th ACM SIGPLAN International Conference on Functional Programming (ICFP)*, pages 377–390. ACM, 2013.
- [60] V. Vafeiadis. Concurrent separation logic and operational semantics. *Electronic Notes in Theoretical Computer Science*, 276:335–351, 2011.
- [61] P. Wadler. Linear types can change the world! In *Working Conference on Programming Concepts and Methods*, 1990.
- [62] P. Wadler. Propositions as sessions. In *17th ACM SIGPLAN International Conference on Functional Programming (ICFP)*, pages 273–286. ACM, 2012.
- [63] D. Walker and K. Watkins. On regions and linear types. In *6th ACM SIGPLAN International Conference on Functional Programming (ICFP’06)*, pages 181–192. ACM, 2001.
- [64] K. Watkins, I. Cervesato, F. Pfenning, and D. Walker. A concurrent logical framework i: Judgments and properties. Technical Report CMU-CS-02-101, Computer Science Department, Carnegie Mellon University, 2002. Revised May 2003.
- [65] M. Willsey, R. Prabhu, and F. Pfenning. Design and implementation of Concurrent C0. In *Fourth International Workshop on Linearity*, June 2016.