# Binary Decision Diagrams Part 1

15-414 Bug Catching: Automated Program Verification and Testing

Sagar Chaki September 12, 2011

#### BDDs in a nutshell

Typically mean Reduced Ordered Binary Decision Diagrams (ROBDDs)

Canonical representation of Boolean formulas

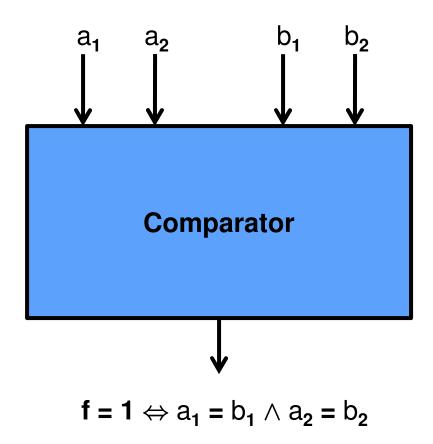
Often substantially more compact than a traditional normal form

Can be manipulated very efficiently

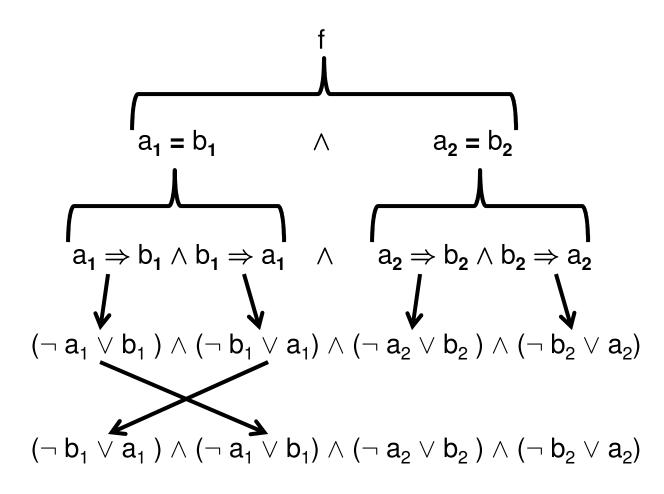
• Conjunction, Disjunction, Negation, Existential Quantification

R. E. Bryant. Graph-based algorithms for boolean function manipulation. *IEEE Transactions on Computers, C-35(8), 1986.* 

## **Running Example: Comparator**



## **Conjunctive Normal Form**



**Not Canonical** 

## **Truth Table (1)**

a <sub>1</sub>	b <sub>1</sub>	a <sub>2</sub>	b <sub>2</sub>	f
0	0	0	0	1
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

**Still Not Canonical** 

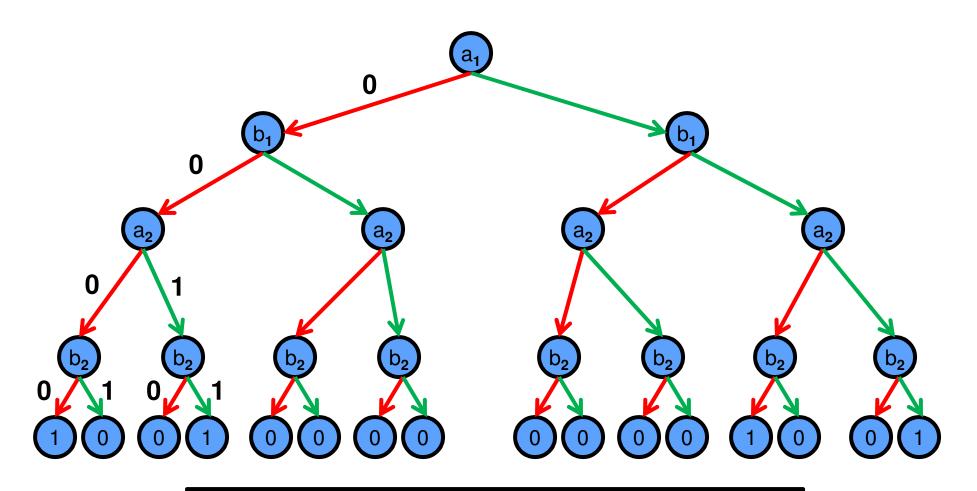
## Truth Table (2)

a <sub>1</sub>	a <sub>2</sub>	b <sub>1</sub>	b <sub>2</sub>	f
0	0	0	0	1
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	1
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

Canonical if you fix variable order.



# Representing a Truth Table using a Graph



**Binary Decision Tree (in this case ordered)** 



## **Binary Decision Tree: Formal Definition**

Balanced binary tree. Length of each path = # of variables

Leaf nodes labeled with either 0 or 1

Internal node v labeled with a Boolean variable var(v)

Every node on a path labeled with a different variable

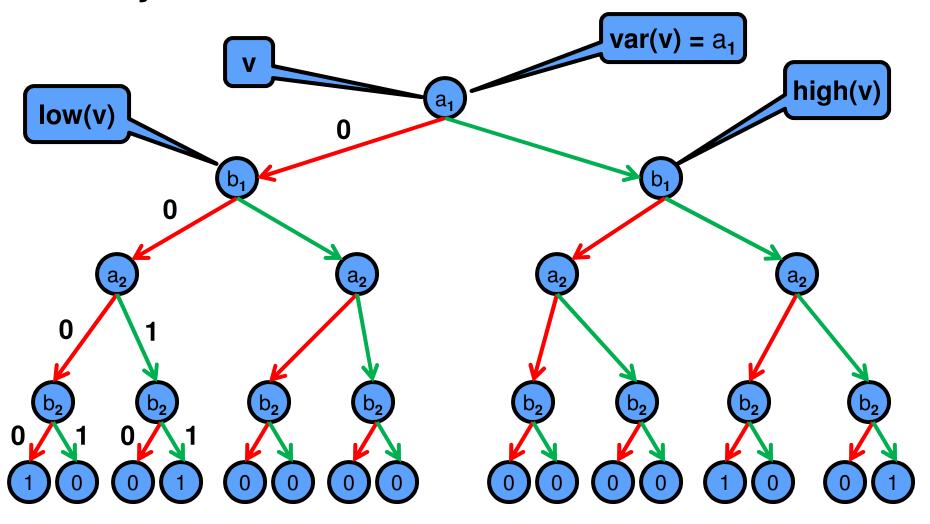
Internal node v has two children: low(v) and high(v)

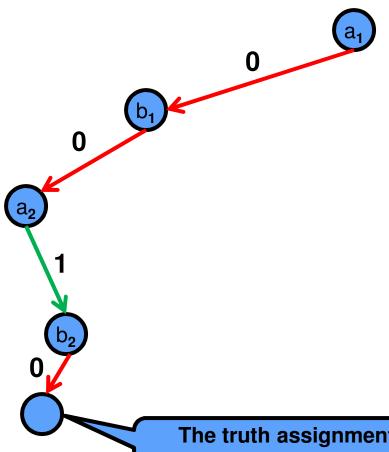
Each path corresponds to a (partial) truth assignment to variables

Assign 0 to var(v) if low(v) is in the path, and 1 if high(v) is in the path

Value of a leaf is determined by:

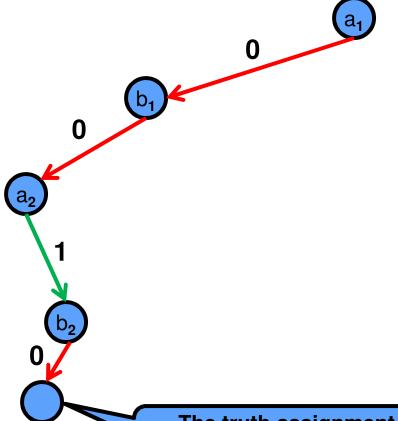
- Constructing the truth assignment for the path leading to it from the root
- Looking up the truth table with this truth assignment





The truth assignment corresponding to the path to this leaf is:

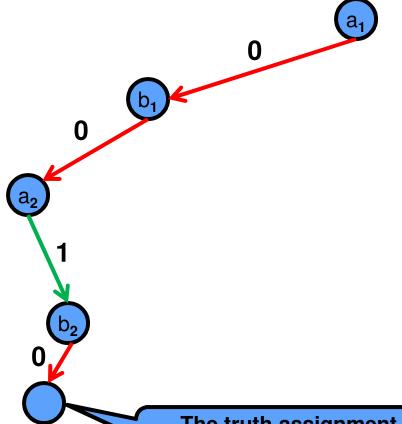
$$a_1 = ? b_1 = ? a_2 = ? b_2 = ?$$



a <sub>1</sub>	b <sub>1</sub>	a <sub>2</sub>	b <sub>2</sub>	f
0	0	0	0	1
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

The truth assignment corresponding to the path to this leaf is:

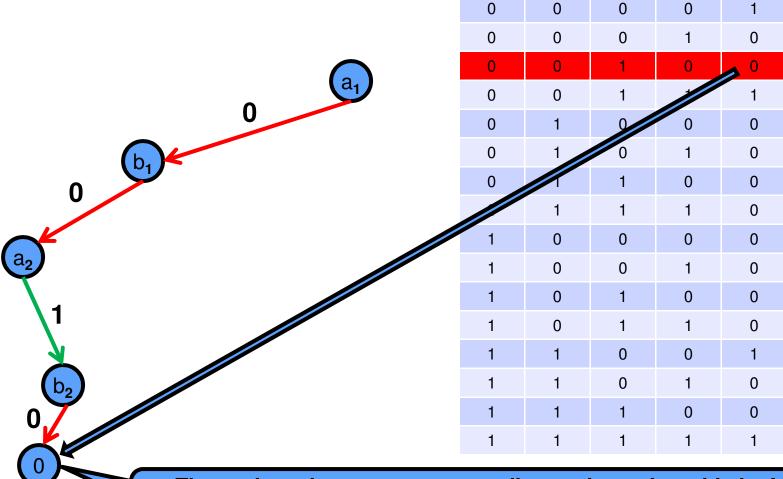
 $a_1 = 0 b_1 = 0 a_2 = 1 b_2 = 0$ 



a <sub>1</sub>	b <sub>1</sub>	a <sub>2</sub>	b <sub>2</sub>	f
0	0	0	0	1
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0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
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The truth assignment corresponding to the path to this leaf is:

 $a_1 = 0 b_1 = 0 a_2 = 1 b_2 = 0$ 



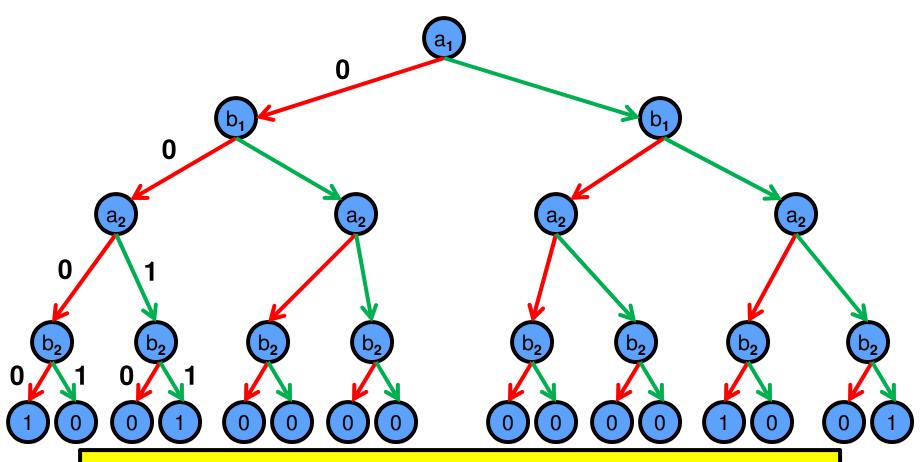
The truth assignment corresponding to the path to this leaf is:

 $b_1$ 

 $b_2$ 

 $a_1 = 0 b_1 = 0 a_2 = 1 b_2 = 0$ 

## **Binary Decision Tree (BDT)**



Canonical if you fix variable order (i.e., use ordered BDT)



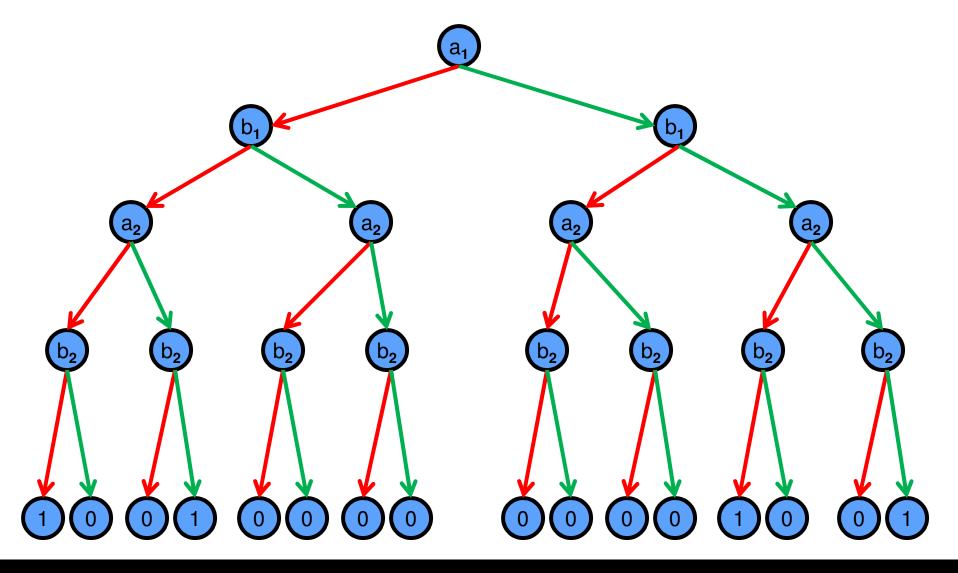
#### Reduced Ordered BDD

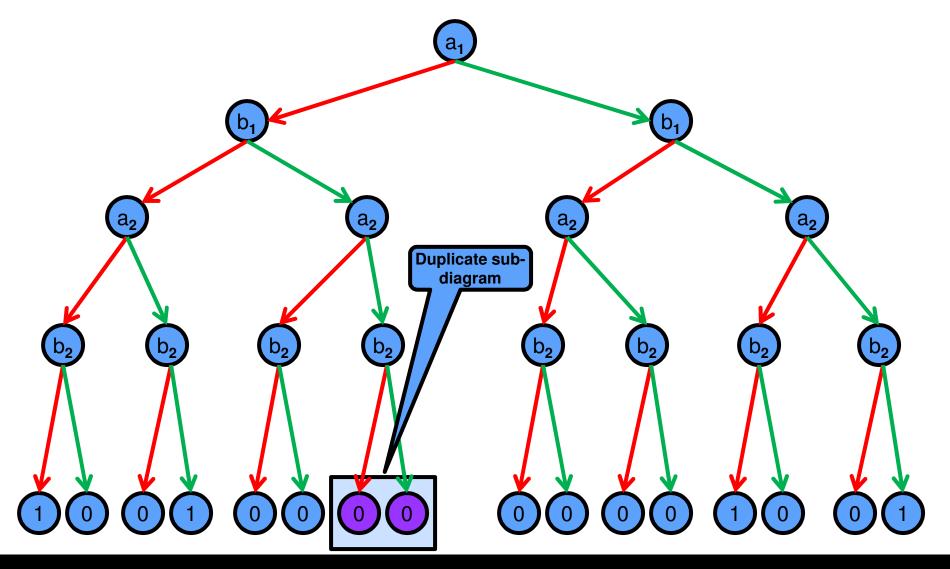
Conceptually, a ROBDD is obtained from an ordered BDT (OBDT) by eliminating redundant sub-diagrams and nodes

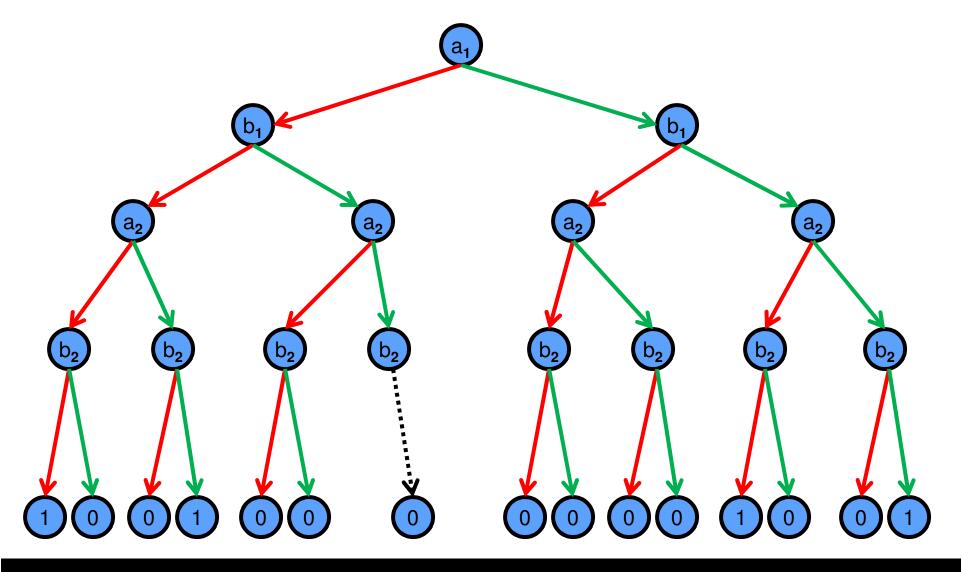
Start with OBDT and repeatedly apply the following two operations as long as possible:

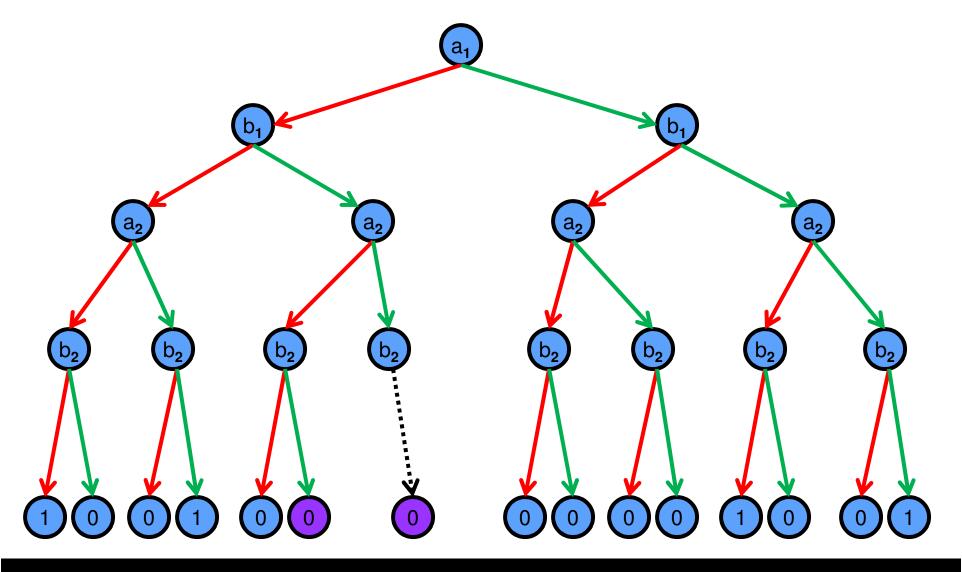
- 1. Eliminate duplicate sub-diagrams. Keep a single copy. Redirect edges into the eliminated duplicates into this single copy.
- 2. Eliminate redundant nodes. Whenever low(v) = high(v), remove v and redirect edges into v to low(v).
- Why does this terminate?

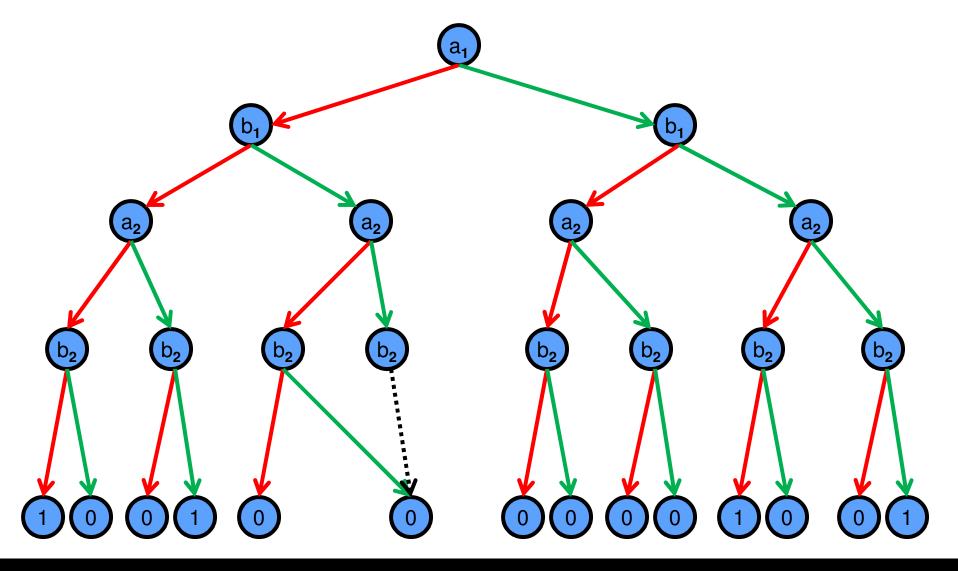
ROBDD is often exponentially smaller than the corresponding OBDT

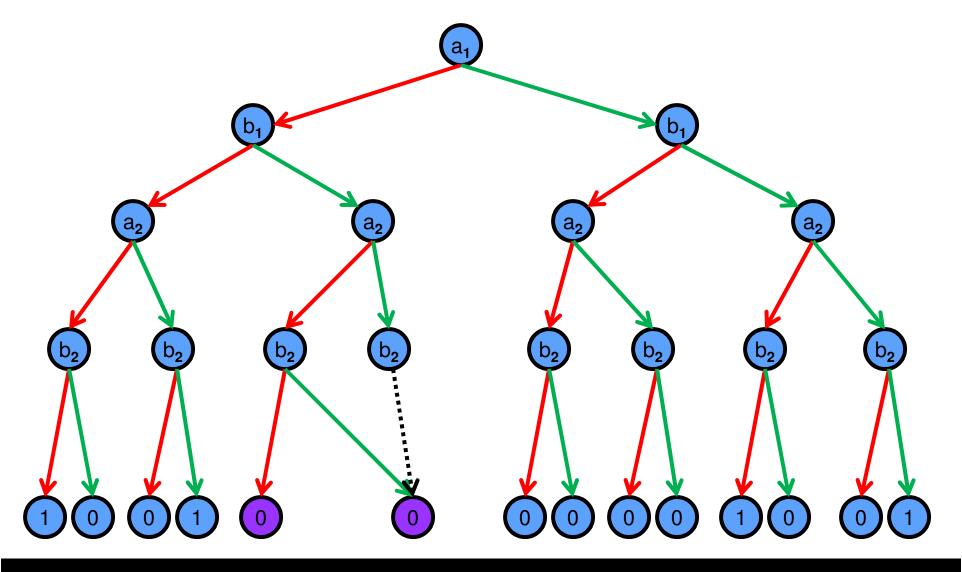


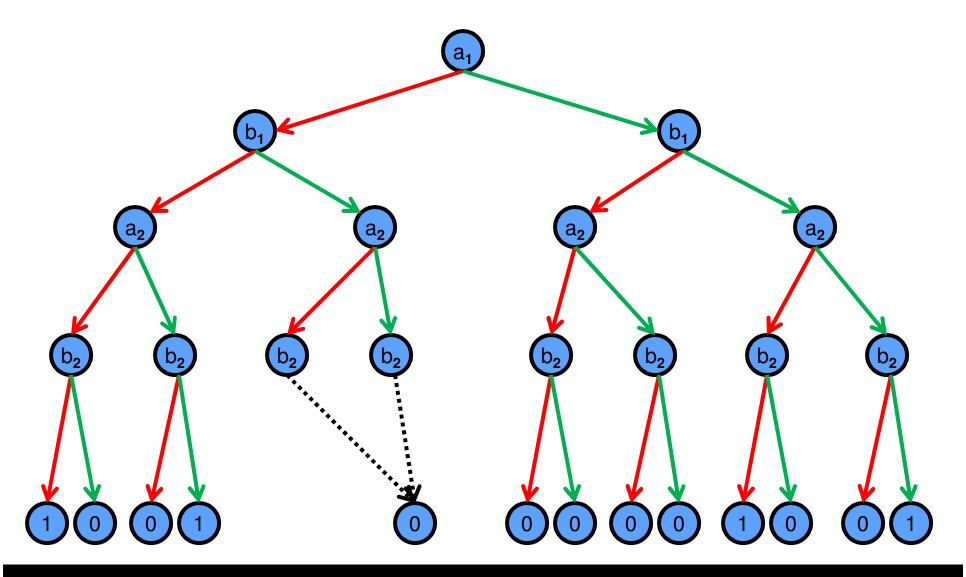


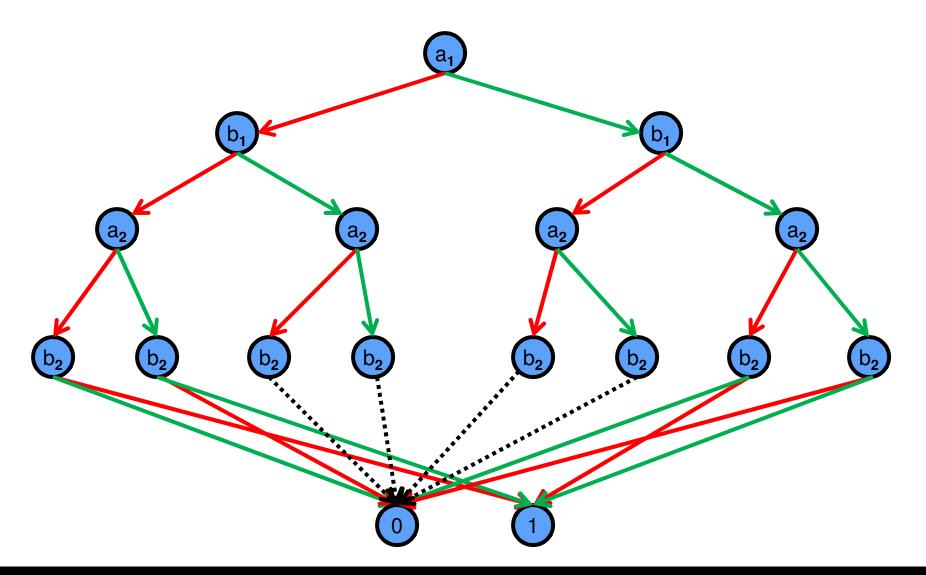


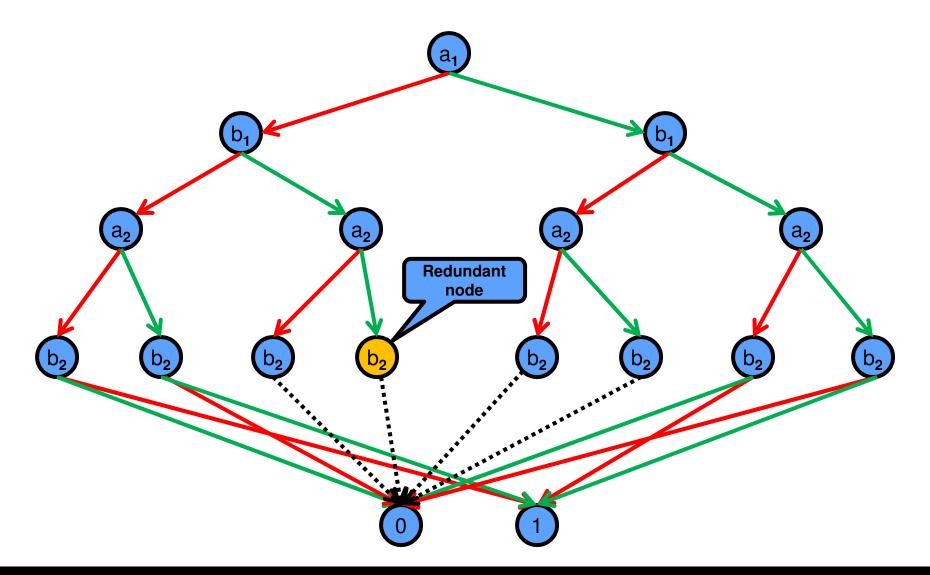


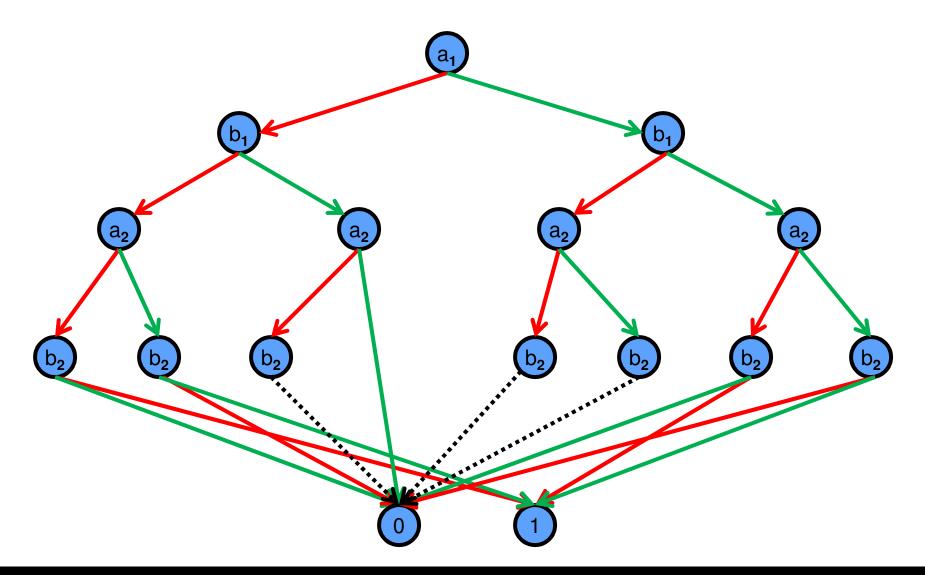


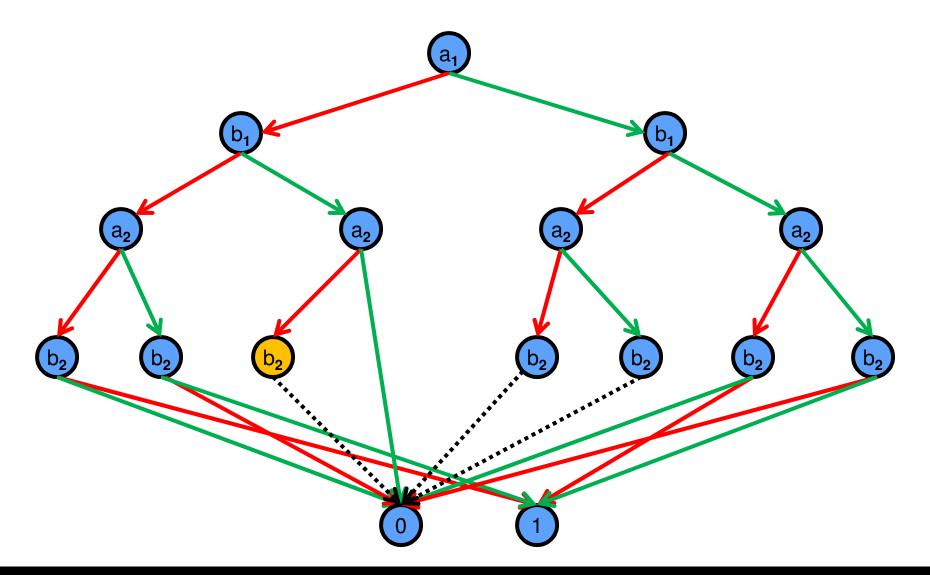


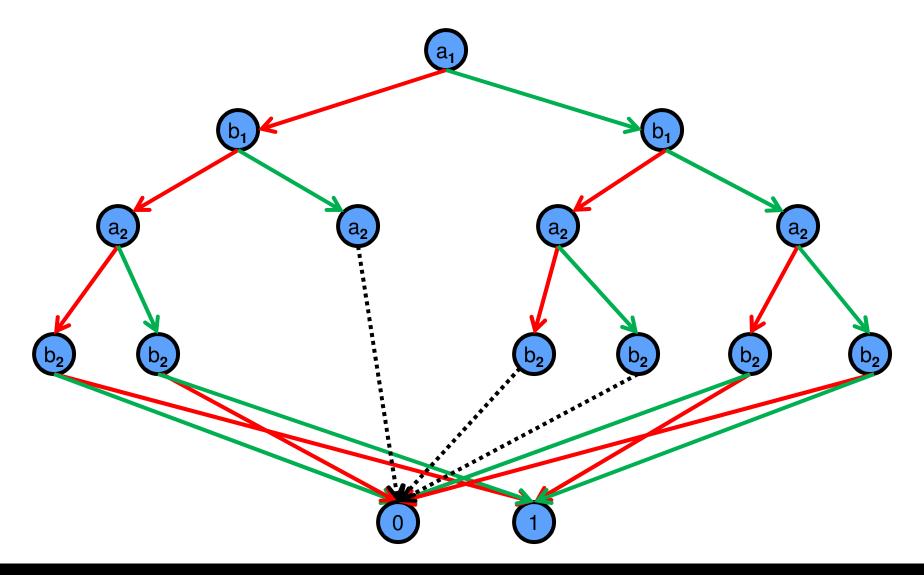


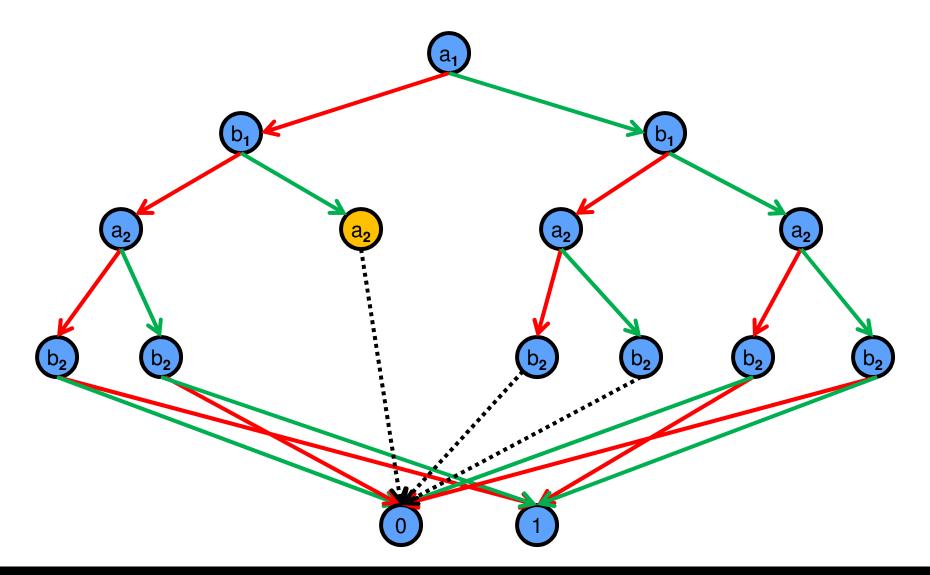


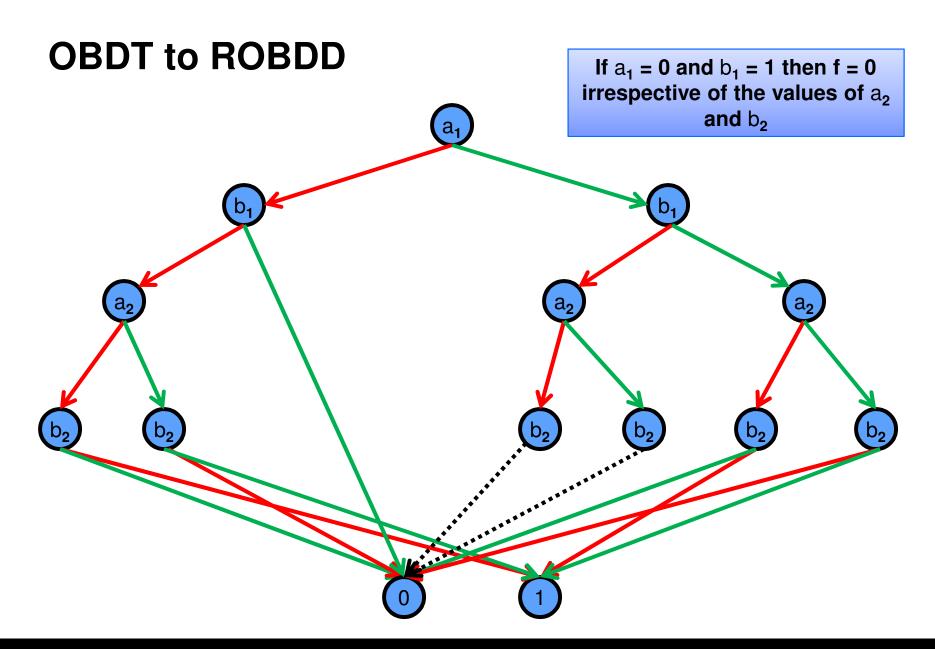


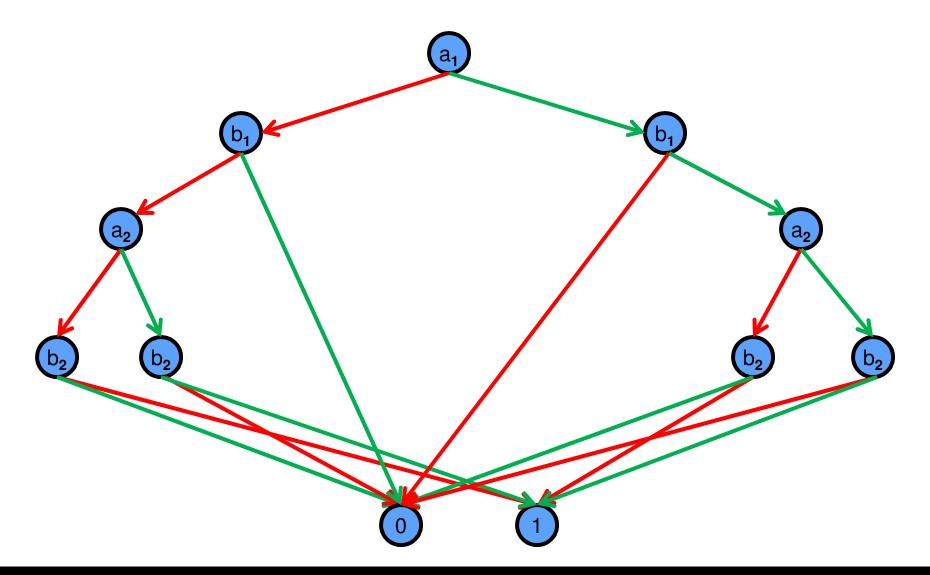


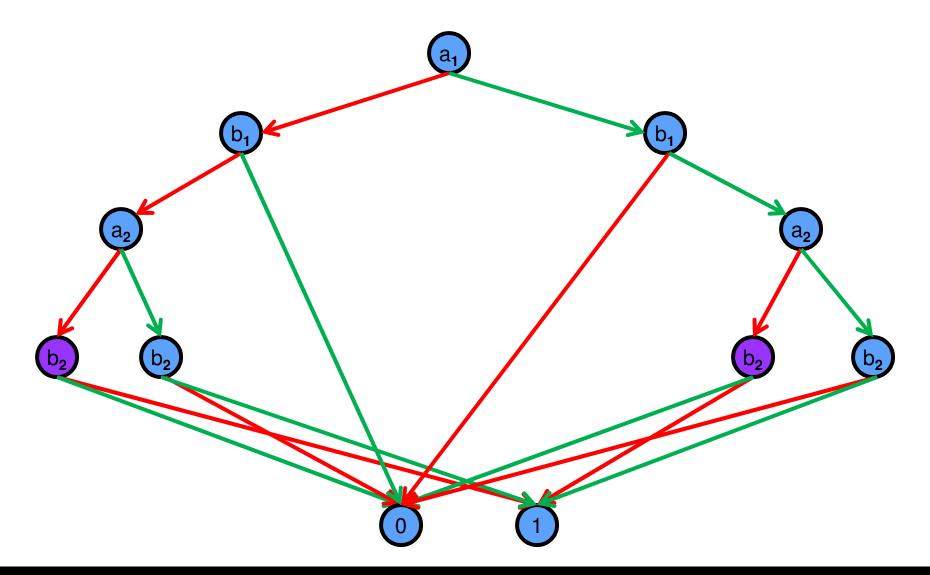


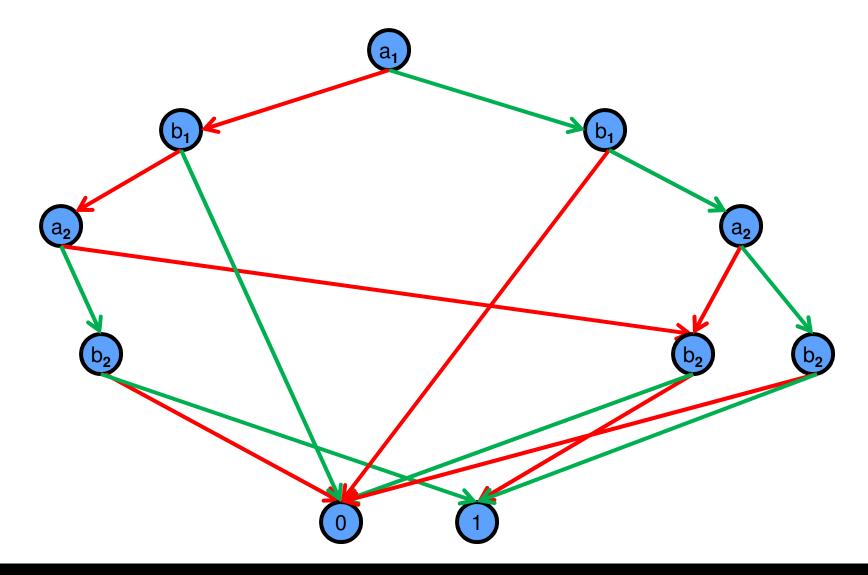


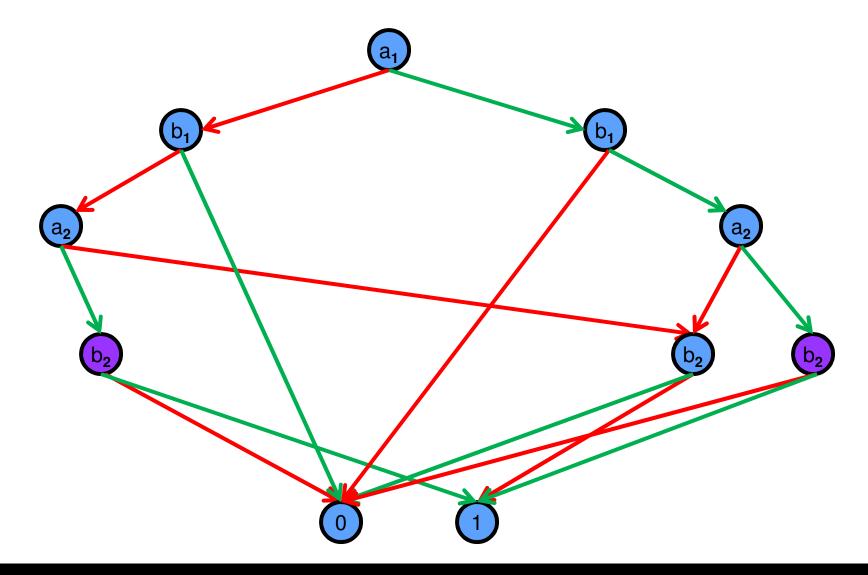


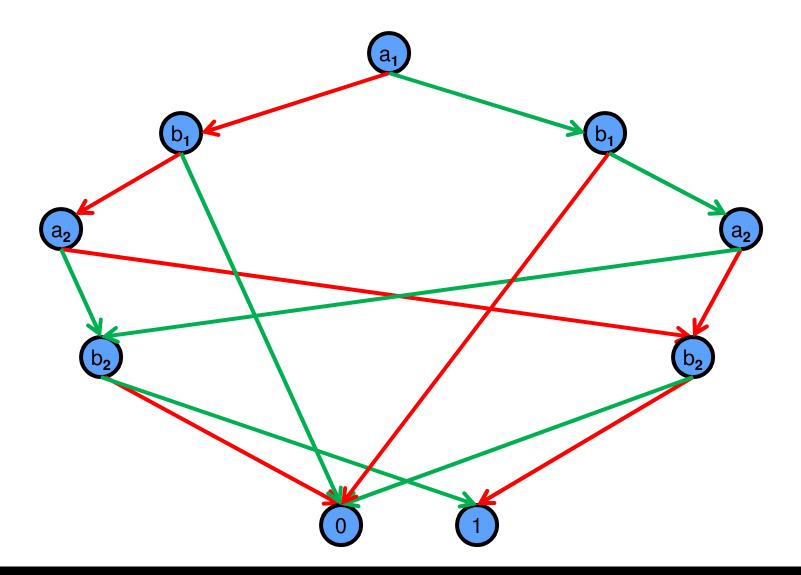


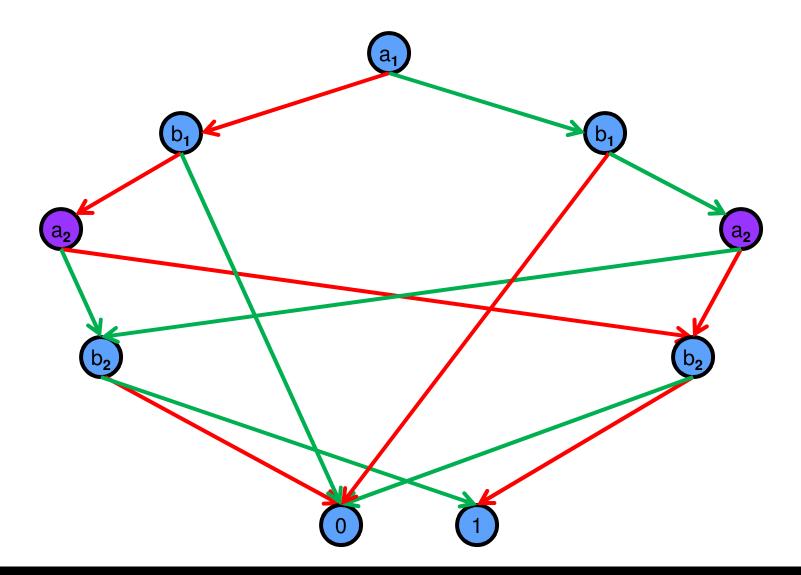


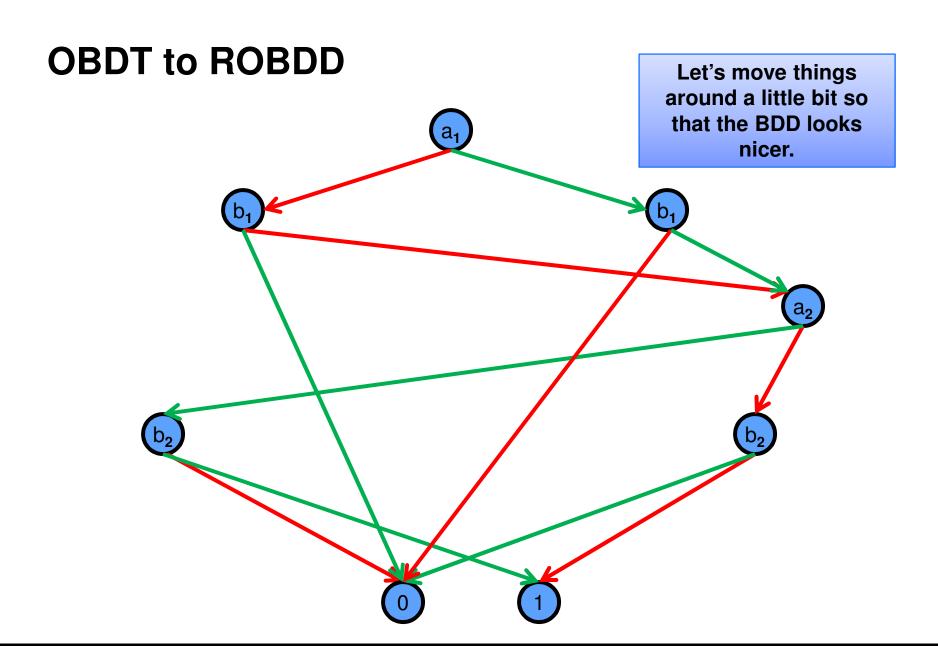




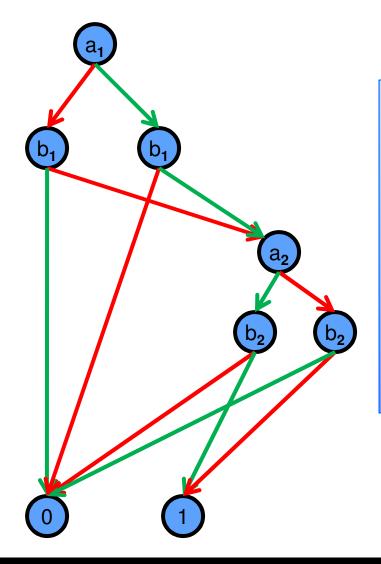








#### **OBDT to ROBDD**



Bryant gave a linear-time algorithm (called Reduce) to convert OBDT to ROBDD.

In practice, BDD packages don't use Reduce directly. They apply the two reductions on-the-fly as new BDDs are constructed from existing ones. Why?

BDDs are canonical representations of Boolean formulas

• 
$$f_1 = f_2 \Leftrightarrow ?$$

BDDs are canonical representations of Boolean formulas

- $f_1 = f_2 \Leftrightarrow BDD(f_1)$  and  $BDD(f_2)$  are isomorphic
- f is unsatisfiable ⇔ ?

#### BDDs are canonical representations of Boolean formulas

- $f_1 = f_2 \Leftrightarrow BDD(f_1)$  and  $BDD(f_2)$  are isomorphic
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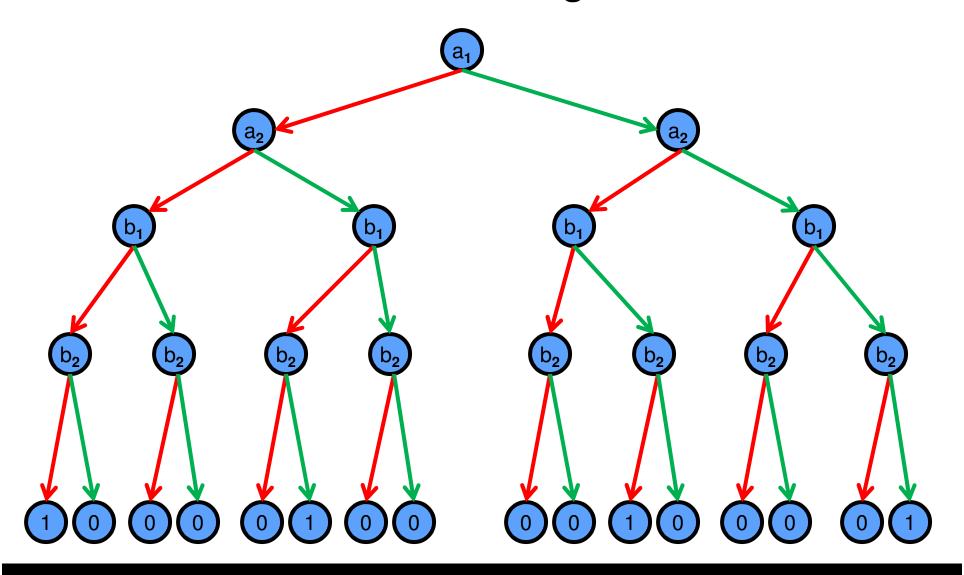
  ⇒ BDD(f) is the leaf node "1"
- BDD packages do these operations in constant time

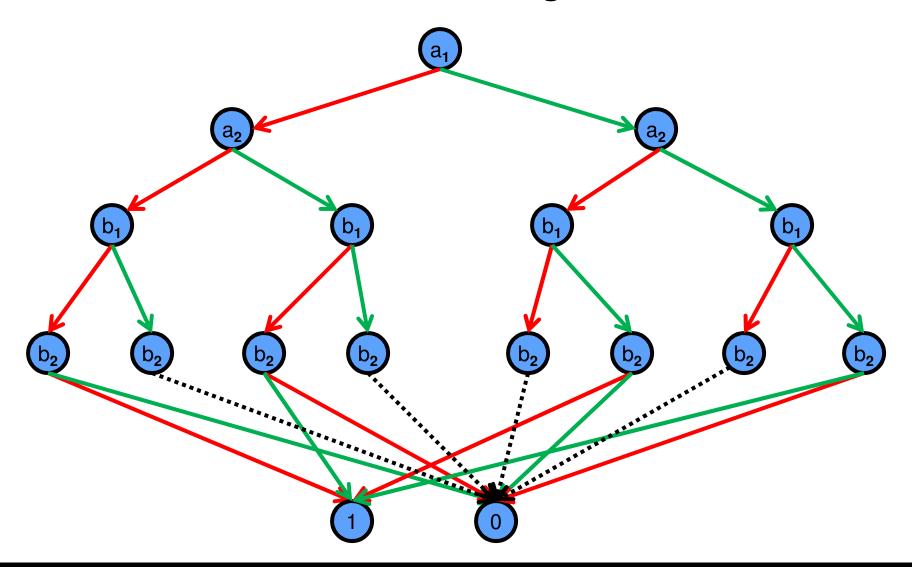
#### Logical operations can be performed efficiently on BDDs

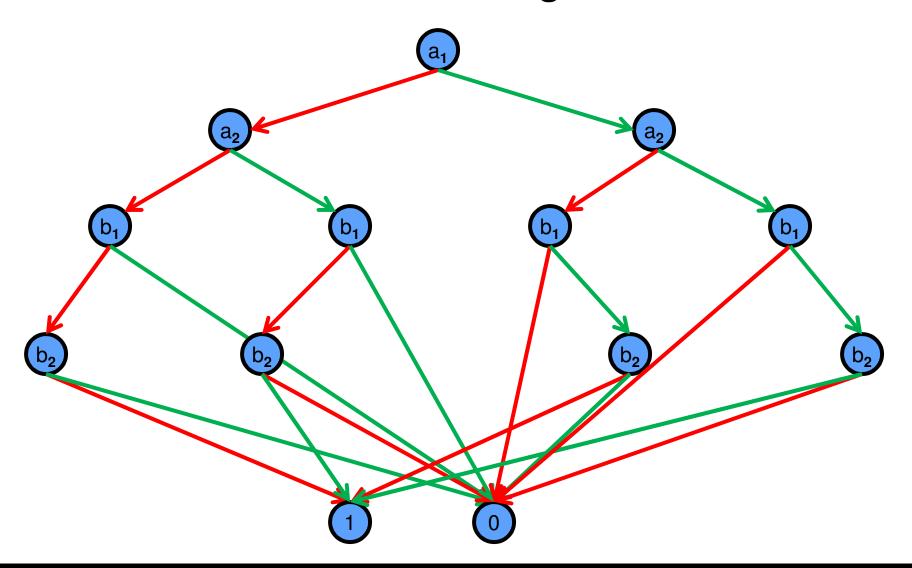
- Polynomial in argument size
- More details in next lecture

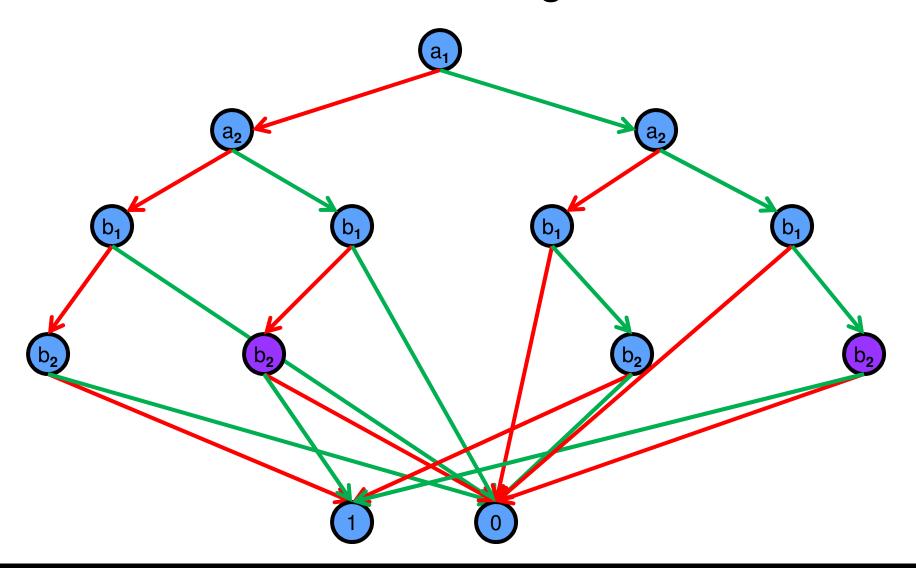
#### BDD size depends critically on the variable ordering

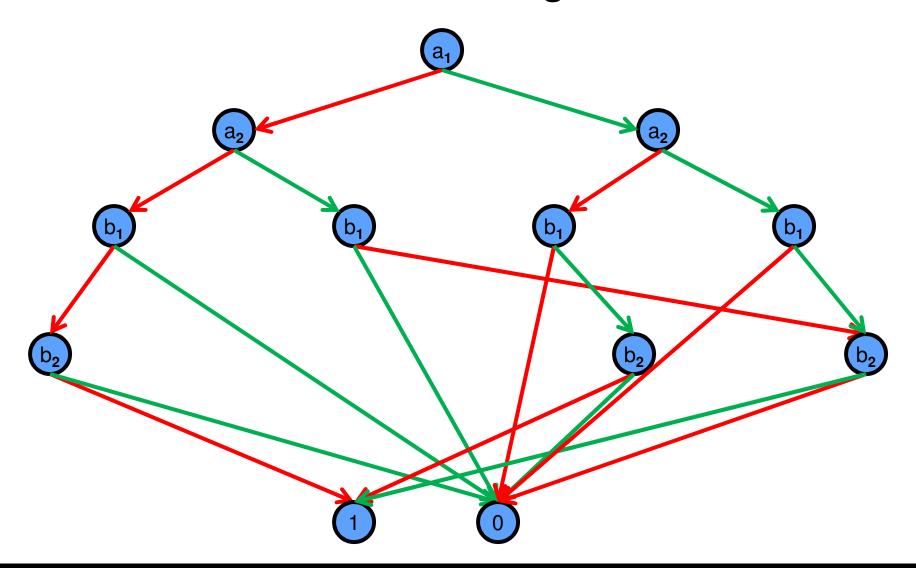
- Some formulas have exponentially large sizes for all ordering
- Others are polynomial for some ordering and exponential for others

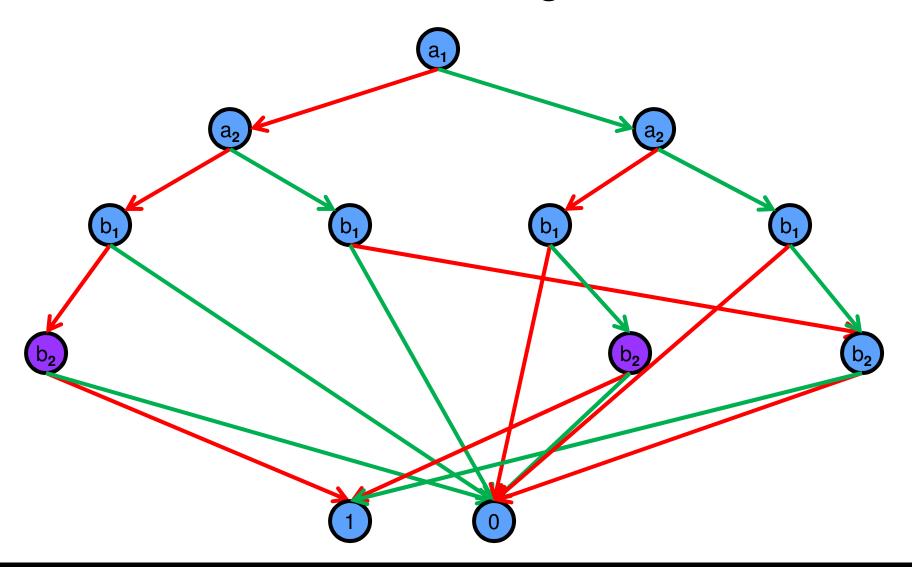




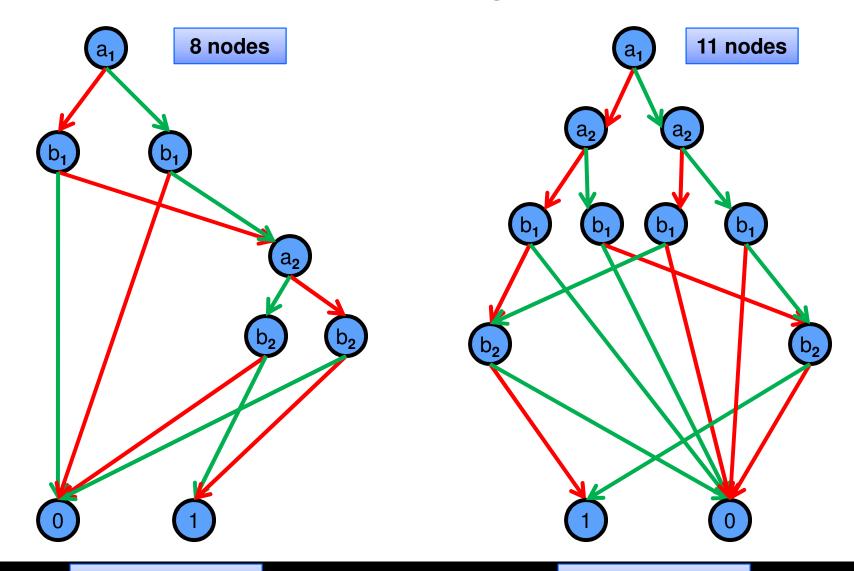


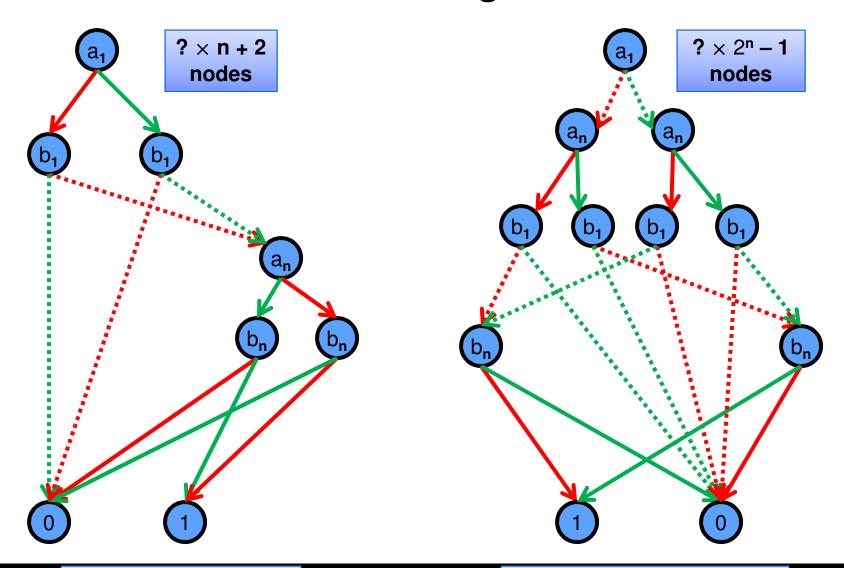


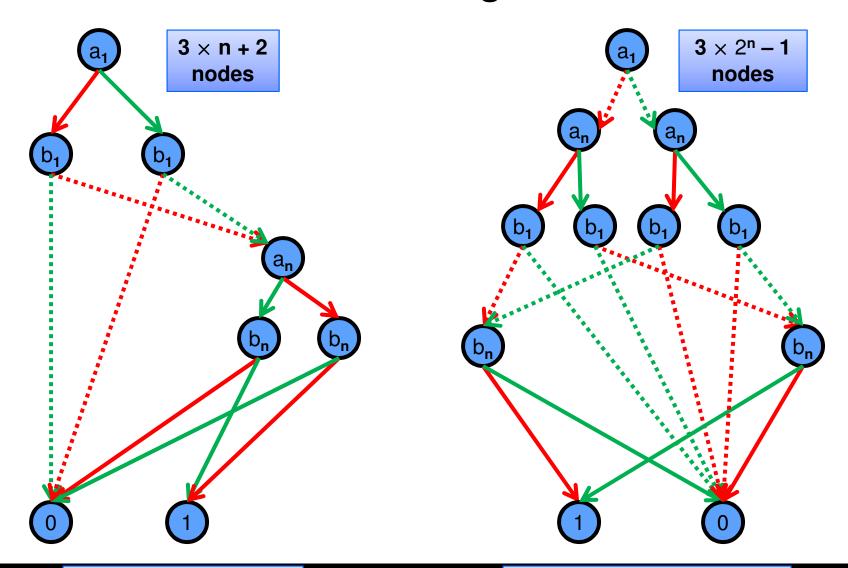




# **ROBDD** and variable ordering Let's move things around a little bit so that the BDD looks nicer.







#### **Next Class**

BDD recap

**BDD** operations

**BDD** applications

Next homework

See you then ...

#### **Questions?**

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