Class Announcements

- Please see the bboard post entitled "Re: P4 Handin Instructions?"
- How's that going, by the way?

Lock-free Programming

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Outline

Introduction

Lock-Free Linked List Insertion

Lock-Free Linked List Deletion

Read-Copy-Update Mutual Exclusion

Tradeoffs

Some real algorithms?

Introduction

- Suppose some madman says "We shouldn't use locks!"
- You know that this results (eventually!) in inconsistent data structures.
  - Loss of invariants within the data structure
  - Live pointers to dead memory
  - Live pointers to undead memory (Hey, my type changed! Stop poking there!)

Introduction
Locks Can Be Expensive

- Consider `XCHG` style locks which use
`while( xchg( &locked, LOCKED ) == LOCKED )`
as their core operation.
- We could spend a long time here waiting or yielding. . .
- This implies we'll have very high latency *on contention*. . .
- Locks *by definition* reduce parallelism.

Introduction
Locks Can Be Expensive

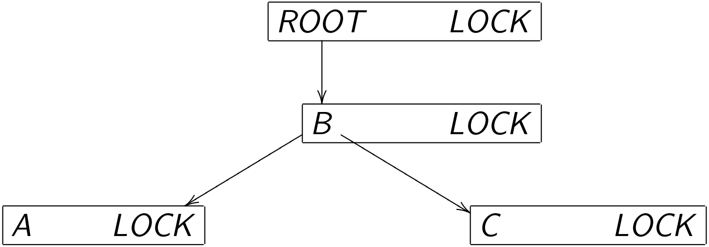
- That is, if N people are contending for a lock, $N - 1$ of them are `yield()`ing, just wasting time.
- It would be nice if they could all work at once ...
- ... being careful not to step on each other when there was actually a problem.

Introduction
Locks Can Be Expensive

- For a large data structure, we would *like* multiple *local* (independent) operations to be allowed concurrently.
- Can somewhat get this with a data structure full of locks
- . . . but order requirements mean that threads can still pile up while trying to get to their local site.

Introduction
Locks Can Be Expensive

- Instead of a lock around a tree, we could have a tree with locks:



- Here every time a thread decides to go down one branch, it gets out of roughly half of the others' ways.

Introduction

- But let's see what we can do without any locks at all.

Lock-Free Linked List Insertion

Lock-Free Linked List Node

- Node definition is simple:

label_t label
void* next

- When drawing, we'll use a shorthand:

label_t label = A
void* next = &B

$\Leftrightarrow$

A	*&B*

Insertion into a Linked List Without Locks
Insertion Code

```
insertAfter(after, newlabel) {
    //lockList();
    new = newNode(newlabel);
    prev = findLabel(after);
    new->next = prev->next;
    prev->next = new;
    //unlockList();
}
```

Insertion into a Linked List Without Locks
Good trace in 410 notation

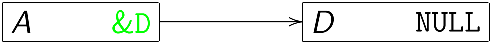
insertAfter(A,B)	insertAfter(A,C)
prev = &A	
B.next=prev->next	
prev->next=B	
	prev = &A
	C.next=prev->next
	prev->next=C

Insertion into a Linked List Without Locks
Race trace in 410 notation

insertAfter(A,B)	insertAfter(A,C)
prev = &A	
B.next = prev -> next	
	prev = &A
	C.next = prev -> next
prev -> next = B	prev -> next = C

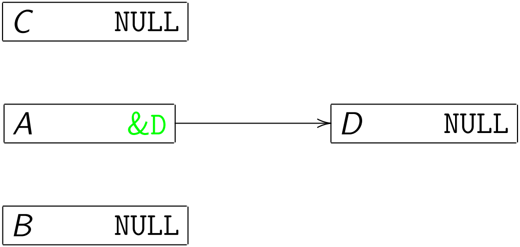
- Either of these assignments makes sense in isolation, but one of them will override the other!

Insertion into a Linked List Without Locks *Precondition*



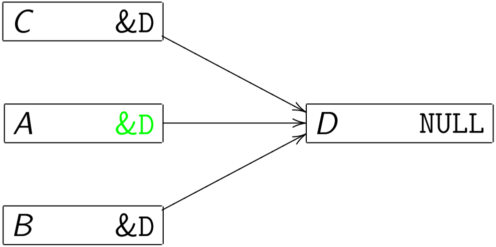
- One list, two items on it: *A* and *D*.

Insertion into a Linked List Without Locks
First step



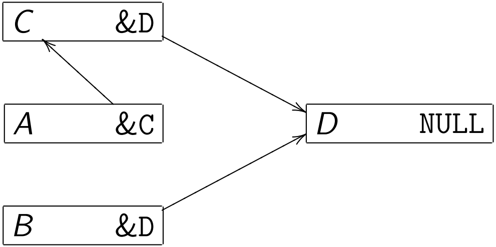
- Two threads get two nodes, *B* and *C* and want to insert.
- Thread 1: `new = newNode(B);`
- Thread 2: `new = newNode(C);`
- `prev = &A; /* in both */`

Insertion into a Linked List Without Locks
Second step



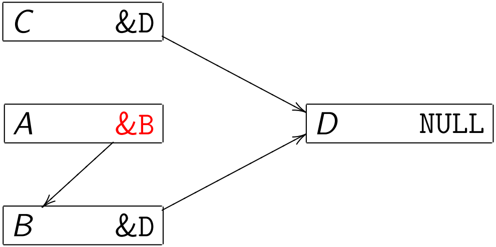
- Two threads point their respective nodes *C* and *B* into list at *D*
- `new->next = prev->next;`

Insertion into a Linked List Without Locks
One thread goes



- Suppose the thread owning C completes its assignment first.
  - `A.next = &C;`

Insertion into a Linked List Without Locks
And the other...



- And the other (owning *B*) completes second, overwriting ...
  - `A.next = &B;`
- Node *C* is unreachable!

Insertion into a Linked List Without Locks

- Our assignments were really supposed to be

insertAfter(A,B)	insertAfter(A,C)
ATOMICALLY	ATOMICALLY
if A->next == D	if A->next == D
A->next = B	A->next = C
else	else
do_retry = 1	do_retry = 1

- If we do that, one critical section will *safely* fail out and tell us to try again.
- How do we do this ATOMICALLY without locking?

Review of Atomic Primitives

- `XCHG (ptr, val)` atomically:

```
old_val = *ptr;
*ptr = val;
return old_val;
```

- `CAS (ptr, expect, new)` atomically:

```
old_val = *ptr;
if ( old_val == expect )
   *ptr = new;
return old_val;
```

- Note that `CAS` is no harder - it's a read and a write; the logic is free (it's on the chip).

Insertion into a Lock-free Linked List

- Our assignments were really supposed to be

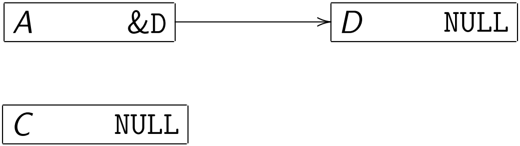
Thread 1	Thread 2
ATOMICALLY	ATOMICALLY
`if A->next == D`	`if A->next == D`
`A->next = B`	`A->next = C`
`else`	`else`
`do_retry = 1`	`do_retry = 1`

- This translates into

Thread 1	Thread 2
`CAS(&A->next,D,B)`	`CAS(&A->next,D,C)`

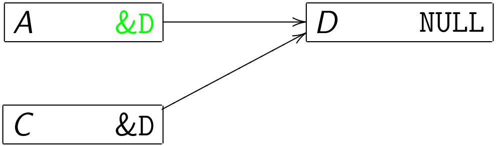
- CAS will let us do asignment when the data matches and will bail out when it doesn't!

Insertion into a Lock-free Linked List
Simple case, setup



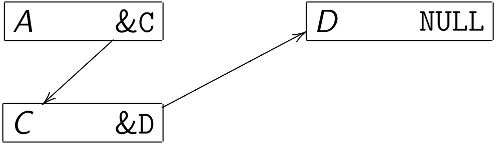
- Some thread constructs the bottom node C; wishes to place it between the two above, A and D.
- `new = newNode(C);`
- `prev = findLabel(A); /* == &A */`

Insertion into a Lock-free Linked List
Simple case, first step



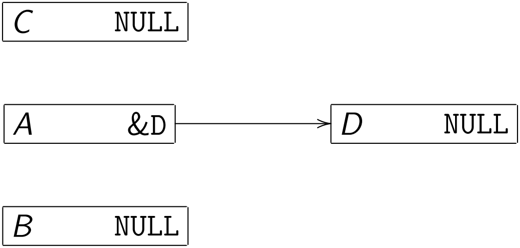
- Thread points *C* node's next into list at *D*.
- `C.next = A.next;`

Insertion into a Lock-free Linked List
Simple case, second step



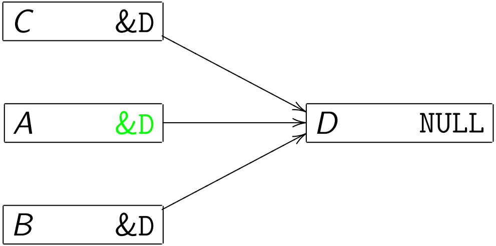
- `CAS(&A.next, &D, &C);`

Insertion into a Lock-free Linked List
Race case, setup



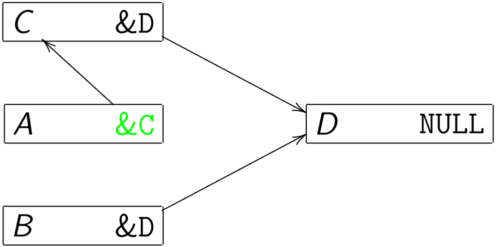
- Two threads get their respective nodes *B* and *C*.
- `new = newNode(...);`
- `prev = &A;`

Insertion into a Lock-free Linked List
Race case, first step



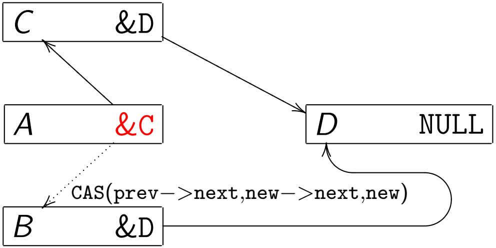
- Both set their new node's next pointer.
- `new->next = prev->next;`

Insertion into a Lock-free Linked List
Race case, first thread



- Thread C goes first ...
- `CAS(&prev->next, new->next, new)`

Insertion into a Lock-free Linked List
Race case, second thread



- And the other (owning B)...
- `CAS(&prev->next, new->next, new)`
- Fails since `prev->next == C` and `new->next == D`.
- So this thread tries again.

Insertion into a Lock-free Linked List

- Rewrite the insertion code to be `insertAfter(after, newlabel) {`

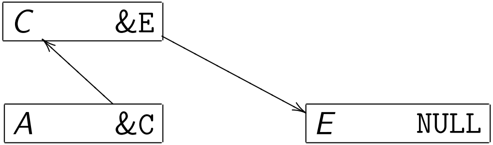
```
    new = newNode(newlabel);
    do {
        prev = findLabel(after);
        new->next = prev->next;
    } while
        ( CAS(&prev->next, new->next, new)
                != new->next);
}
```

That's great!

- It works!
  - No locks!
  - Can simultaneously scan and modify the list!
  - Can simultaneously *modify* and modify the list!
- Are we done?
  - Most data structures need to support deletion as well ...

Deletion is easy?

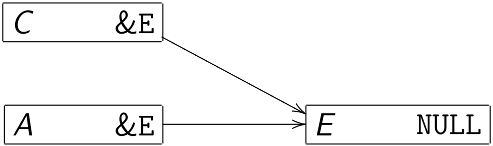
- Suppose we have



- And want to get rid of C.
- So `CAS(&A.next, &C, &E)`

Deletion is easy?

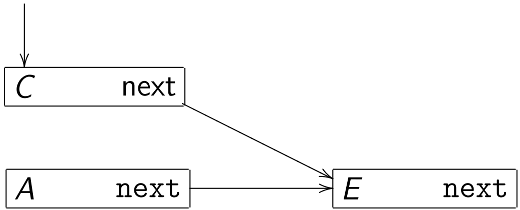
- Now we have



- Great, looks like deletion to me!

Deletion is easy?
Continued

- But imagine there was another thread accessing C (say, scanning the list).



- We don't know when that thread is done with C!
- So we can never `free(C);`

Deletion is easy?
What's to be done?

- Deletion turns out to be connected with the infamous "ABA problem."
- We need *some* way to reclaim that memory for reuse..
- (Some implementations cheat and assume as stop-the-world garbage collector.)
- Doing this honestly is remarkably tricky!

ABA Problem

- A problem of confused identity

global = malloc(sizeof(Foo))	
local_1 = global	local_2 = global
global = NULL	
free(local_1)	
global = malloc(sizeof(Foo))	
	/* Validity check */ if (global == local_2) global->foo_baz = ...

- Even though local_2 and global might share the same value, they don't *really* mean the same thing.

ABA Problem

- We begin with an innocent linked list:

- Where `head` is a a global pointer to the list.
- We're just going to do operations at the head – treating the list like a stack.

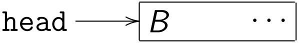
ABA Problem
Pop

- We begin with a linked list:

- Removing the head looks like

`lhead = head`	`/* == &A */`
`lnext = lhead->next`	`/* == &B */`
`CAS(head, lhead, lnext);`	

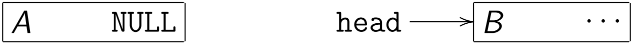
- If the `CAS` is successful, we are done, and the list is



- If not, start over.

ABA Problem
Push

- We begin with a linked list and private item



- Inserting at the head looks like

`lhead = head`	`/* == &B */`
`A.next = lhead`	`/* A points at B */`
`CAS(head, lhead, &A);`	

- If the `CAS` is successful, we are done, and the list is

- If not, start over.

ABA Problem
And now it breaks!

Here's a 30,000 foot look at how this is going to break.

Process 1	Process 2
P	Pop
o	Use memory
p	Push
BANG!	

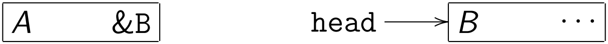
- In words: An extremely, agonizingly slow pop is racing against a pop and a push, with some scribbling in the middle.
- All operations are going to be aimed at the same node, A.
- The end is catastrophe.

ABA Problem

- The first thread gets one instruction into its pop, while
- The second thread completes its pop operation:

`h1 = head`	`h2 = head`	`== &A`
	`n2 = h2->next`	`== &B`
	`CAS(head, h2, n2)`	Success!

- The world now looks like



ABA Problem

- Now the faster thread is going to do something to the node it just popped, and then try to push it back on.

[A &B]   head → [B ···]

	`A.next = NULL;`	Use memory
`n1 = h1->next`		`== NULL`
	`h2 = head;`	`== &B`
	`A.next = h2;`	`== &B`
	`CAS(head, h2, &A)`	Success!
`CAS(head, h1, n1)`		Suc... hm!

- The list is now corrupted and looks like

head → [A NULL]   [B ···]

ABA Problem

- The left thread missed its chance to be notifed of having stale data.
  - Notice that the choice of writing NULL was arbitrary.
  - In particular, we might have instead done a much larger series of operations.
  - All that matters is that *A* ended up back on the list head when Thread 1 was CAS-ing.
- In punishment, the datastructure is now broken!

Fixing ABA

- It turns out that we need a more sophisticated delete (and maybe insert and lookup!) function. Look at [Fomitchev and Ruppert(2004)] or [Michael(2002a)] (or others) for more details.
- Generation counters are a simple way to solve ABA
    - Let's replace all pointers with

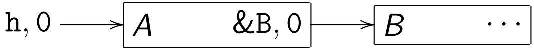
```
struct {
     void * p; /* Pointer */
     unsigned int c; /* Counter */
};
```

- This will allow a "reasonably large" number of pointer updates before we have to worry.

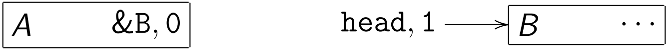
Fixing ABA

- Imagine that instead of `CAS` we had `CAS2`, which operates on two words at once:
  `CAS2(ptr[2], expect[2], new[2])` atomically:
    - `if (ptr[0] != expect[0] || ptr[1] != expect[1])`
        - `return {ptr[0], ptr[1]};`
    - `else`
        - `ptr[0] = new[0]; ptr[1]= new[1];`
        - `return { expect[0], expect[1] };`
- `CAS2` looks more expensive than `CAS`.
    - Two reads, two writes.
    - With luck, it's two cache lines, not four.
    - May not be quite twice as hard as `CAS`.

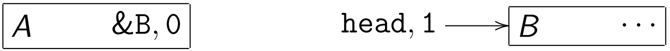
Fixing ABA



h1 = head.p	h2 = head.p	== &A
	n2 = h2->next.p	== &B
	c2 = head.c	== 0
	CAS2(head,{h2,c2},{n2,c2+1})	Success!



Fixing ABA



`n1 = h1->next.p`	
`c1 = head.c`	
	`h2 = head.p;`
	`c2 = head.c;`
	`A.next.p = h2;`
	`A.next.c = 0;`
	`CAS2(head,{h2,c2},{&A,c2+1})`

Fixing ABA

Now when the left process does `CAS2(head,{h1,c1},{n1,c1+1})`, it's going to be expecting `head`'s generation counter to be at value `c1`, or `1`. Since it is now at 2, the `CAS2` will fail.

Read-Copy-Update Mutual Exclusion
Preliminaries

- The ABA problems would all be solved if we could force everybody who might have read what is now a stale pointer to complete.
- Phrased slightly differently, we need to separate the *update* phase from the *reclaim* phase.
- And ensure that no readers hold a critical section that might see the update *and* reclaim phases.
    - Seeing one or the other is OK!

Read-Copy-Update Mutual Exclusion

- Read-Copy-Update (RCU, [Wikipedia(2006a), McKenney(2003)]) uses techniques from lock-free programming.
- Is used in several OSes, including Linux.
- It's a bit more complicated than the examples given here and not truly lock-free, but certainly interesting.

Read-Copy-Update Mutual Exclusion
Preliminaries

- Looks like a reader-writer lock from $30,000$ ft.
- Key observations:
  - Many more readers than writers.
  - Readers frequently can avoid blocking inside the critical section.
  - Readers want to see a consistent datastructure.
  - The ABA problems would all be solved if we could force everybody who might have read what is now a stale pointer to complete.

Read-Copy-Update Mutual Exclusion Preliminaries

- Many more readers than writers.
    - So we should make sure that the readers don't have to do much.
    - Kind of like a rwlock.
- Readers frequently can avoid blocking inside the critical section.
    - We'll see why this is important in a bit.
- Readers want to see a consistent datastructure.
    - Not all consistency guarantees need to be kept, but, for example, we want to avoid use-after-free and the possibility of faulting.
    - But it might be the case that we let `node->next->prev != node` as readers only use these pointers to traverse.

Read-Copy-Update Mutual Exclusion
Preliminaries

- Disclaimer: function names have been changed from, *e.g.*, the Linux implementation, to make the meanings more clear.
- Disclaimer 2: RCU comes in many flavors - the one here is a small toy model but works on real hardware (like Pebbles).

Read-Copy-Update Mutual Exclusion
API

- Reader critical section functions.
  - `void rcu_read_lock(void);`
  - `void rcu_read_unlock(void);`
  - Note the absence of parameters (how odd!).
- Accessor functions:
  - `void * rcu_fetch(void *);` is used to fetch a pointer from an RCU protected data structure.
  - `void * rcu_assign(void *, void *);` is used to assign a new value to an RCU protected pointer.
- Synchronization points:
  - `void rcu_synchronize(void);` is used once a writer is finished to signal that updates are complete.

Read-Copy-Update Mutual Exclusion *Reader's View*

- Suppose we have a global list, called `list`, that we want to read under RCU.
- The code for iteration looks like

```
rcu_read_lock();
list_head_t *llist = rcu_fetch(list);
list_node_t *node = rcu_fetch(llist->head);
while(node != NULL) {
   ... /* Do something reader-like */
   node = rcu_fetch(node->next);
}
rcu_read_unlock();
```

Read-Copy-Update Mutual Exclusion Writer's View

- Suppose we want to delete the head of the same global list, `list`.
- We need to give it a writer exclusion mutex, `list_wlock`.

```
void replace_head_of_list() {
    list_node_t *head;
    mutex_lock(&list_wlock);
       head = list->head;
       list_node_t *next = head->next;
       rcu_assign(list, next);
       rcu_synchronize();
    mutex_unlock(&list_wlock);
    free(head); /* Reclaim phase */
}
```

Read-Copy-Update Mutual Exclusion
Hey now!

- Readers can run alongside writers! There's no mechanism in the reader to serialize against the writer! See:

CPU 1 (reader)	CPU 2 (writer)
`rcu_read_lock();`	`mutex_lock(...);`
`llist = rcu_fetch(list);`	`...`
	`rcu_assign(list, new);`
	`rcu_synchronize();`
`rcu_fetch(llist->head);`	

Read-Copy-Update Mutual Exclusion
Guts

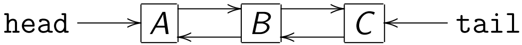
- All right, now we actually need to talk about how this works.
- `rcu_read_lock()` simply disables the local CPU's preemptive scheduler.
  - This is where the requiement that readers not block comes from.
- `rcu_assign()` inserts a write memory barrier ("write fence") to force all writes in the out-of-order buffers to be made visible *before* it does the assignment requested.
- `rcu_fetch()` is just a dereference on most architectures.

Read-Copy-Update Mutual Exclusion
Guts

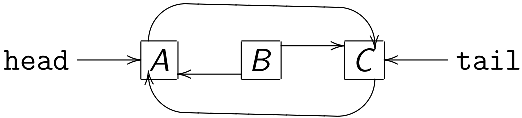
- Given all of this, what does `rcu_synchronize()` do?
- It ensures that every CPU undergoes a context switch!
    - Many ways to do this, but the simplest is to simply ensure that the thread calling synchronize gets run on every CPU before the synchronize returns.
- Because readers are non-preemptible, this will force all critical sections that began before the synchronize to complete before the writer can enter reclaim phase.
- That enables safe reclaim and as a side-effect solves the ABA problem for us!

Read-Copy-Update Mutual Exclusion
Pictures

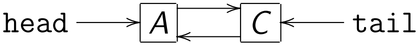
- Let's again take a linked list, this time a doubly linked one.



- Now suppose the writer acquires the write lock and updates to delete B:

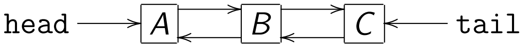


- Now the writer synchronizes, forcing all readers with references to B out of the list. Only then can B be reclaimed!

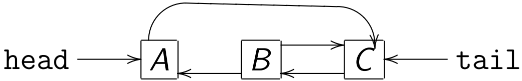


Read-Copy-Update Mutual Exclusion *Pictures*

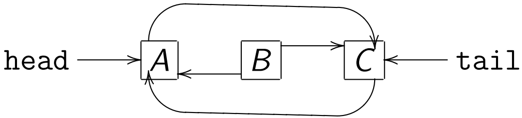
- Looking at that again, from the reader's side now. Originally



- The writer first sets it to



- And then



Read-Copy-Update Mutual Exclusion
Pictures

- The writer forced memory consistency (fencing) between each update.
- So each reader's dereference occurred *entirely before* or *entirely after* each write.
- So the reader's traversal in either direction is entirely consistent!
- Though moving back and forth might expose the writer's action.
- But it's OK, because we'll just see a disconnected node.
- It's not *gone* yet, just disconnected.
- It won't be reclaimed until we drop our critical section.

Tradeoffs
Write Your Own?

- It's *extremely hard* to roll your own lockfree algorithm.
- But moreover, it's *almost impossible* to debug one.
- Thus all the papers are long not because the algorithms are hard, . . .
- . . . but because they prove the correctness of the algorithm so they can skip the debugging step!

Tradeoffs
Vs. Locking.

- Most lock-free algorithms increase the number of atomic operations, compared to the lockful variants.
- Thus we starve processors for bus activity on Intel-like bus-locking systems.
- On systems with cache coherency protocols, we might livelock with no processor able to make progress due to cacheline stealing and high transit times.
    - Nobody can get all the cachelines to execute an instruction before a request comes in and and steals one of the ones they had.

Tradeoffs
Vs. Locking.

- Interestingly, RCU tends to decrease the number of atomic operations.
  - It can because it requires readers to be non-blocking and can interact with the scheduler.
- RCU requires the ability to force a thread to run on every CPU or at least observe when every CPU has context switched.
  - Difficult to use RCU in userland!
- RCU still suffers a slowdown from cache line shuffling, but will make progress due to there being only one writer.

Some real algorithms?

- [Michael(2002a)] specifies a `CAS`-based lock-free list-based sets and hash tables using a technique called SMR to solve ABA and allow reuse of memory.
    - Like RCU, SMR actually solves ABA as a side effect of safely reclaiming memory. Instead of blocking the writer until everybody leaves a critical section, it can efficiently scan to see if threads are interested in a particular chunk of memory.
    - Their performance figures are worth looking at. Summary: fine-grained locks (lock per node) show linear-time increase with # threads, their algorithm shows essentially constant time.

Conclusion

- Lock-free datastructures are extremely cool.
- Understanding them
  - Necessitates understanding of "modern" ("clever") hardware.
    - This is probably good for one's soul anyway.
    - Hardware is only going to get more "clever."
  - Leads to real-world tools like RCU.
  - Gives a topic for conversation at parties.
- Lock-free algorithms proper have their place, but that place is somewhat small.
  - Generally more complex than standard lockful algorithms.
  - Much harder ("impossible?") to debug.
  - Usually used only when there is no other option.

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Full fledged deletion & reclaim

- Even though we might be able to solve ABA, it still doesn't solve memory reclaim!
- Imagine that instead of being reclaimed by the list, the deleted node before had been reclaimed by something else...
    - A different list
    - A tree
    - For use as a thread control block

Full fledged deletion & reclaim

- What if we looked at ABA differently ...
- It only matters if there is the possibility of confusion.
- In particular, might demonstrate strong interest in things that might confuse me
  - Hazard Pointers ("Safe Memory Reclaimation" or just "SMR") [Michael(2002b)] and [Michael(2004)]
  - Wait-free reference counters [Sundell(2005)]
- These are ways of asking "If I, Thread 189236, were to put something here, would anybody be confused?"
- This solves ABA, but really as a side effect: it lets us reclaim address space (and therefore memory) because we know nobody's using it!

The SMR Algorithm

- Every thread comes pre-equipped with a *finite* list of "hazards"
- Memory reclaim involves scanning everybody's hazards to see if there's a collision
- Threads doing reclaim `yield()` (to the objecting thread) until the hazard is clear
- Difficulty
  - Show that hazards can only decrease when deletions are pending
  - Show that deletions eventually succeed (can't deadlock on hazards)
  - Managing the list of threads' hazards is difficult

Observation On Object Lifetime

Instance of a general problem [Memishian(2006)]:

> *Things get tricky when the object must go away. [...] Any thread looking up the object – by definition – does not yet have the object and thus cannot hold the object's lock during the lookup operation. [...] Thus, whatever higher-level synchronization is used to coordinate the threads looking up the object must also be used as part of removing the object from visibility.*