



Machine-Level Programming III: Procedures

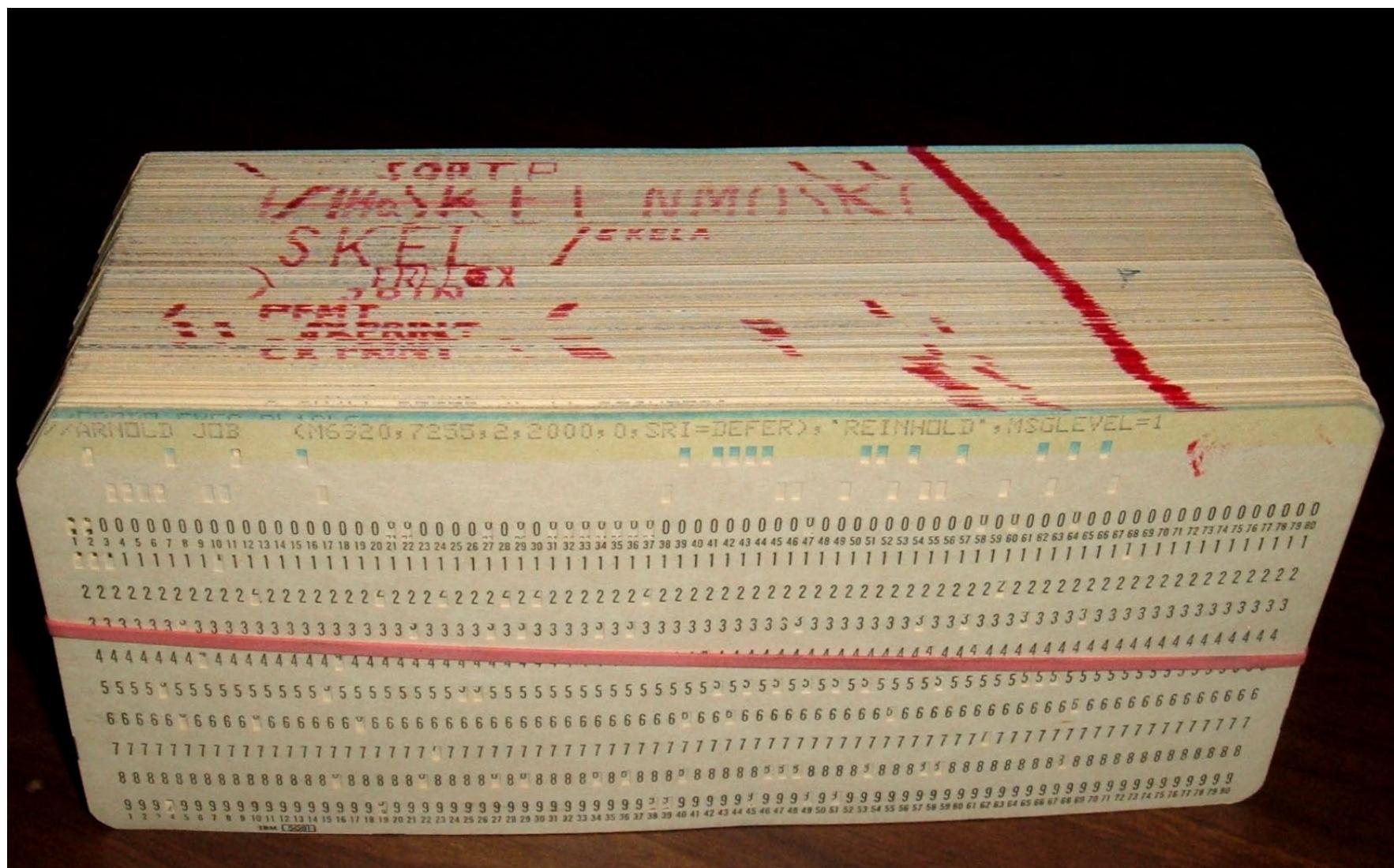
15-213/14-513/15-513: Introduction to Computer Systems
5th Lecture, Sept 12, 2023

Today

■ Procedures

- **Mechanisms** CSAPP 3.7 preamble
- **Stack Structure** CSAPP 3.7.1
- **Calling Conventions**
 - **Passing control** CSAPP 3.7.2
 - **Passing data** CSAPP 3.7.3
 - **Managing local data** CSAPP 3.7.4 – 3.7.5
- **Illustration of Recursion** CSAPP 3.7.6

Procedures



Mechanisms in Procedures

What's needed?

■ Passing control

- To beginning of procedure code
- Back to return point

■ Passing data

- Procedure arguments
- Return value

■ Memory management

- Allocate during procedure execution
- Deallocate upon return

```
P (...) {  
    •  
    •  
    y = Q(x);  
    print(y)  
    •  
}
```

```
int Q(int i)  
{  
    int t = 3*i;  
    int v[10];  
    •  
    •  
    return v[t];  
}
```

Mechanisms in Procedures

■ Passing control

- To beginning of procedure code
- Back to return point

■ Passing data

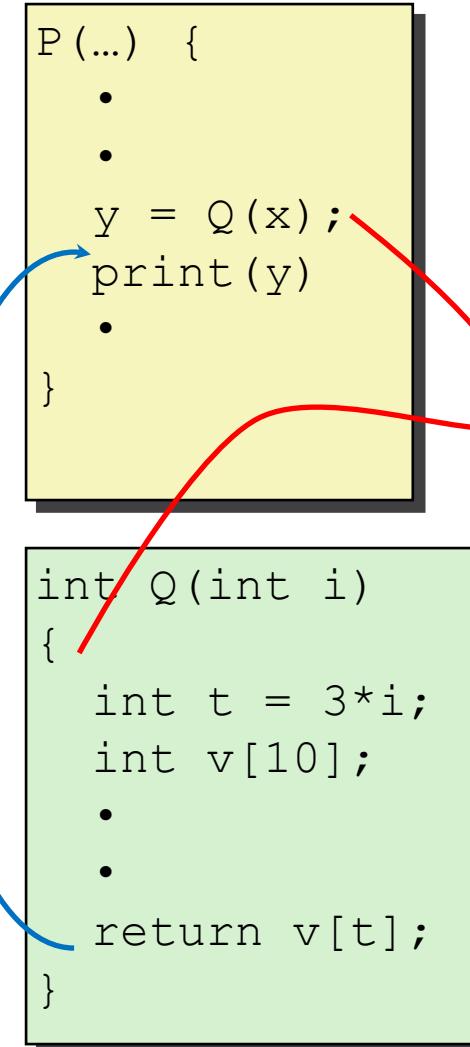
- Procedure arguments
- Return value

■ Memory management

- Allocate during procedure execution
- Deallocate upon return

■ Mechanisms all implemented with machine instructions

■ x86-64 implementation of a procedure uses only those mechanisms required



Mechanisms in Procedures

■ Passing control

- To beginning of procedure code
- Back to return point

■ Passing data

- Procedure arguments
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■ Memory management

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■ Mechanisms all implemented with machine instructions

■ x86-64 implementation of a procedure uses only those mechanisms required

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```
int Q(int i)  
{  
    int t = 3*i;  
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    •  
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```

Mechanisms in Procedures

■ Passing control

- To beginning of procedure code
- Back to return point

■ Passing data

- Procedure arguments
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■ Memory management

- Allocate during procedure execution
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■ Mechanisms all implemented with machine instructions

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```
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{  
    int t = 3*i;  
    int v[10];  
    •  
    •  
    return v[t];  
}
```

Mechanisms in Procedures

```
P( ) {
```

Machine instructions implement the mechanisms, but the choices are determined by designers. These choices make up the **Application Binary Interface (ABI)**.

- Deallocate upon return
- **Mechanisms all implemented with machine instructions**
- **x86-64 implementation of a procedure uses only those mechanisms required**

```
int v[10];  
:  
:  
return v[t];  
}
```

Today

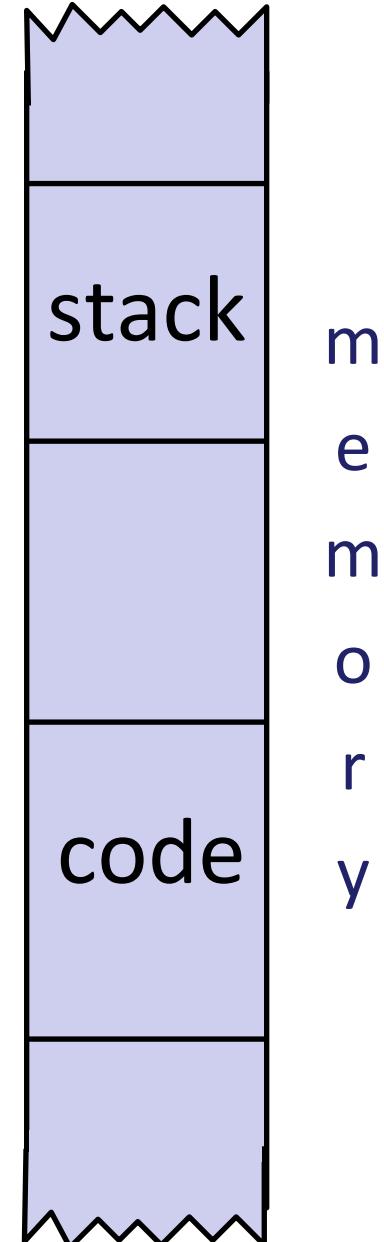
■ Procedures

- Mechanisms
- Stack Structure
- Calling Conventions
 - Passing control
 - Passing data
 - Managing local data
- Activity
- Illustration of Recursion

x86-64 Stack

■ Region of memory managed with stack discipline

- Memory viewed as array of bytes.
- Different regions have different purposes.
- (Like ABI, a policy decision)



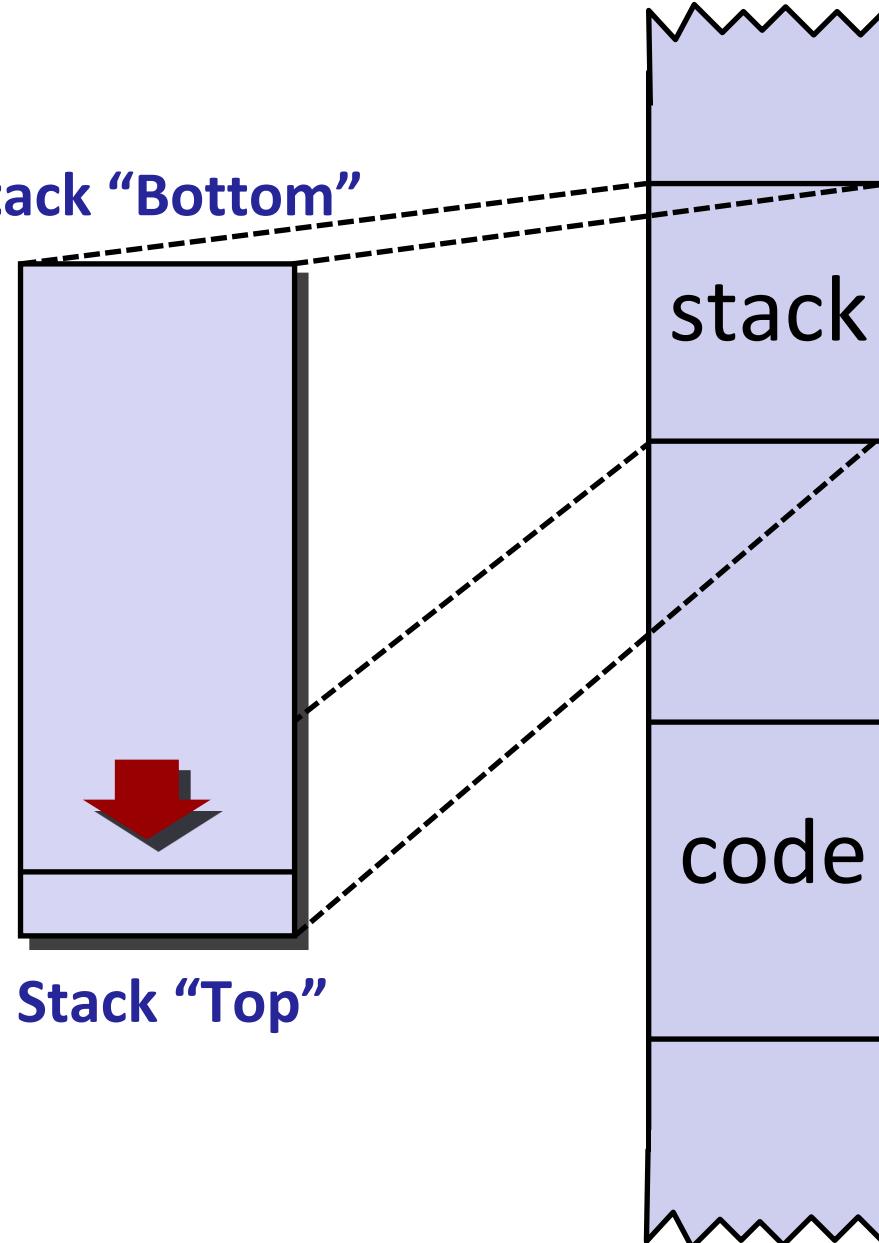
x86-64 Stack

- Region of memory managed with stack discipline

Stack Pointer: `%rsp` →

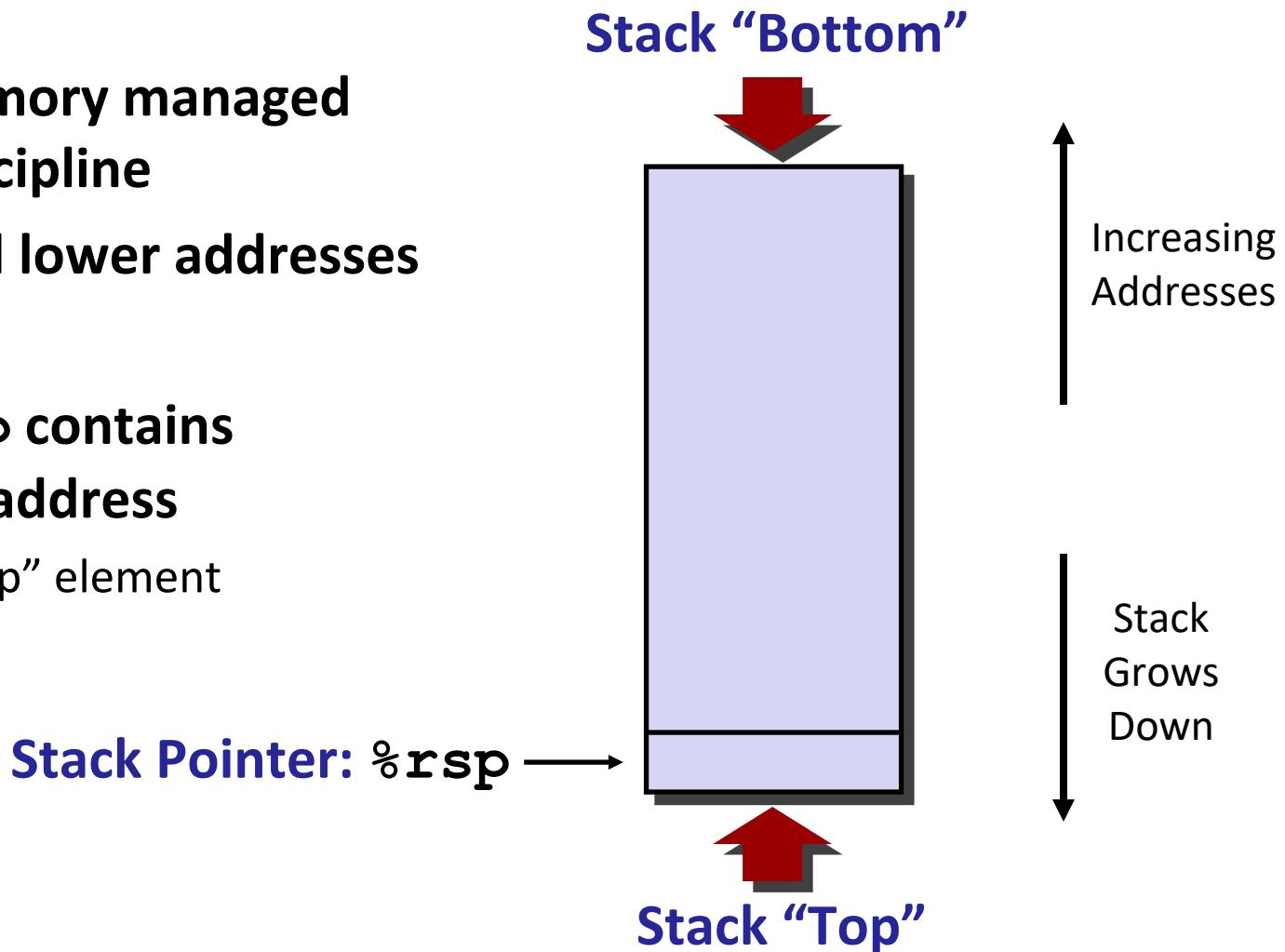
Stack “Bottom”

Stack “Top”



x86-64 Stack

- Region of memory managed with stack discipline
- Grows toward lower addresses
- Register `%rsp` contains lowest stack address
 - address of “top” element



x86-64 Stack: Push

■ **pushq Src**

- Fetch operand at *Src*
- Decrement `%rsp` by 8
- Write operand at address given by `%rsp`

val

Stack Pointer:

`%rsp`

Stack “Bottom”



Increasing
Addresses

↓
Stack
Grows
Down

Stack “Top”



x86-64 Stack: Push

■ **pushq Src**

- Fetch operand at *Src*
- Decrement `%rsp` by 8
- Write operand at address given by `%rsp`

val

Stack Pointer:

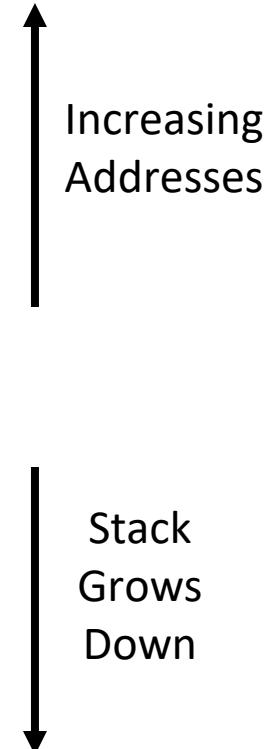
`%rsp`

-8

Stack “Bottom”



Stack “Top”



x86-64 Stack: Pop

■ **popq Dest**

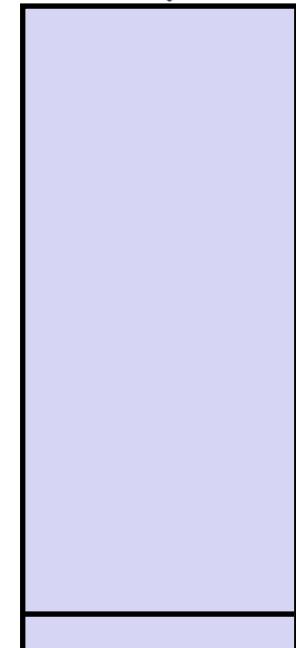
- Read value at address given by `%rsp`
- Increment `%rsp` by 8
- Store value at Dest (usually a register)

Value is **copied**; it remains
in memory at old `%rsp`

Stack Pointer:

`%rsp` +8

Stack “Bottom”



Increasing
Addresses

Stack
Grows
Down

Stack “Top”



Today

■ Procedures

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Code Examples

```
void multstore(long x, long y, long *dest)
{
    long t = mult2(x, y);
    *dest = t;
}
```

```
000000000400540 <multstore>:
    400540: push    %rbx          # Save %rbx
    400541: mov     %rdx,%rbx    # Save dest
    400544: call    400550 <mult2>  # mult2(x,y)
    400549: mov     %rax,(%rbx)   # Save at dest
    40054c: pop    %rbx          # Restore %rbx
    40054d: ret                 # Return
```

```
long mult2(long a, long b)
{
    long s = a * b;
    return s;
}
```

```
000000000400550 <mult2>:
    400550: mov     %rdi,%rax    # a
    400553: imul   %rsi,%rax    # a * b
    400557: ret                 # Return
```

Procedure Control Flow

■ Use stack to support procedure call and return

■ Procedure call: `call label`

- Push return address on stack
- Jump to *label*

■ Return address:

- Address of the next instruction right after call
- Example from disassembly

■ Procedure return: `ret`

- Pop address from stack
- Jump to address

These instructions are sometimes printed with a `q` suffix

- This is just to remind you that you're looking at 64-bit code

Control Flow Example #1

```
0000000000400540 <multstore>:
```

```
•  
•  
400544: call    400550 <mult2>  
400549: mov     %rax, (%rbx)  
•  
•
```

```
0000000000400550 <mult2>:
```

```
400550: mov     %rdi,%rax  
•  
•  
400557: ret
```

0x130

0x128

0x120

%rsp

%rip

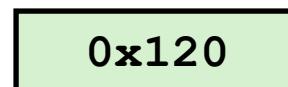
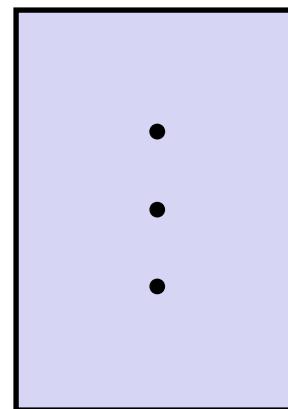
•

•

•

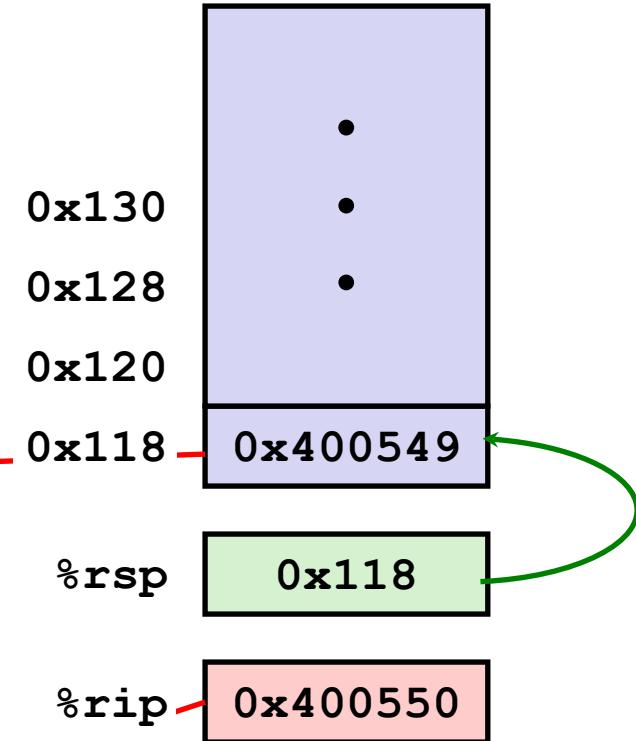
0x120

0x400544



Control Flow Example #2

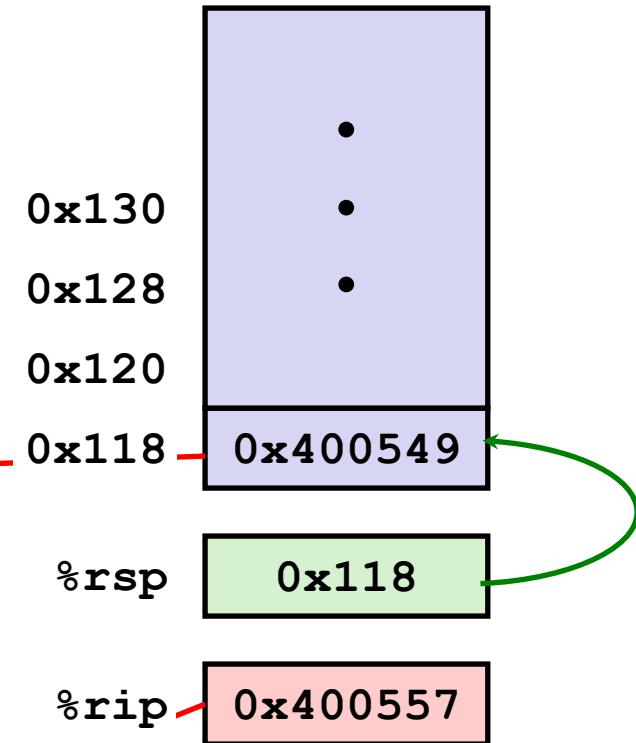
```
0000000000400540 <multstore>:  
.  
.  
400544: call    400550 <mult2>  
400549: mov     %rax, (%rbx) ←
```



```
0000000000400550 <mult2>:  
400550: mov     %rdi,%rax ←  
. .  
400557: ret
```

Control Flow Example #3

```
0000000000400540 <multstore>:  
.  
.  
400544: call    400550 <mult2>  
400549: mov     %rax, (%rbx) ←
```

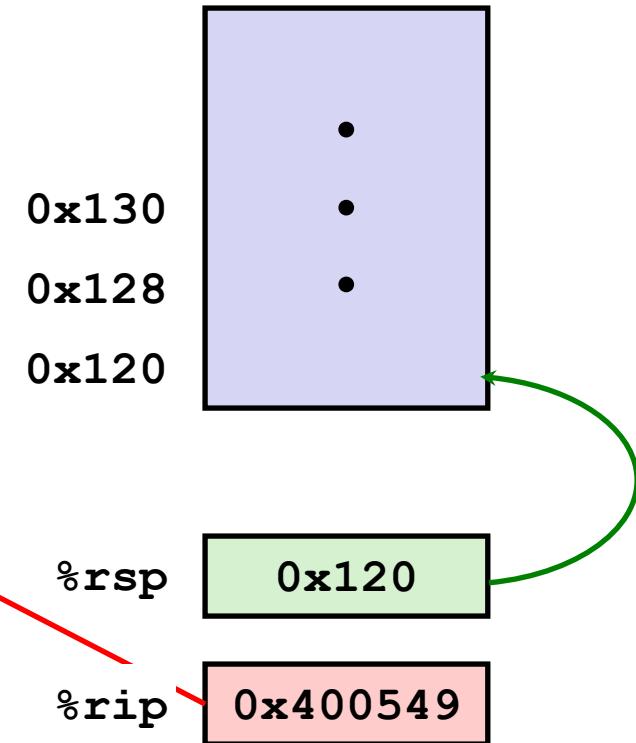


```
0000000000400550 <mult2>:  
400550: mov     %rdi,%rax  
. .  
400557: ret ←
```

Control Flow Example #4

```
0000000000400540 <multstore>:  
.  
.  
400544: call    400550 <mult2>  
400549: mov     %rax, (%rbx)  
.  
.
```

```
0000000000400550 <mult2>:  
400550: mov     %rdi,%rax  
.  
.  
400557: ret
```



Today

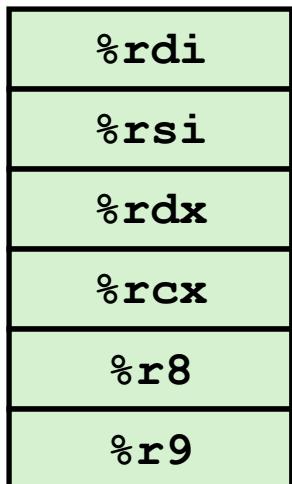
■ Procedures

- Mechanisms
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- Calling Conventions
 - Passing control
 - Passing data
 - Managing local data
- Activity
- Illustration of Recursion

Procedure Data Flow

Registers

■ First 6 arguments



Stack



■ Return value



■ Only allocate stack space when needed

Data Flow Examples

```
void multstore
    (long x, long y, long *dest)
{
    long t = mult2(x, y);
    *dest = t;
}
```

000000000400540 <multstore>:

```
# x in %rdi, y in %rsi, dest in %rdx
...
400541: mov    %rdx,%rbx          # Save dest
400544: call   400550 <mult2>    # mult2(x,y)
# t in %rax
400549: mov    %rax,(%rbx)       # Save at dest
...
```

```
long mult2
    (long a, long b)
{
    long s = a * b;
    return s;
}
```

000000000400550 <mult2>:

```
# a in %rdi, b in %rsi
400550: mov    %rdi,%rax          # a
400553: imul   %rsi,%rax          # a * b
# s in %rax
400557: ret
```

Return

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■ Procedures

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Stack-Based Languages

■ Languages that support recursion

- e.g., C, Pascal, Java
- Code must be “*Reentrant*”
 - Multiple simultaneous instantiations of single procedure
- Need some place to store state of each instantiation
 - Arguments
 - Local variables
 - Return pointer

■ Stack discipline

- State for given procedure needed for limited time
 - From when called to when return
- Callee returns before caller does

■ Stack allocated in *Frames*

- state for single procedure instantiation

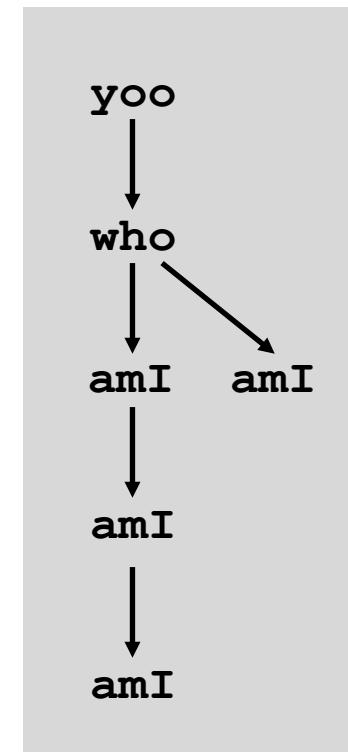
Call Chain Example

```
yoo (...)  
{  
    •  
    •  
    who () ;  
    •  
    •  
}
```

```
who (...)  
{  
    • • •  
    amI () ;  
    • • •  
    amI () ;  
    • • •  
}
```

```
amI (...)  
{  
    •  
    •  
    amI () ;  
    •  
    •  
}
```

Example Call Chain

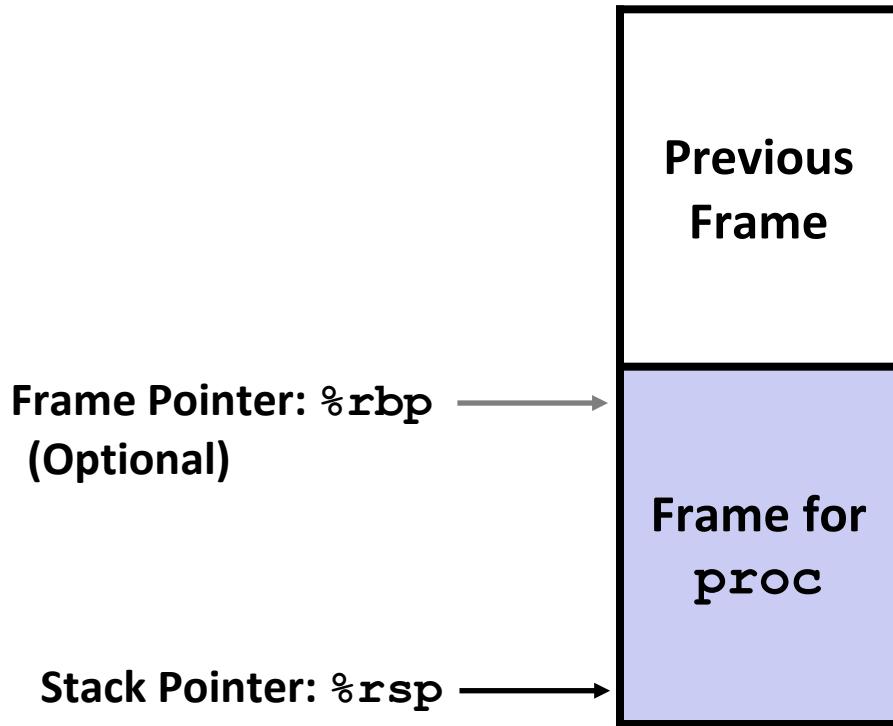


Procedure **amI ()** is recursive

Stack Frames

Contents

- Return information
- Local storage (if needed)
- Temporary space (if needed)

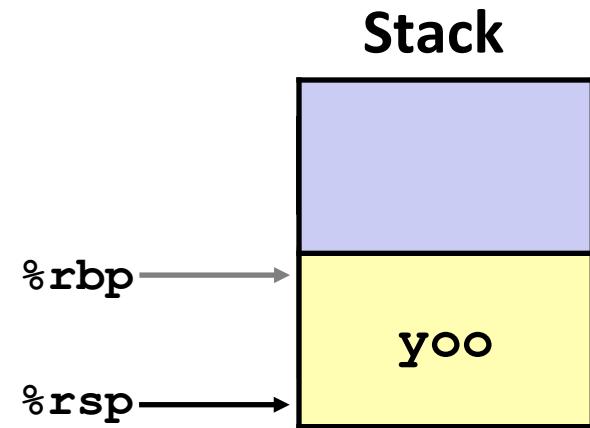
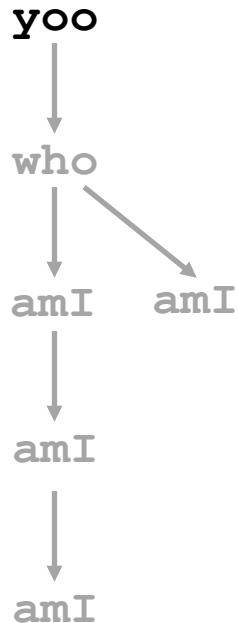


Management

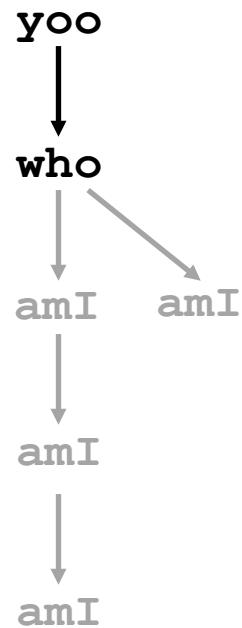
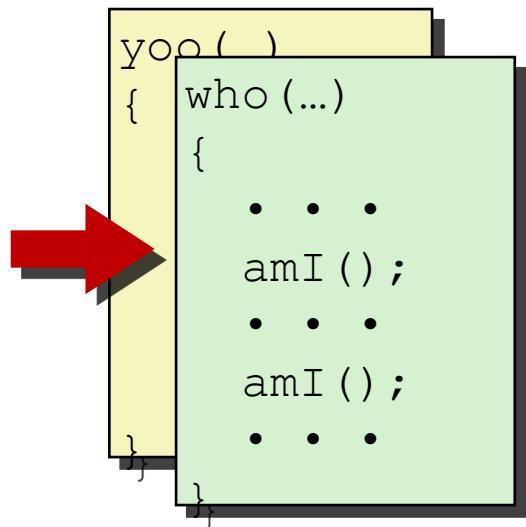
- Space allocated when enter procedure
 - “Set-up” code
 - Includes push by **call** instruction
- Deallocated when return
 - “Finish” code
 - Includes pop by **ret** instruction

Example

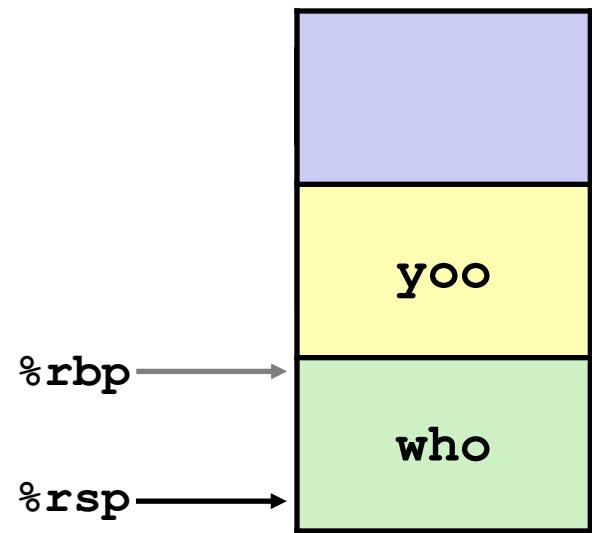
```
yoo (...)  
{  
    •  
    •  
    who () ;  
    •  
    •  
}
```



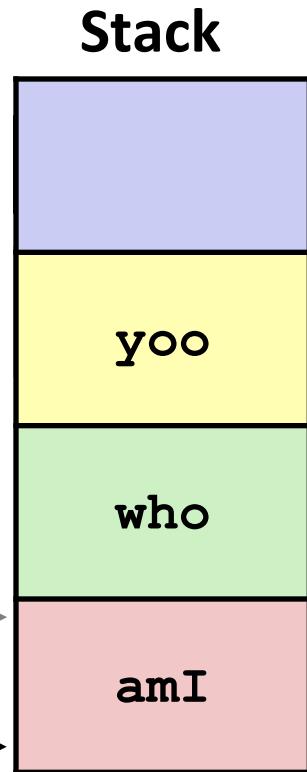
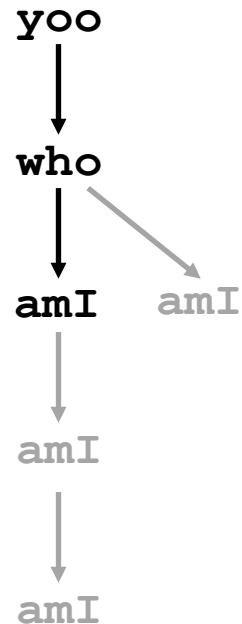
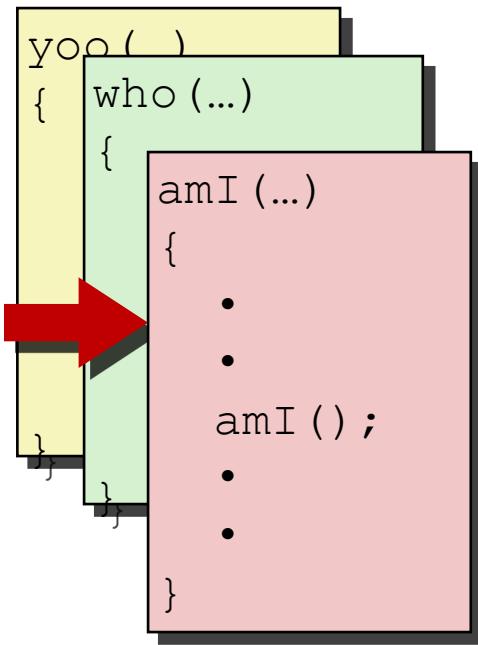
Example



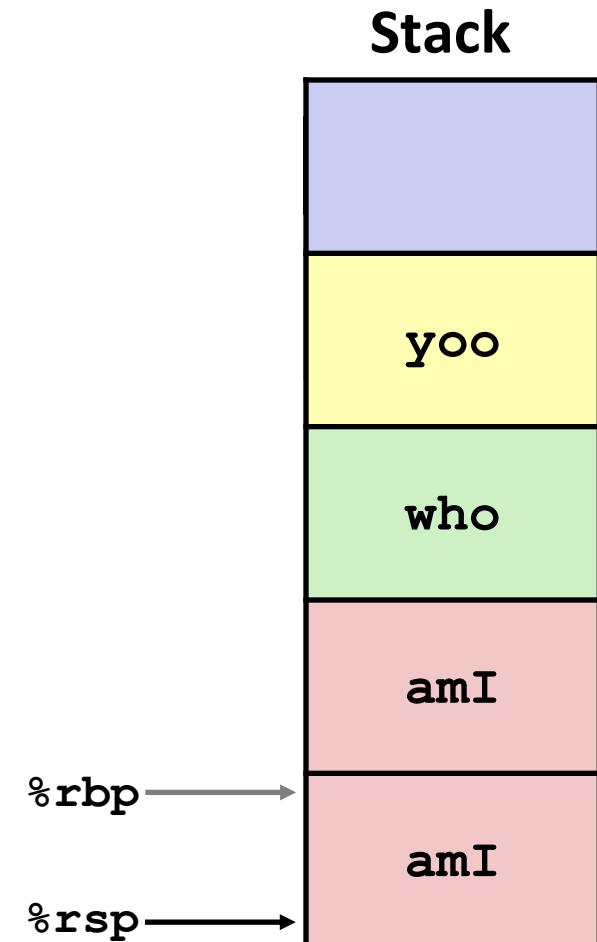
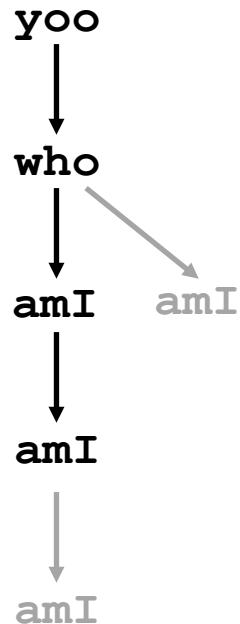
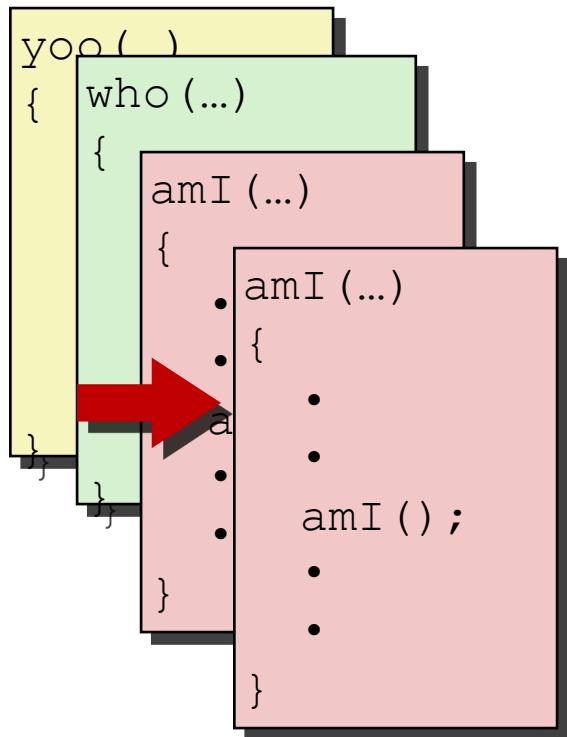
Stack



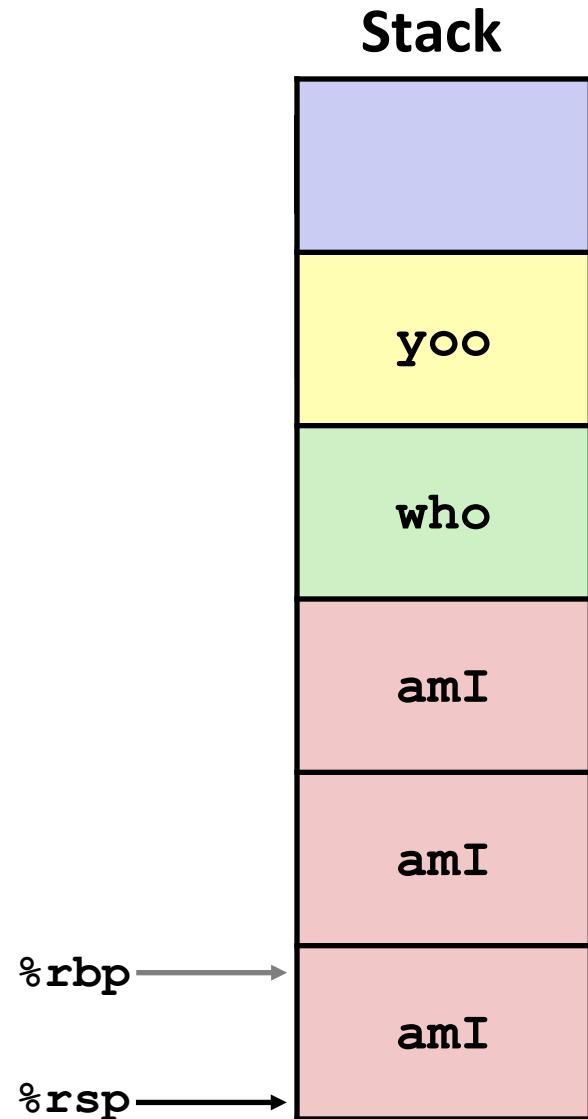
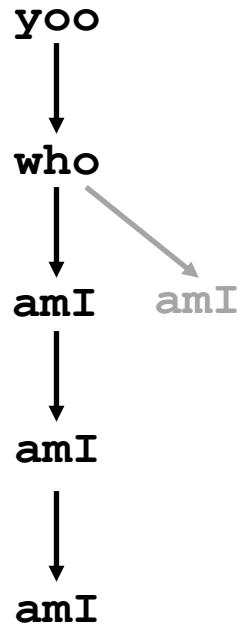
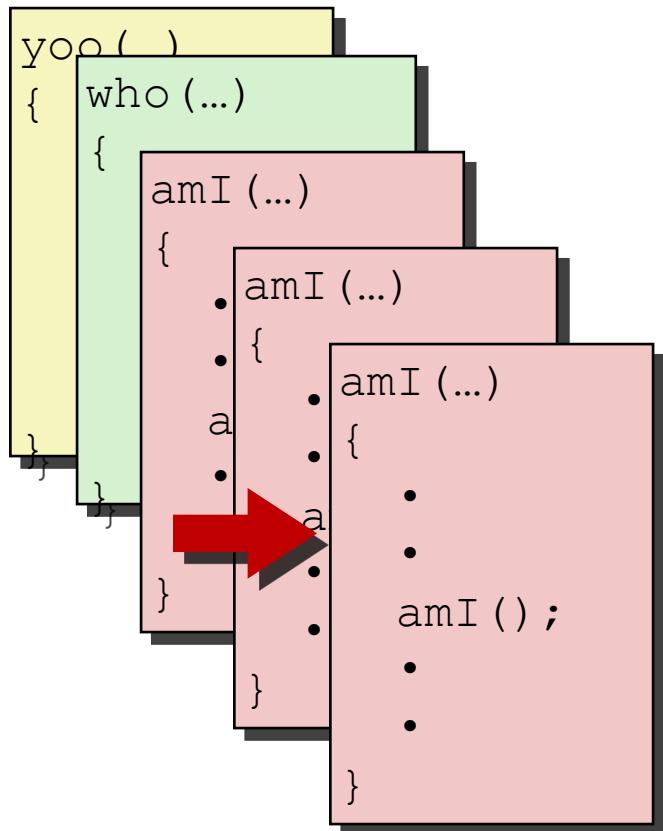
Example



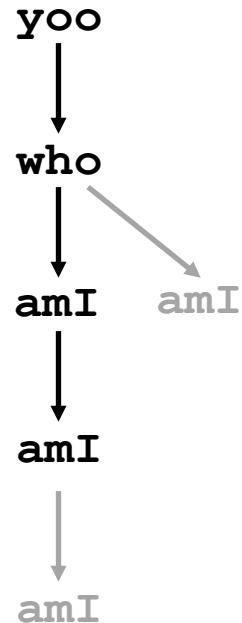
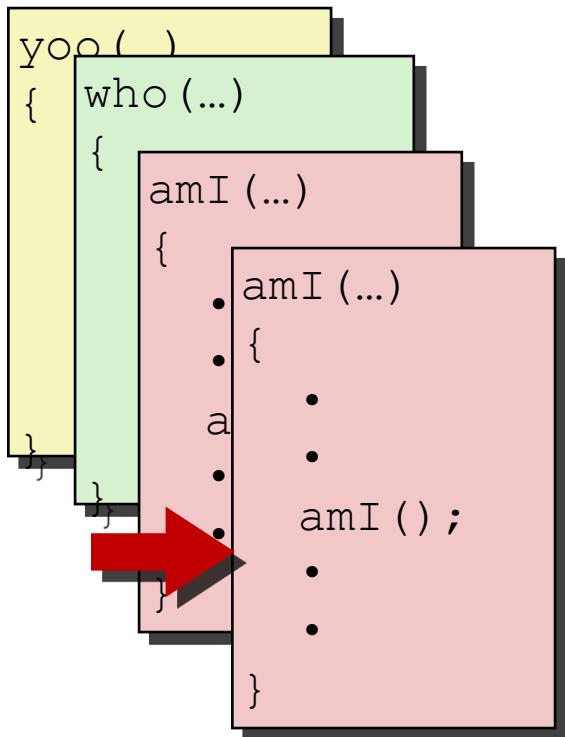
Example



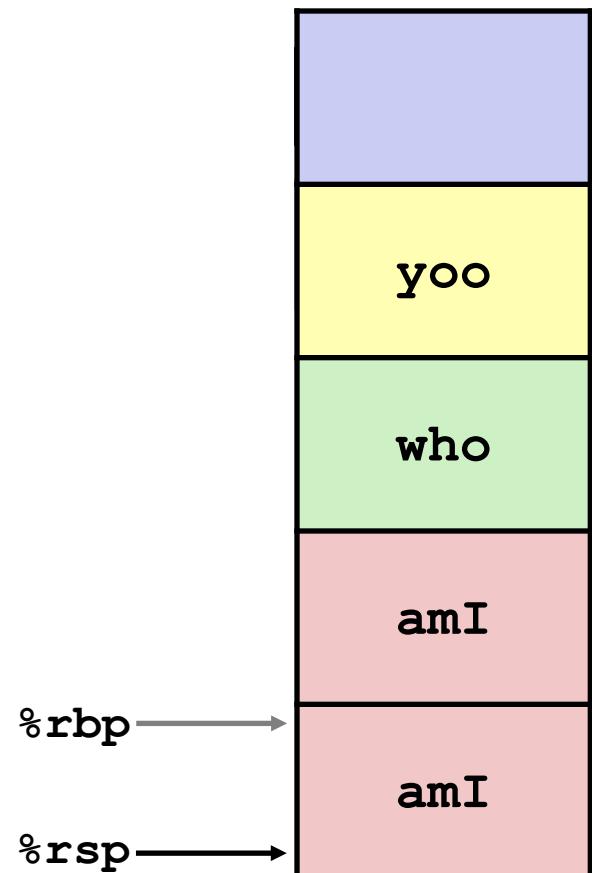
Example



Example

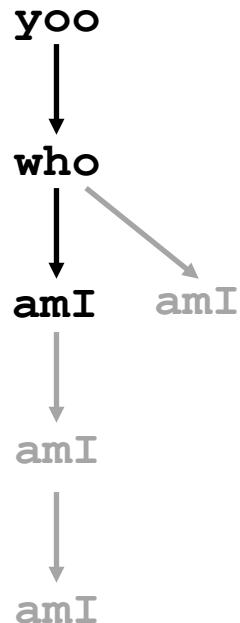


Stack

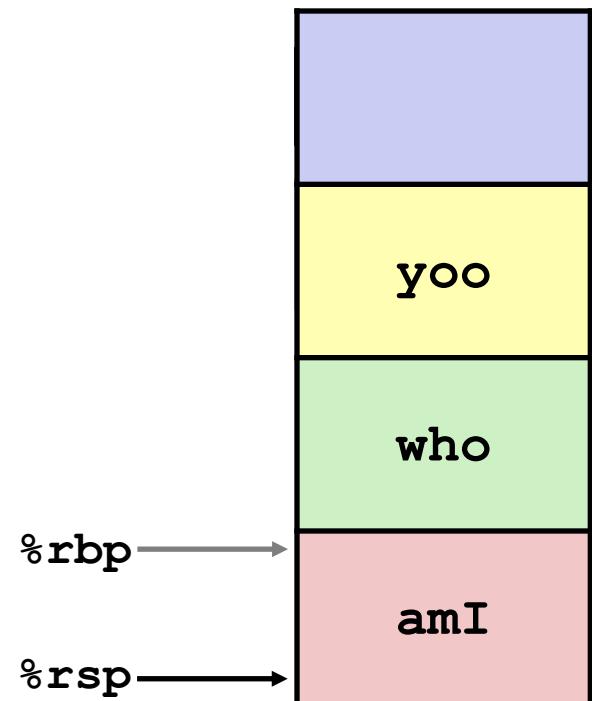


Example

```
yoo( )  
{   who( ... )  
{     amI( ... )  
{       .  
       .  
       amI( );  
       .  
       .  
     }  
   }  
}
```

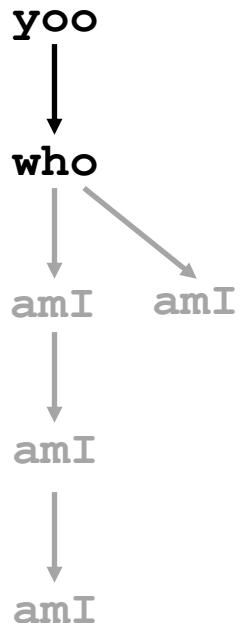
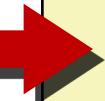


Stack

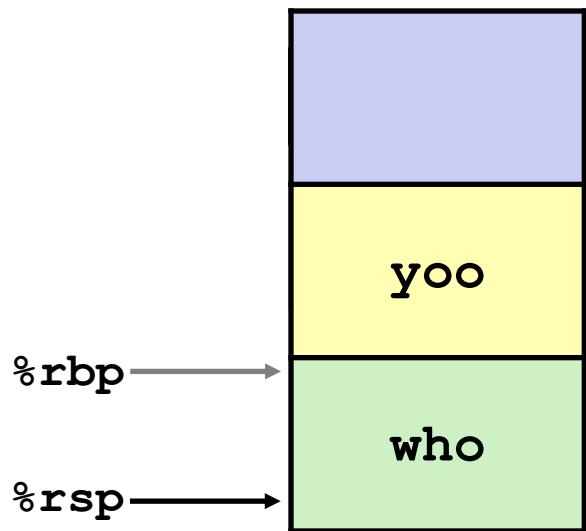


Example

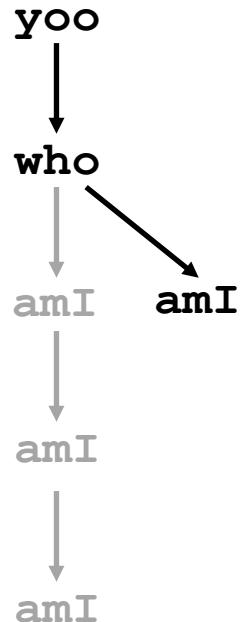
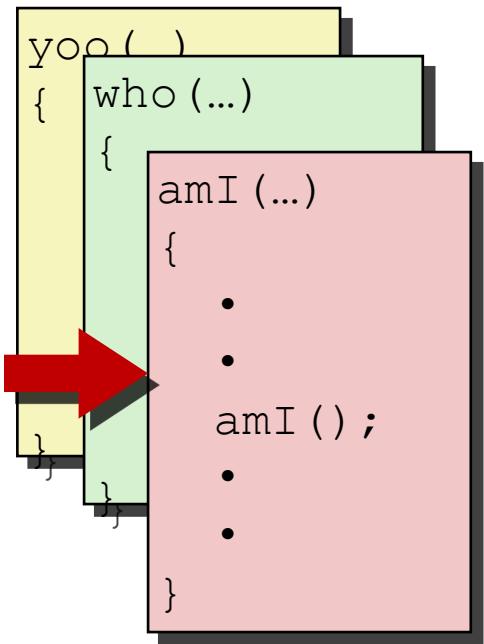
```
yoo( )  
{    who( ... )  
{  
    . . .  
    amI( );  
    . . .  
    amI( );  
    . . .  
}
```



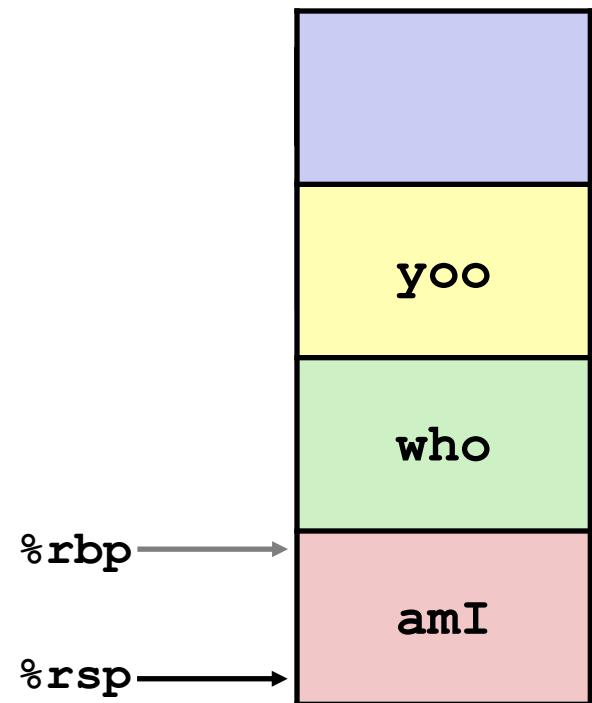
Stack



Example

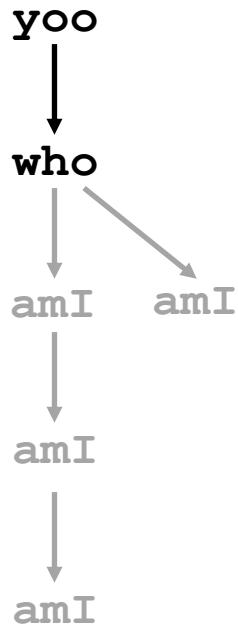


Stack

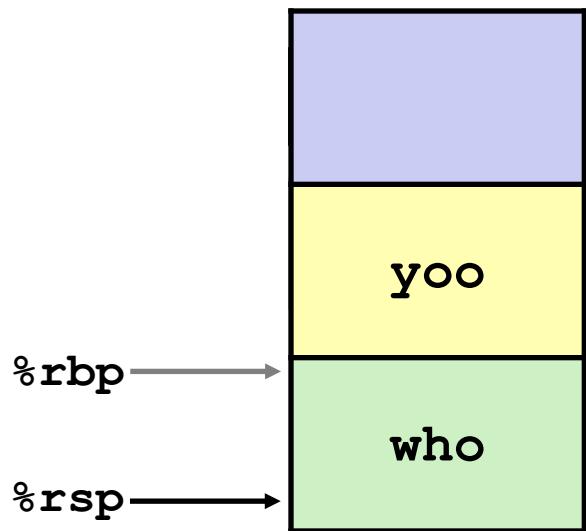


Example

```
yoo( )  
{    who( ... )  
{  
    . . .  
    amI( );  
    . . .  
    amI( );  
    . . .  
}
```

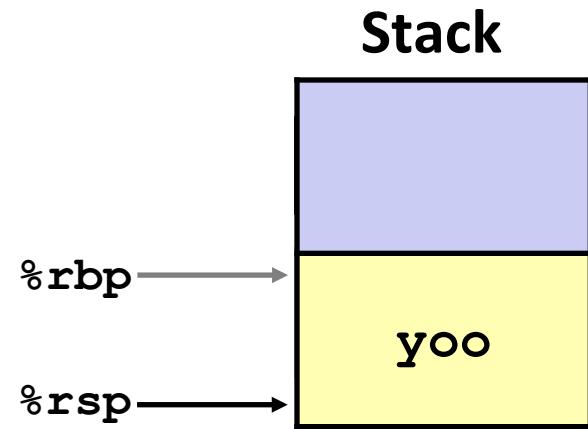
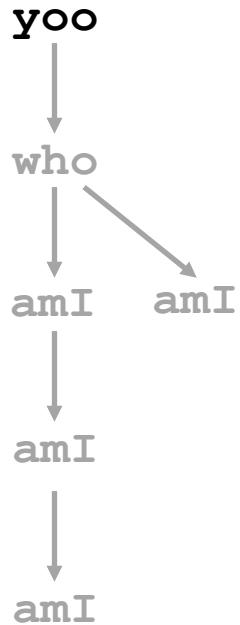


Stack



Example

```
yoo (...)  
{  
    •  
    •  
    who () ;  
    •  
    •  
}
```



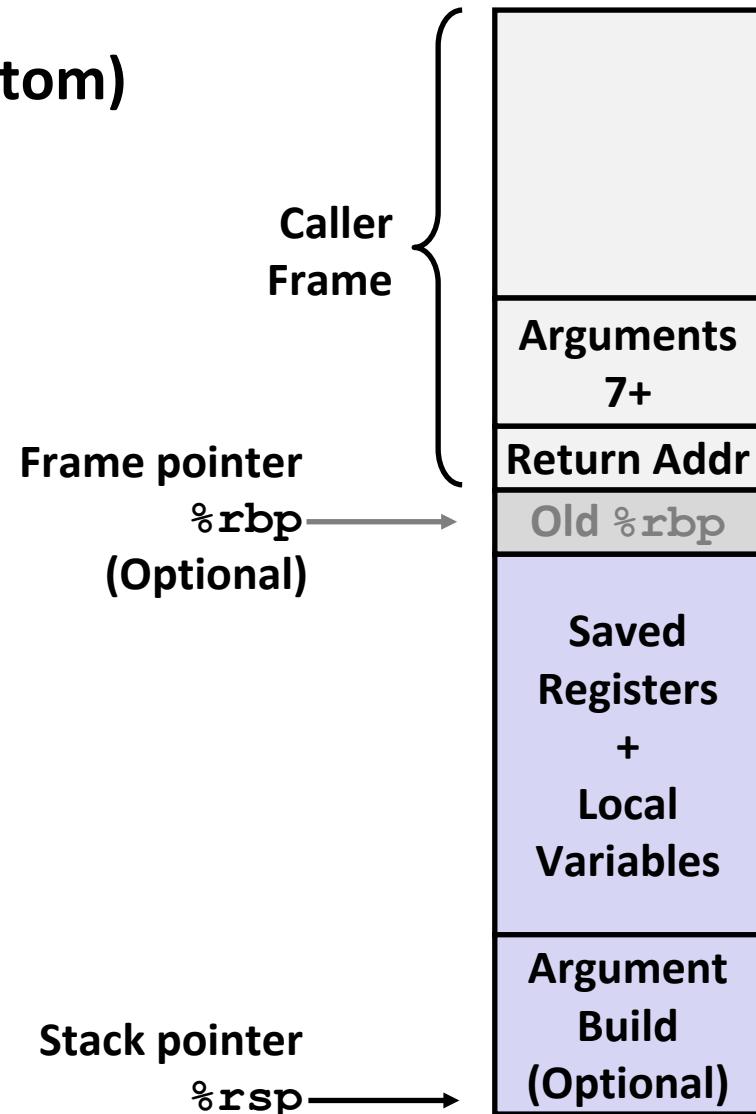
Quiz Time!

Canvas Quiz: Day 5 - Machine Procedures

x86-64/Linux Stack Frame

■ Current Stack Frame (“Top” to Bottom)

- “Argument build:”
Parameters for function about to call
- Local variables
If can’t keep in registers
- Saved register context
- Old frame pointer (optional)



■ Caller Stack Frame

- Return address
 - Pushed by `call` instruction
- Arguments for this call

Example: `incr`

```
long incr(long *p, long val) {  
    long x = *p;  
    long y = x + val;  
    *p = y;  
    return x;  
}
```

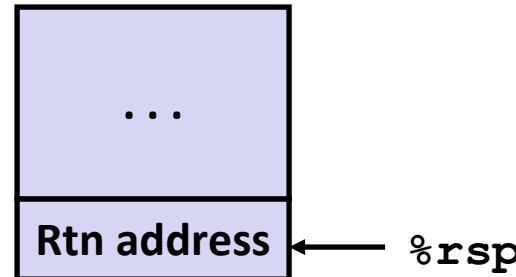
```
incr:  
    movq    (%rdi), %rax  
    addq    %rax, %rsi  
    movq    %rsi, (%rdi)  
    ret
```

| Register | Use(s) |
|----------|--------------------------------|
| %rdi | Argument p |
| %rsi | Argument val , y |
| %rax | x , Return value |

Example: Calling `incr` #1

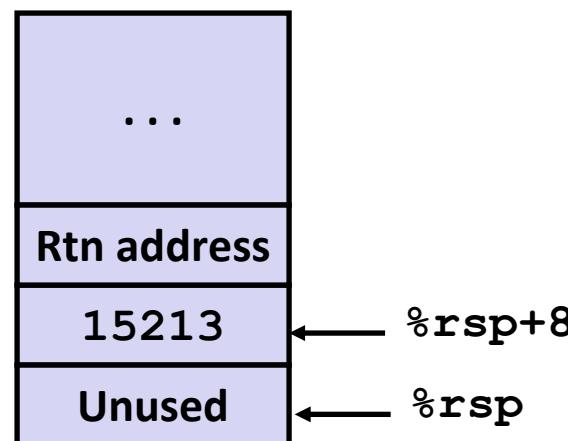
```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

Initial Stack Structure



```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Resulting Stack Structure

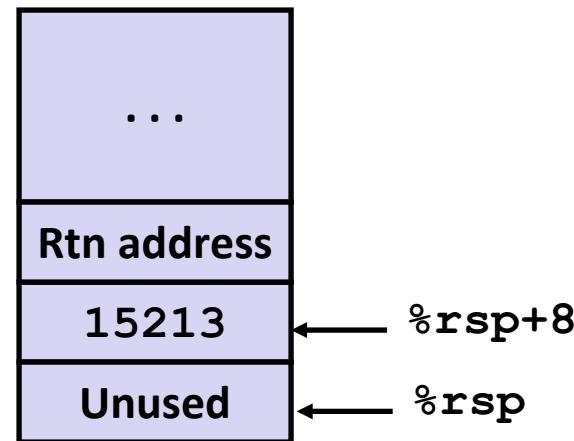


Example: Calling incr #2

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Stack Structure

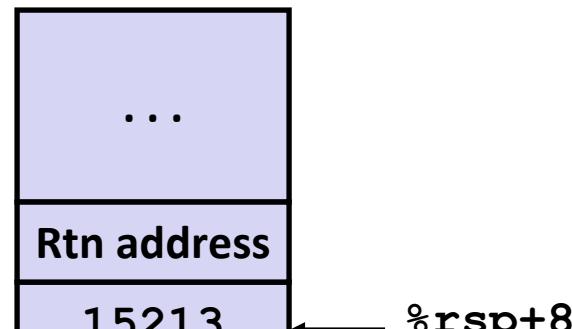


| Register | Use(s) |
|----------|--------|
| %rdi | &v1 |
| %rsi | 3000 |

Example: Calling incr #2

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

Stack Structure



Aside 1: `movl $3000, %esi`

- Remember, `movl` -> `%exx` zeros out high order 32 bits.
- Why use `movl` instead of `movq`? 1 byte shorter.

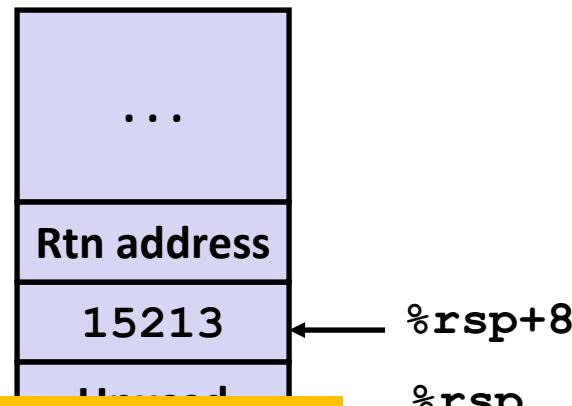
```
movl    $3000, %esi
leaq    8(%rsp), %rdi
call    incr
addq    8(%rsp), %rax
addq    $16, %rsp
ret
```

| | |
|-------------------|----------------------|
| <code>%rdi</code> | <code>&v1</code> |
| <code>%rsi</code> | 3000 |

Example: Calling incr #2

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

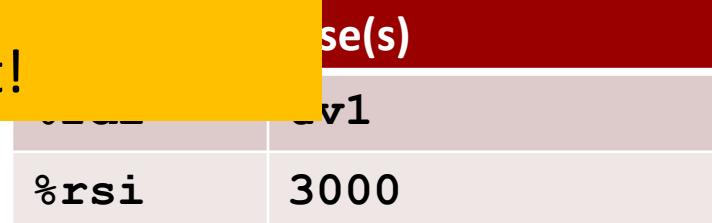
Stack Structure



Aside 2: `leaq 8(%rsp), %rdi`

- Computes %rsp+8
- Actually, used for what it is meant!

```
leaq    8(%rsp), %rdi
call   incr
addq    8(%rsp), %rax
addq    $16, %rsp
ret
```

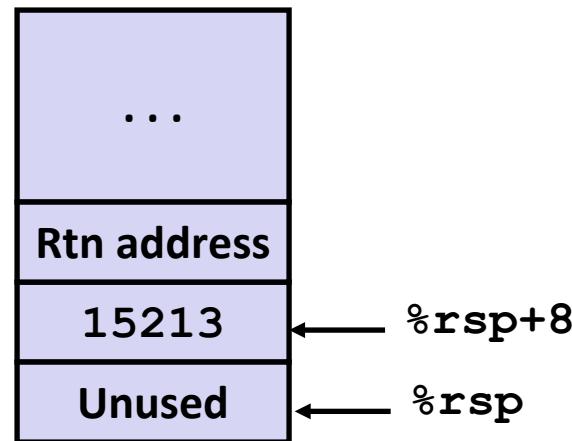


Example: Calling incr #2

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

Stack Structure

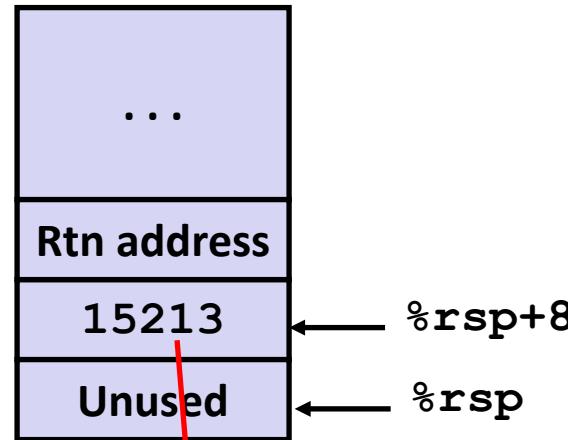


| Register | Use(s) |
|----------|--------|
| %rdi | &v1 |
| %rsi | 3000 |

Example: Calling incr #3a

Stack Structure

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```



```
call_incr:
subq    $16, %rsp
movq    $15213, 8(%rsp)
movl    $3000, %esi
leaq    8(%rsp), %rdi
call    incr
addq    8(%rsp), %rax
addq    $16, %rsp
ret
```

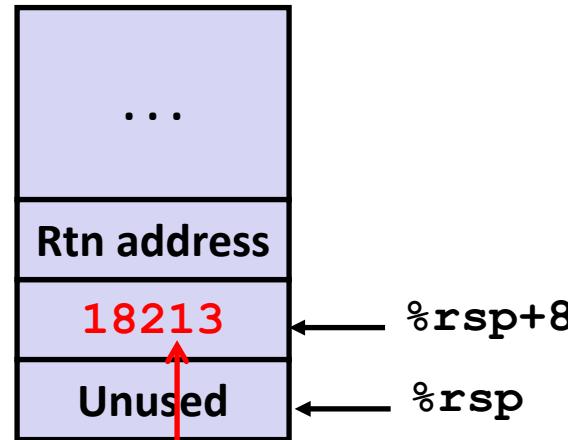
| Register | Use(s) |
|----------|--------|
| %rdi | &v1 |
| %rsi | 3000 |

```
long incr(long *p, long val) {
    long x = *p;
    long y = x + val;
    *p = y;
    return x;
}
```

Example: Calling `incr` #3b

Stack Structure

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```



```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

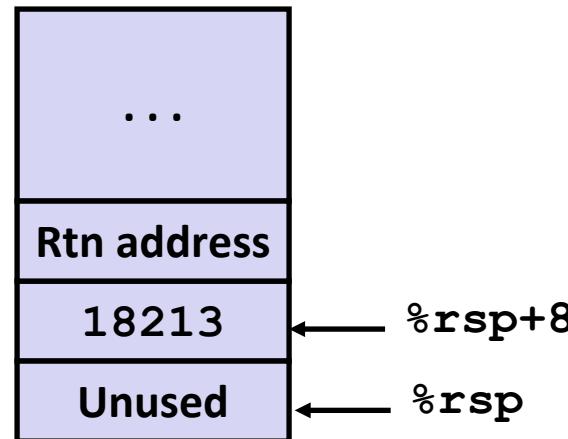
| Register | Use(s) |
|----------|--------|
| %rdi | &v1 |
| %rsi | 3000 |

```
long incr(long *p, long val) {
    long x = *p;
    long y = x + val;
    *p = y;
    return x;
}
```

Example: Calling `incr` #4

Stack Structure

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```



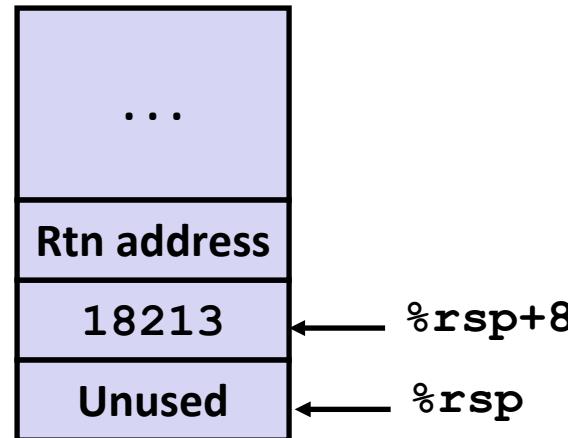
```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

| Register | Use(s) |
|----------|--------------|
| %rax | Return value |

Example: Calling `incr` #5a

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

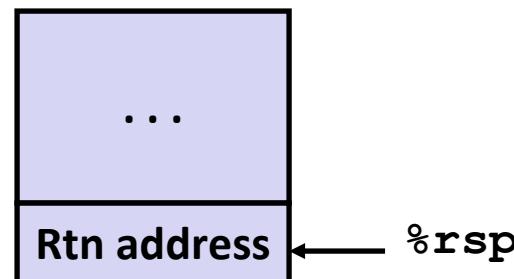
Stack Structure



```
call_incr:
subq    $16, %rsp
movq    $15213, 8(%rsp)
movl    $3000, %esi
leaq    8(%rsp), %rdi
call    incr
addq    8(%rsp), %rax
addq    $16, %rsp
ret
```

| Register | Use(s) |
|-------------------|--------------|
| <code>%rax</code> | Return value |

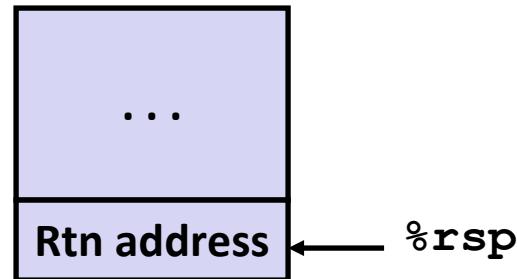
Updated Stack Structure



Example: Calling `incr` #5b

```
long call_incr() {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return v1+v2;
}
```

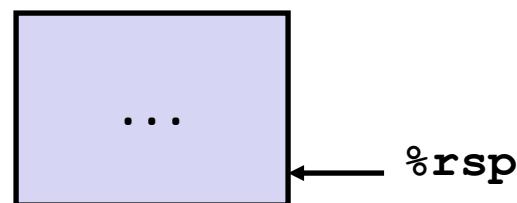
Updated Stack Structure



```
call_incr:
    subq    $16, %rsp
    movq    $15213, 8(%rsp)
    movl    $3000, %esi
    leaq    8(%rsp), %rdi
    call    incr
    addq    8(%rsp), %rax
    addq    $16, %rsp
    ret
```

| Register | Use(s) |
|----------|--------------|
| %rax | Return value |

Final Stack Structure



Register Saving Conventions

■ When procedure **yoo** calls **who**:

- **yoo** is the *caller*
- **who** is the *callee*

■ Can register be used for temporary storage?

```
yoo:
```

```
    • • •  
    movq $15213, %rdx  
    call who  
    addq %rdx, %rax  
    • • •  
    ret
```

```
who:
```

```
    • • •  
    subq $18213, %rdx  
    • • •  
    ret
```

- Contents of register **%rdx** overwritten by **who**
- This could be trouble → something should be done!
 - Need some coordination

Register Saving Conventions

■ When procedure **yoo** calls **who**:

- **yoo** is the *caller*
- **who** is the *callee*

■ Can register be used for temporary storage?

■ Conventions

- “*Caller Saved*” (aka “*Call-Clobbered*”)
 - Caller saves temporary values in its frame before the call
- “*Callee Saved*” (aka “*Call-Preserved*”)
 - Callee saves temporary values in its frame before using
 - Callee restores them before returning to caller

x86-64 Linux Register Usage #1

■ %rax

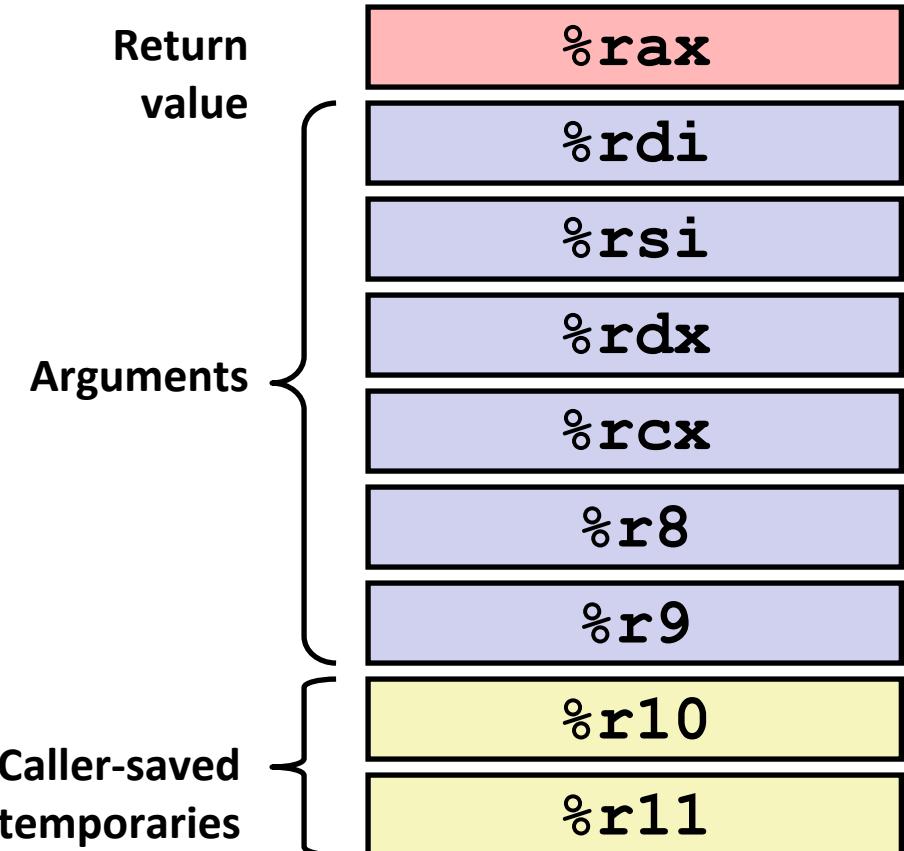
- Return value
- Also caller-saved
- Can be modified by procedure

■ %rdi, ..., %r9

- Arguments
- Also caller-saved
- Can be modified by procedure

■ %r10, %r11

- Caller-saved
- Can be modified by procedure



x86-64 Linux Register Usage #2

■ **%rbx, %r12, %r13, %r14**

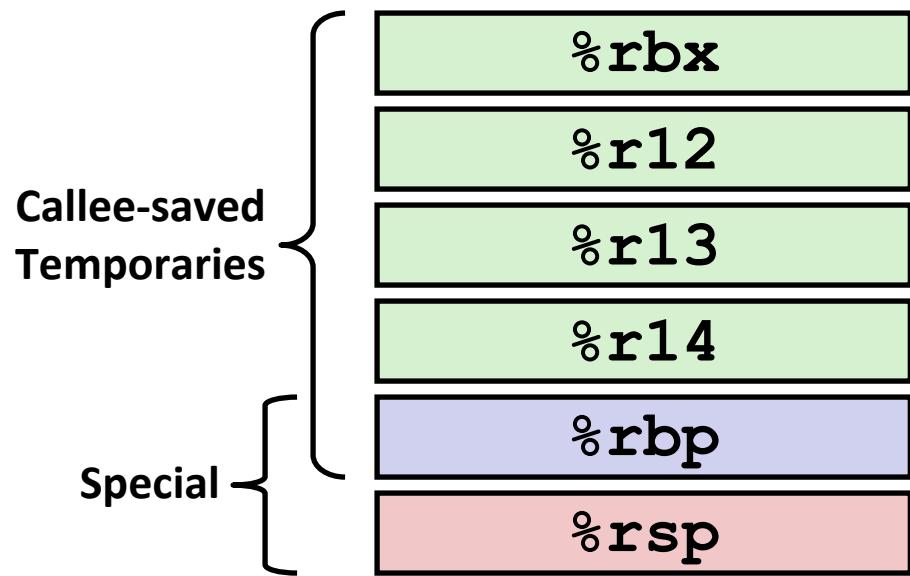
- Callee-saved
- Callee must save & restore

■ **%rbp**

- Callee-saved
- Callee must save & restore
- May be used as frame pointer
- Can mix & match

■ **%rsp**

- Special form of callee save
- Restored to original value upon exit from procedure



Activity Time!

1. Download **machine-procedures.pdf** from
<http://www.cs.cmu.edu/~213/schedule.html>

2. Log into a shark machine and do the following:

```
$ wget http://www.cs.cmu.edu/~213/activities/machine-procedures.tar  
$ tar xf machine-procedures.tar  
$ cd machine-procedures
```

3. Do **Activity 2: Problems 6, 7, 8, and 9**

```
$ gdb --args ./locals  
(gdb) r  
[follow the instructions in bold...]
```

Today

■ Procedures

- Mechanisms
- Stack Structure
- Calling Conventions
 - Passing control
 - Passing data
 - Managing local data
- Activity
- Illustration of Recursion

Recursive Function

```
/* Recursive popcount */
long pcount_r(unsigned long x) {
    if (x == 0)
        return 0;
    else
        return (x & 1)
            + pcount_r(x >> 1);
}
```

`pcount_r:`

```
    movl    $0, %eax
    testq   %rdi, %rdi
    je      .L6
    pushq   %rbx
    movq   %rdi, %rbx
    andl   $1, %ebx
    shrq   %rdi
    call   pcount_r
    addq   %rbx, %rax
    popq   %rbx
```

`.L6:`

```
    rep; ret
```

Recursive Function Terminal Case

```
/* Recursive popcount */
long pcount_r(unsigned long x) {
    if (x == 0)
        return 0;
    else
        return (x & 1)
            + pcount_r(x >> 1);
}
```

pcount_r:

| | |
|-------|------------|
| movl | \$0, %eax |
| testq | %rdi, %rdi |
| je | .L6 |
| pushq | %rbx |
| movq | %rdi, %rbx |
| andl | \$1, %ebx |
| shrq | %rdi |
| call | pcount_r |
| addq | %rbx, %rax |
| popq | %rbx |

.L6:

rep; ret

| Register | Use(s) | Type |
|----------|--------------|--------------|
| %rdi | x | Argument |
| %rax | Return value | Return value |

Recursive Function Register Save

```
/* Recursive popcount */
long pcount_r(unsigned long x) {
    if (x == 0)
        return 0;
    else
        return (x & 1)
            + pcount_r(x >> 1);
}
```

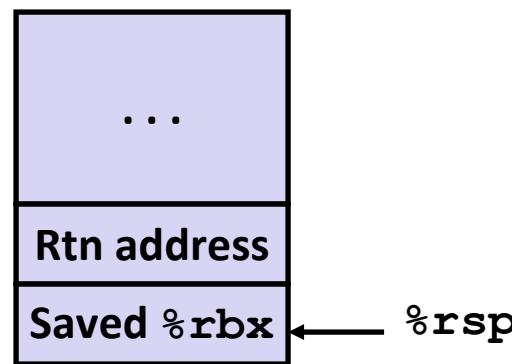
`pcount_r:`

| | |
|--------------------|-------------------------|
| <code>movl</code> | <code>\$0, %eax</code> |
| <code>testq</code> | <code>%rdi, %rdi</code> |
| <code>je</code> | <code>.L6</code> |
| <code>pushq</code> | <code>%rbx</code> |
| <code>movq</code> | <code>%rdi, %rbx</code> |
| <code>andl</code> | <code>\$1, %ebx</code> |
| <code>shrq</code> | <code>%rdi</code> |
| <code>call</code> | <code>pcount_r</code> |
| <code>addq</code> | <code>%rbx, %rax</code> |
| <code>popq</code> | <code>%rbx</code> |

`.L6:`

`rep; ret`

| Register | Use(s) | Type |
|-------------------|----------------|----------|
| <code>%rdi</code> | <code>x</code> | Argument |



Recursive Function Call Setup

```
/* Recursive popcount */
long pcount_r(unsigned long x) {
    if (x == 0)
        return 0;
    else
        return (x & 1)
            + pcount_r(x >> 1);
}
```

`pcount_r:`

```
    movl    $0, %eax
    testq   %rdi, %rdi
    je      .L6
    pushq   %rbx
    movq    %rdi, %rbx
    andl    $1, %ebx
    shrq    %rdi
    call    pcount_r
    addq    %rbx, %rax
    popq    %rbx
```

`.L6:`

```
    rep; ret
```

| Register | Use(s) | Type |
|----------|---------------------------|---------------|
| %rdi | <code>x >> 1</code> | Rec. argument |
| %rbx | <code>x & 1</code> | Callee-saved |

Recursive Function Call

```
/* Recursive popcount */
long pcount_r(unsigned long x) {
    if (x == 0)
        return 0;
    else
        return (x & 1)
            + pcount_r(x >> 1);
}
```

`pcount_r:`

```
    movl    $0, %eax
    testq   %rdi, %rdi
    je      .L6
    pushq   %rbx
    movq    %rdi, %rbx
    andl    $1, %ebx
    shrq    %rdi
    call    pcount_r
    addq    %rbx, %rax
    popq    %rbx
```

`.L6:`

```
rep; ret
```

| Register | Use(s) | Type |
|-------------------|--------------------------------|--------------|
| <code>%rbx</code> | <code>x & 1</code> | Callee-saved |
| <code>%rax</code> | Recursive call return value | |

Recursive Function Result

```
/* Recursive popcount */
long pcount_r(unsigned long x) {
    if (x == 0)
        return 0;
    else
        return (x & 1)
            + pcount_r(x >> 1);
}
```

`pcount_r:`

```
    movl    $0, %eax
    testq   %rdi, %rdi
    je      .L6
    pushq   %rbx
    movq   %rdi, %rbx
    andl   $1, %ebx
    shrq   %rdi
    call   pcount_r
    addq   %rbx, %rax
    popq   %rbx
```

`.L6:`

```
rep; ret
```

| Register | Use(s) | Type |
|-------------------|------------------------|--------------|
| <code>%rbx</code> | <code>x & 1</code> | Callee-saved |
| <code>%rax</code> | Return value | |

Recursive Function Completion

```
/* Recursive popcount */
long pcount_r(unsigned long x) {
    if (x == 0)
        return 0;
    else
        return (x & 1)
            + pcount_r(x >> 1);
}
```

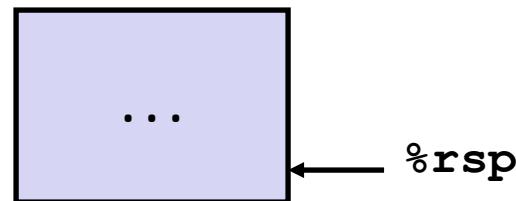
`pcount_r:`

```
    movl    $0, %eax
    testq   %rdi, %rdi
    je      .L6
    pushq   %rbx
    movq   %rdi, %rbx
    andl   $1, %ebx
    shrq   %rdi
    call   pcount_r
    addq   %rbx, %rax
    popq   %rbx
```

`.L6:`

`rep; ret`

| Register | Use(s) | Type |
|-------------------|--------------|--------------|
| <code>%rax</code> | Return value | Return value |



Observations About Recursion

■ Handled Without Special Consideration

- Stack frames mean that each function call has private storage
 - Saved registers & local variables
 - Saved return pointer
- Register saving conventions prevent one function call from corrupting another's data
 - Unless the C code explicitly does so (e.g., buffer overflow in Lecture 7)
- Stack discipline follows call / return pattern
 - If P calls Q, then Q returns before P
 - Last-In, First-Out

■ Also works for mutual recursion

- P calls Q; Q calls P

x86-64 Procedure Summary

■ Important Points

- Stack is the right data structure for procedure call/return
 - If P calls Q, then Q returns before P

■ Recursion (& mutual recursion) handled by normal calling conventions

- Can safely store values in local stack frame and in callee-saved registers
- Put function arguments at top of stack
- Result return in **%rax**

■ Pointers are addresses of values

- On stack or global

