1 Introduction

So far we’ve focused on model checking algorithms that assume a computation structure is given. It should come as no surprise that our goal is to perform model checking of programs given as code, so today we’ll describe techniques that allow us to apply model checking in this setting. There are several challenges to doing so, foremost among them the fact that the statespace of programs may be infinite. We’ll describe two approaches for dealing with this: bounded model checking and predicate abstraction.

Each of these techniques addresses the problem by computing an approximation. Bounded model checking computes an underapproximation of the reachable statespace by assuming a fixed computation depth in advance, and treating paths within this depth limit symbolically to explore all possible states. Predicate abstraction computes an overapproximation of reachable states by constructing a transition structure that treats distinct program states identically, in a way that makes it possible to reason over a finite number of states. While either approach has its limitations, both are used effectively in practice, and are the core techniques that make software model checking possible.

2 Review: Transition structures, LTL

Definition 1 (Trace semantics of programs). The trace semantics, \(\tau(\alpha)\), of a program \(\alpha\), is the set of all its possible traces and is defined inductively as follows:

1. \(\tau(x := e) = \{(\omega, \nu) : \nu = \omega \text{ except that } \nu(x) = \omega[e] \text{ for } \omega \in S\}\)

2. \(\tau(?Q) = \{(\omega) : \omega \models Q\} \cup \{(\omega, \Lambda) : \omega \not\models Q\}\)
3. \( \tau(\text{if}(Q) \alpha \text{ else } \beta) = \{ \sigma \in \tau(\alpha) : \sigma_0 \models Q \} \cup \{ \sigma \in \tau(\beta) : \sigma_0 \not\models Q \} \)

4. \( \tau(\alpha; \beta) = \{ \sigma \circ \varsigma : \sigma \in \tau(\alpha), \varsigma \in \tau(\beta) \} \)

   the composition of \( \sigma = (\sigma_0, \sigma_1, \sigma_2, \ldots) \) and \( \varsigma = (\varsigma_0, \varsigma_1, \varsigma_2, \ldots) \) is

   \[
   \sigma \circ \varsigma := \begin{cases} 
   (\sigma_0, \ldots, \sigma_n, \varsigma_1, \varsigma_2, \ldots) & \text{if } \sigma \text{ terminates in } \sigma_n \text{ and } \sigma_n = \varsigma_0 \\
   \sigma & \text{if } \sigma \text{ does not terminate} \\
   \text{not defined} & \text{otherwise}
   \end{cases}
   \]

5. \( \tau(\text{while}(Q) \alpha) = \{ \sigma^{(0)} \circ \sigma^{(1)} \circ \cdots \circ \sigma^{(n)} : \text{for some } n \geq 0 \text{ such that for all } 0 \leq i < n: \)

   \( \circ \) \ the loop condition is true \( \sigma^{(i)}_0 \models Q \) and \( \circ \) \( \sigma^{(i)} \in \llbracket \alpha \rrbracket \) and \( \circ \) \( \sigma^{(n)} \) either does not terminate or it terminates in \( \sigma^{(n)}_m \) and \( \sigma^{(n)}_m \not\models Q \) in the end.

   \( \cup \{ \sigma^{(0)} \circ \sigma^{(1)} \circ \sigma^{(2)} \circ \cdots : \text{for all } i \in \mathbb{N}: \ \sigma^{(i)}_0 \models Q \text{ and } \circ \ \sigma^{(i)} \in \llbracket \alpha \rrbracket \} \)

   \( \cup \{ \{ \omega \} : \omega \not\models Q \} \)

   That is, the loop either runs a nonzero finite number of times with the last iteration either terminating or running forever, or the loop itself repeats infinitely often and never stops, or the loop does not even run a single time.

6. \( \tau(\alpha^*) = \bigcup_{n \in \mathbb{N}} \tau(\alpha^n) \) where \( \alpha^{n+1} \overset{\text{def}}{=} (\alpha^n; \alpha) \) for \( n \geq 1 \), and \( \alpha^1 \overset{\text{def}}{=} \alpha \) and \( \alpha^0 \overset{\text{def}}{=} (\text{true}) \).

**Definition 2** (Kripke structure). A Kripke frame \((W, I, \circ)\) consists of a set \( W \) with a transition relation \( \circ \subseteq W \times W \) where \( s \circ t \) indicates that there is a direct transition from \( s \) to \( t \) in the Kripke frame \((W, \circ)\). The elements \( s \in W \) are also called states, and \( I \subseteq W \) are the set of initial states. A Kripke structure \( K = (W, I, \circ, v) \) is a Kripke frame \((W, \circ)\) with a mapping \( v : W \rightarrow \Sigma \rightarrow \{ \text{true}, \text{false} \} \) assigning truth-values to all the propositional atoms in all states.

   The program semantics \( \llbracket \alpha \rrbracket \) which was defined as a relation of initial and final states in Lecture 3 is an example of a Kripke structure.

**Definition 3** (Computation structure). A Kripke structure \( K = (W, I, \circ, v) \) is called a computation structure if \( W \) is a finite set of states, \( I \subseteq W \) is a set of initial states, and every element \( s \in W \) has at least one direct successor \( t \in W \) with \( s \circ t \). A (computation) path in a computation structure is an infinite sequence \( s_0, s_1, s_2, s_3, \ldots \) of states \( s_i \in W \) such that \( s_i \circ s_{i+1} \) for all \( i \).

A computation structure is a special case of a Kripke structure. Although we could simply refer to both types of structures as Kripke structures, we will often refer to the general class of all such structures as “transition structures” when we don’t care to distinguish between the two.

## 3 Bounded Model Checking

The first approach that we consider computes an underapproximation of \( \tau(\alpha) \): not all possible traces will appear in the approximation, but all those that do appear are certain to be in the true trace semantics. In principle Bounded Model Checking (BMC)
can be used to verify arbitrary temporal properties, but it is most commonly used to check invariants of the form $\square \text{terminated} \rightarrow P$, and we will focus on this case for the remainder.

The way in which BMC approximates $\tau(\alpha)$ is by assuming that all loops in the program are unrolled to some fixed, pre-determined finite depth $k$. There are two useful ways to think about this operation. The first, which might have occurred to you naturally before having taken this course, is to transform the original program, which may contain loops, into a loop-free program using the bound $k$. Recall from a much earlier lecture the [unwind] axiom, which allows us to replace a loop with a conditional statement, within which is a copy of the original loop.

$$([\text{unwind}]) \ [\text{while}(Q) \alpha]P \leftrightarrow [\text{if}(Q) \{ \alpha; \text{while}(Q) \alpha \}]P$$

Axiom [unwind] tells us that it is perfectly acceptable when reasoning about a safety property to replace while statements with if statements in this way. To perform bounded model checking, we first apply [unwind] to each loop in the program up to $k$ times. When we are finished, we replace any remaining loops with skip statements (or equivalently, $?Q$).

Let’s see an example. Consider the following program, which doesn’t do anything useful but is simple enough to illustrate the key ideas here.

```plaintext
i := N;
while (0 ≤ x < N) {
  i := i - 1;
  x := x + 1;
}
```

Suppose that we want to check that $\square \text{terminated} \rightarrow 0 \leq i$ holds, up to a bound of $k = 2$. We begin by applying [unwind] twice to the loop. When we stop, we replace the remaining loop with an empty statement.

```plaintext
i := N;
if (0 ≤ x < N) {
  i := i - 1;
  x := x + 1;
  if (0 ≤ x < N) {
    i := i - 1;
    x := x + 1;
  }
}
```

With all of the loops removed from the program, verification is straightforward using the deductive techniques covered earlier in the semester: the formula we need to prove is just $[\alpha]0 \leq i$. In particular, we can apply [if], [;], and [:=] repeatedly until we are left with a term containing no modalities and literals involving only integer operations. In
the current example, we have the following after applying the necessary steps.

\begin{align*}
&\neg (0 \leq x < N) \rightarrow 0 \leq N \\
&\wedge (0 \leq x < N \rightarrow \neg (0 \leq x + 1 < N) \rightarrow 0 \leq N - 1) \\
&\wedge (0 \leq x < N \rightarrow 0 \leq x + 1 < N \rightarrow 0 \leq N - 2)
\end{align*}

If this formula is valid (which it is not), then the original property holds. In practice, these formulas are converted into satisfiability problems and given to a decision procedure.

Notice that there are three clauses in this formula, one for each possible path through the program after unwinding at \( k = 2 \). What bounded model checking essentially does is to “symbolically” evaluate each path through the program up to the unwinding depth. Each path corresponds to a conjunctive clause, so that if the formula is not valid, there will be a clause that the model checker can identify as being at fault. The corresponding path gives a counterexample, and a satisfying solution to its negation a valuation of the input variables that will violate the property.

In the example above, we see that the first clause is already invalid. We negate it to look for a satisfying solution:

\[ \neg (0 \leq x < N) \rightarrow 0 \leq N \leftrightarrow (\neg (0 \leq x < N) \wedge \neg (0 \leq N)) \]

A satisfying solution to the above is \( x = 0, N = -1 \). Notice that if we run the original program starting in a state that matches this assignment, then it terminates immediately without executing the loop, leaving \( i = -1 \).

**Limitations**

Because bounded model checking is an underapproximation, it might not consider some traces that are in the trace semantics of the program. This means that if it does not find a property violation, we cannot necessarily conclude that the program is bug-free. However, in some cases we can. Consider the following variation of the above example.

```plaintext
i := 3;
while (0 \leq x < 3) {
    i := i - 1;
    x := x + 1;
}
```

While a bound of \( k = 2 \) is insufficient to conclude that there are no bugs in this program, setting \( k = 3 \) is in fact sufficient. Furthermore, we can modify the unwinding process slightly so that if no bugs are found up to a particular depth, and we’ve chosen a sufficiently large enough \( k \), we will conclude as much. Likewise, if no bugs are found but we chose an inadequately large \( k \), we’ll know that to be the case as well.

The approach uses what are called unwinding assertions. Whereas before when we finished applying [unwind], we replaced the remaining loop with an empty statement, now we will replace it with a statement that violates safety if the unwinding is insufficient. In the above example, we would have the following for \( k = 2 \).
\[ i := 3; \]
\[ \text{if}(0 \leq x < 3) \{ \]
\[ \quad i := i - 1; \]
\[ \quad x := x + 1; \]
\[ \quad \text{if}(0 \leq x < 3) \{ \]
\[ \quad \quad i := i - 1; \]
\[ \quad \quad x := x + 1; \]
\[ \quad \quad \text{assert}(\neg(0 \leq x < 3)); \]
\[ \}
\]}

Although we haven’t talked about assertions before, we can model them using existing constructs and safety properties. To check that an assertion isn’t violated, we replace the `assert` statement with a corresponding conditional, which makes an assignment to a special variable whenever its condition is true.

\[ \text{error} := 0; \]
\[ i := 3; \]
\[ \text{if}(0 \leq x < 3) \{ \]
\[ \quad i := i - 1; \]
\[ \quad x := x + 1; \]
\[ \quad \text{if}(0 \leq x < 3) \{ \]
\[ \quad \quad i := i - 1; \]
\[ \quad \quad x := x + 1; \]
\[ \quad \quad \text{if}(0 \leq x < 3) \text{error} := 1; \]
\[ \}
\]}

We can then check the validity of the formula [\[ \alpha \text{error} = 0. \] In this case, the formula would be invalid, because \( x \) is at most 2 on the path containing the `assert`. This means that the unwinding assertion fails to hold, and so we should not conclude that the program is bug-free by unwinding up to \( k = 2 \).

### 4 Transition Structures for Programs

Moving on, we’ll now look at a different technique that builds an abstraction of the program’s reachable states. This abstraction will be in the form of a transition structure. Until now, we’ve been rather informal about the fact that the programs we’ve discussed all semester can be modeled as transition structures. Now let’s get serious about it and write the definition.

**Definition 4** (Transition Structure of a Program). Given a program \( \alpha \) over program states \( S \), let \( L \) be a set of locations given by the inductively-defined function \( \text{locs}(\alpha) \), \( \iota(\alpha) \) be the initial locations of \( \alpha \), and \( \kappa(\alpha) \) be the final locations of \( \alpha \):

- \( \text{locs}(x := e) = \{\ell_i, \ell_f\} \),
- \( \iota(x := e) = \{\ell_i\} \),
- \( \kappa(x := e) = \{\ell_f\} \)
• $\text{locs}(?Q) = \{\ell_i,\ell_f\}$,
  \begin{align*}
    \iota(?Q) &= \{\ell_i\}, \\
    \kappa(?Q) &= \{\ell_f\}
  \end{align*}

• $\text{locs}(\text{if}(Q)\ \text{then}\ \alpha\ \text{else}\ \beta) = \{\ell_i\} \cup \{\ell_t : \forall \ell \in \text{locs}(\alpha)\} \cup \{\ell_f : \forall \ell \in \text{locs}(\beta)\}$,
  \begin{align*}
    \iota(\text{if}(Q)\ \text{then}\ \alpha\ \text{else}\ \beta) &= \{\ell_i\}, \\
    \kappa(\text{if}(Q)\ \text{then}\ \alpha\ \text{else}\ \beta) &= \kappa(\alpha) \cup \kappa(\beta)
  \end{align*}

• $\text{locs}(\text{while}(Q)\ \alpha) = \{\ell_i,\ell_f\} \cup \{\ell_t : \forall \ell \in \text{locs}(\alpha)\}$,
  \begin{align*}
    \iota(\text{while}(Q)\ \alpha) &= \{\ell_i\}, \\
    \kappa(\text{while}(Q)\ \alpha) &= \{\ell_f\}
  \end{align*}

As a convenient shorthand, given a location $\ell$ we will write $\alpha_{\ell}$ to denote the statement associated with that location. The control flow transition relation $\epsilon(\alpha) \subseteq \text{locs}(\alpha) \times \text{progs} \times \text{locs}(\alpha)$ is given by:

• $\epsilon(x := e) = \{(\ell_i, x := e, \ell_f) : \ell_i \in \iota(x := e), \ell_f \in \kappa(x := e)\}$

• $\epsilon(?) = \{(\ell_i, ?Q, \ell_f) : \ell_i \in \iota(?) , \ell_f \in \kappa(?)\}$

• $\epsilon(\text{if}(Q)\ \text{then}\ \alpha\ \text{else}\ \beta) = \{(\ell_i, ?Q, \ell_{fi}) : \ell_i \in \iota(\cdot), \ell_{fi} \in \iota(\alpha)\} \cup \{(\ell_i, ?-Q, \ell_{fi}) : \ell_i \in \iota(\cdot), \ell_{fi} \in \iota(\beta)\} \cup \epsilon(\alpha) \cup \epsilon(\beta), \text{where}\ \iota(\cdot) = \iota(\text{if}(Q)\ \text{then}\ \alpha\ \text{else}\ \beta)$.  

  In other words, transitions go from the initial location $\ell_i$ to the initial locations of $\alpha$ and $\beta$.

• $\epsilon(\text{while}(Q)\ \alpha) = \{(\ell_i, \sim Q, \ell_f) : \ell_i \in \iota(\cdot), \ell_f \in \kappa(\cdot)\} \cup \{(\ell_i, ?Q, \ell_{ti}) : \ell_i \in \iota(\cdot), \ell_{ti} \in \iota(\alpha)\} \cup \{(\ell_i, ?\top, \ell_{ti}) : \ell_i \in \iota(\cdot), \ell_{ti} \in \kappa(\alpha)\} \cup \epsilon(\alpha)$.

  In other words, transitions go from the initial location $\ell_i$ to the initial location of $\alpha$, as well as from the initial location $\ell_i$ to the final location $\ell_f$ and the final location of the loop body to the initial location of the loop.

• $\epsilon(\alpha;\beta) = \epsilon(\alpha) \cup \epsilon(\beta) \cup \{(\ell_f, ?Q, \ell_i) : \ell_i \in \iota(\beta), \ell_f \in \kappa(\alpha)\}$

Notice that control flow transitions are associated with statements. Intuitively, the locations at the source of a transition correspond to the state immediately prior to executing a statement, and those at the destination the state immediately after. Then the transition structure $K_\alpha = (W, I, \leadsto, v)$ itself is given by:

• $W = \text{locs}(\alpha) \times \{S\}$, $I = \{\ell_i, \sigma) : \ell_i \in \iota(\alpha)\}$.  

• $\leadsto = \{(\ell, \sigma, (\ell', \sigma')) : (\ell, \beta, \ell'') \in \epsilon(\alpha)\}$, where $(\sigma, \sigma') \in \|\beta\|$.

  In other words, a transition in $K_\alpha$ is possible whenever there is a corresponding edge in $(\ell, \beta, \ell'') \in \epsilon(\alpha)$, and the program state components $\sigma, \sigma'$ in the pre- and post-states of the transition are in the semantics of $\beta$.  

---

15-414 Lecture Notes  Matt Fredrikson, André Platzer
• \( v(\langle \ell, \sigma \rangle) = \ell \land \bigwedge_{v \in \text{vars}} v = \sigma(v) \). In other words, states are labeled with formulas that describe their location and valuation. We assume that program locations correspond to literals in such formulas.

Definition 4 is consistent with Def. 1, in that if we start at an initial state and transcribe the program state component in the label of each state entered moving along a possible transition, then we will generate exactly the trace semantics of \( K_\alpha \). However, note that we will never obtain a computation structure using Def. 4 because the state space is infinite: there is at least one state in \( K_\alpha \) for each possible valuation of variables as integers. The model checking techniques that we have discussed all assume that the computation we work with is described by a computation structure, which seems to pose problems for us now.

**Example 1** Consider the program we looked at in the context of bounded model checking. Below is a version annotated with location labels.

\[
\ell_0: \quad i := N; \\
\ell_1: \quad \text{while}(0 \leq x < N) \{ \\
\ell_2: \quad i := i - 1; \\
\ell_3: \quad x := x + 1; \\
\ell_4: \quad \}
\]

We obtain the \( \epsilon \) transition relation according to Definition 4 below. Notice that the construction technically calls for another state after \( \ell_2 \), which transitions to \( \ell_3 \) on \( ?\top \). This is not necessary, and is only specified in Definition 4 to make the formalisation easier to understand. We omit it in the diagram below to keep the relation concise.

**Example 2** Consider the following example, which uses a variable \( L \) in an attempt at a simple mutual exclusion protocol.
L := 0;
C := 0;
while (t > 0) {
    b := *;
    if (b >= 0) {
        L := 1;
        C := C + 1;
        // critical section
    }
    if (C > 0)
        L := 0;
    t := t - 1;
}

This program uses nondeterminism to simulate the fact that a process may not be
granted a lock when requested, in the event that another process already holds it. We
begin by annotating the program with locations.

ℓ₀: \quad L := 0;
ℓ₁: \quad C := 0;
ℓ₂: \quad while (t > 0) {
    ℓ₃: \quad b := *;
    ℓ₄: \quad if (b >= 0) {
    ℓ₅: \quad L := 1;
    ℵ₆: \quad C := C + 1;
        // critical section
    ℵ₇: \quad }
    ℵ₈: \quad if (C > 0)
    ℵ₉: \quad L := 0;
    ℵ₁₀: \quad t := t - 1;
    ℵ₁₁: \quad }
 ℵ₁₃:

The control flow transitions, with guards, are shown below. Note that we can add a
self-loop to the final location ℵ₁₃ to ensure that all states in this structure have a post-
state.
The transitions we’ve constructed so far correspond to $\epsilon(\alpha)$. Now to construct $K_\alpha$, we need one state for each location paired with each possible program valuation. However, we cannot hope to compute such a structure in its entirety, or write it down because of its infinite size. To address this, we will need to approximate the infinite statespace of $K_\alpha$ with a finite one using a technique called *predicate abstraction*.

### 5 Predicate Abstraction

Bounded model checking approximates the statespace “from below”, by computing a subset of the reachable states and verifying that the ensuing traces always satisfy the property. In this sense, bounded model checking gives an underapproximation: any property violations that it finds are sure to be real, but because not all reachable states are explored, we might not discover some real violations.

Predicate abstraction is a different approach to the infinite statespace problem, which in a similar sense gives an *over*approximation. That is, it may “find” errors that do not correspond to real ones, but it will never miss an error that actually exists in the program. The main idea used in predicate abstraction is to merge states in $K_\alpha$ that have the same labeling of atomic propositions. This may not seem to get us very far.
at first, as the labels used in Definition 4 were the original source of the infinite statespace problem. However, by selecting the set of atomic propositions wisely, we can sidestep this problem while at the same time, in many cases, significantly reducing the overall number of states that need to be explored.

**By example.** Consider the mutual exclusion program from before. Crucial to this sort of protocol is that the lock be taken ($\ell_2$) and released ($\ell_3$) in proper order: a process that does not own a lock should not release it, as this could lead to violation of mutual exclusion safety.

To check this, we want to ensure what whenever the lock is taken by assigning $L := 1$ on $\ell_2$, it is currently the case that $L = 0$. Likewise, whenever the lock is released on $\ell_3$, then it must be that $L = 1$. This gives us two LTL safety properties.

$$\Box \ell_2 \rightarrow L = 0 \quad (1)$$
$$\Box \ell_4 \rightarrow L = 1 \quad (2)$$

In the above, we use the shorthand $\ell_i$ to denote any state $\langle \ell_i, \sigma \rangle$, for any $\sigma$. Likewise, $L = x$ denotes any state $\langle \ell, L = x \rangle$, for any $\ell$.

Let’s consider these formulas one at a time. In order to check (1), what states of $K_\alpha$ could we possibly need to explore? Before the first sequence of assignments are executed, $L$ and $C$ could take any values. It stands to reason that we must consider any initial state $s$ where $v(s) \models \ell_0$. But after executing these assignments, we know that both variables will take value 0, so we must only consider in addition at this stage states $s$ where $v(s) \models \ell_1 \land L = 0 \land C = 0$. Similarly, the only states that matter at $\ell_2$ are those where $v(s) \models \ell_2 \land b > 0$.

Following on these observations, we come to the central idea of predicate abstraction: find a set of atomic predicates and corresponding abstract labeling function that is concise but sufficient to capture all of the relevant traces in the program. We then merge all of the states in the “concrete” transition structure $K_\alpha$ that share the same abstract labeling into one, and allow transitions liberally. In particular, if $s$ and $s'$ are abstract states and $\tilde{v}$ an abstract labeling, then we draw a transition from $\tilde{s} \circ \tilde{s}'$ iff there are concrete states $s$ and $s'$ where $s \circ s'$, and additionally $\tilde{v}(s) = \tilde{s}, \tilde{v}(s') = \tilde{s}'$.

We will continue with predicate abstraction in the next lecture, picking up where we left off.