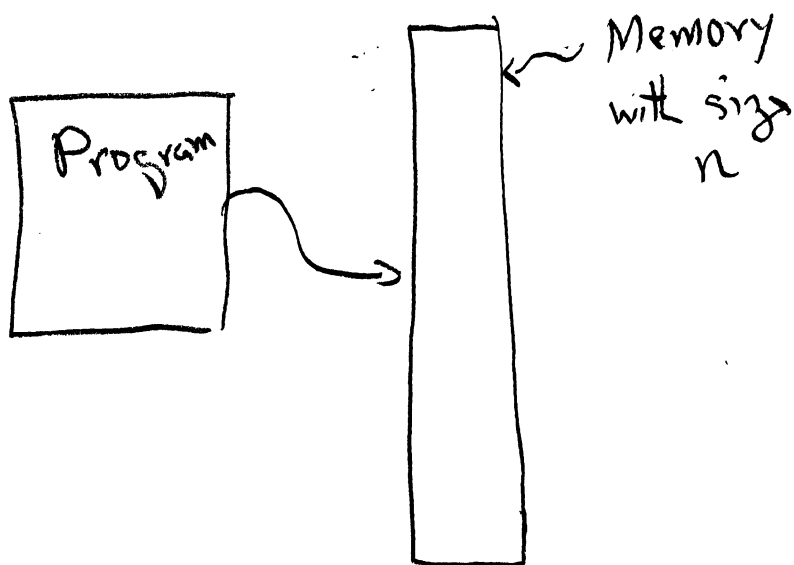


Parallel Algorithms

15-750
3/24/10

So far we have assumed the following
model: RAM model



unit time ops

$(+, \times, \div)$
 $(\log n \text{ bits})$
 $(\text{read/write into memory})$

A central model to describe
Graph Algorithms.

Other models:

- 1) Agents / ants
- 2) pointer machines

RAM is unrealistic as n goes to infinity.

- 1) speed of light (large size machines)
 - 2) quantum effects (small size machines)
-

Bottom Line: RAM

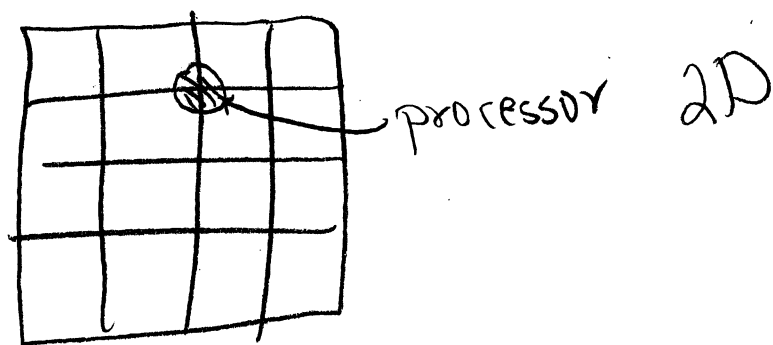
- 1) Many important algorithms were found using this model.
- 2) Most algorithms are coded in a RAM like language.
eg C

Parallel Models

Fixed connection machines
machines $\equiv$ infinite state machine

2) RAM

A) Cellular Arrays 1D, 2D, 3D



1940's von Neumann
60's, 70's algorithms for CA.
Alvy Ray Smith 1974

Highly connected models



1) Hypercube $\equiv (V, E)$ 1980's

$$V \equiv \{(a_1, \dots, a_m) \mid a_i \in \{0, 1\}\} \quad m = \log n$$

$$(a_1, \dots, a_m), (a_1, \dots, \bar{a}_i, \dots, a_m) \in E$$

2) Shuffle-exchange graph 1980's

$$V = \{(a_1, \dots, a_m) \mid a_i \in \{0, 1\}\}$$

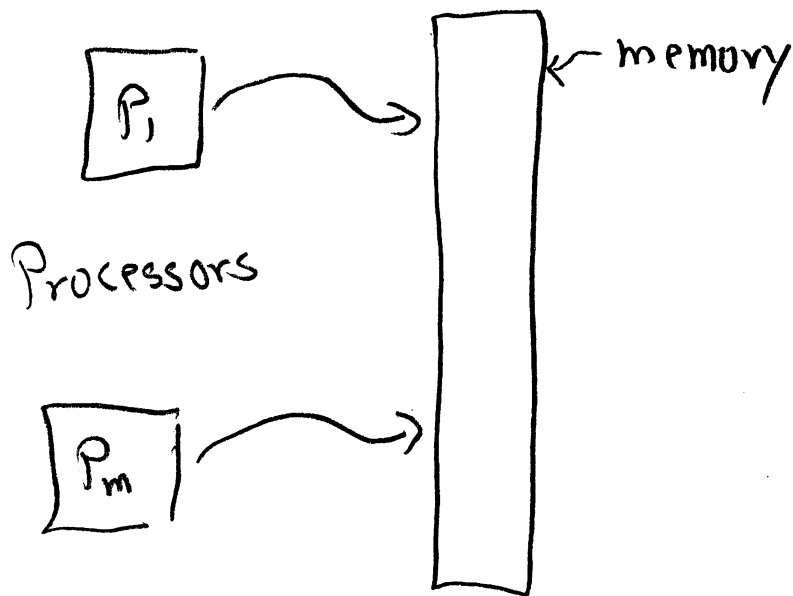
$$((a_1, \dots, a_m), (\bar{a}_1, a_2, \dots, a_m)) \in E$$

$$((a_1, \dots, a_m), (a_m, a_1, \dots, a_{m-1})) \in E$$

3) Randomly connected graphs
possible models of the brain!

Shared memory models

1) PRAM (Parallel Random Access Machine)



Unit time ops

$+$, $\times$, $\div$

read/write

Read

Write

Exclusive Concurrent

ER EW

CR CW

PRAM issues:

Penalty for CR on an ER machine?

1) Machine crashes!

2) garbage read!

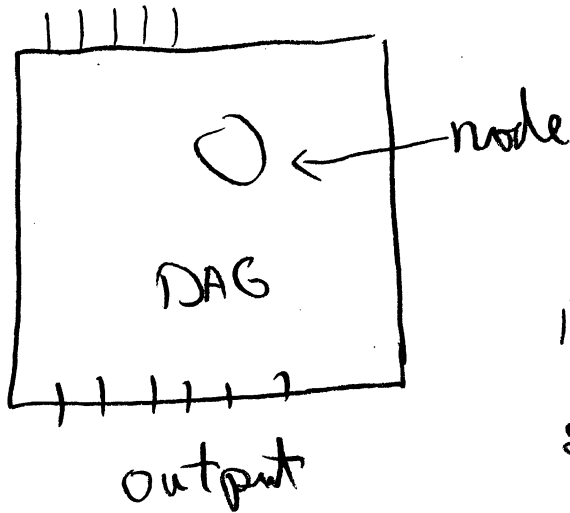
How is synchronization handled?

1) After each unit of time!

2) none!

Circuit Model

inputs in either bits or words



nodes 1) $\wedge, \vee, \neg$ gates

2) Arithmetic ops

1) Constant fan in.

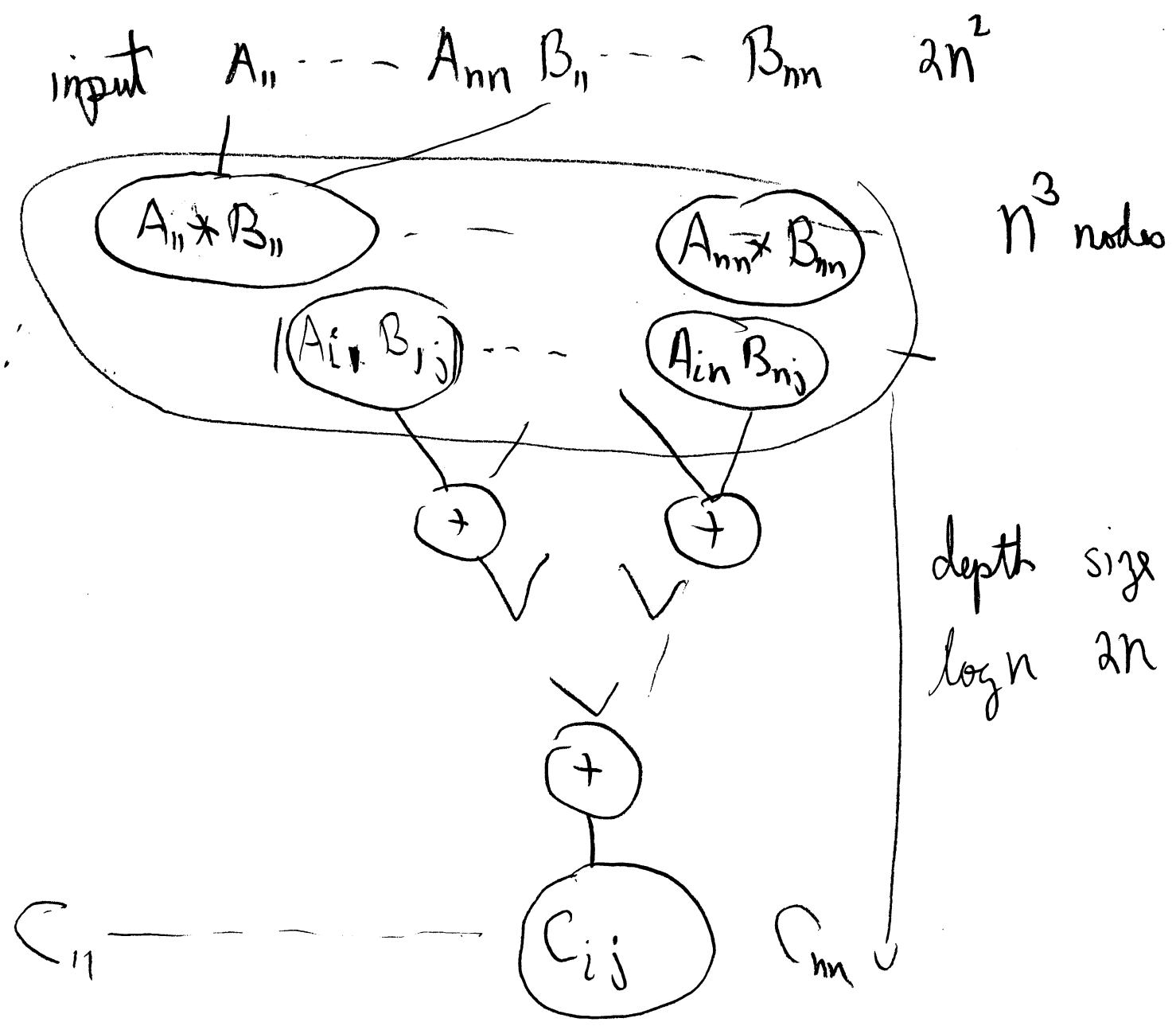
2) Arbitrary fan out.

Work $\equiv$ # nodes

Time $\equiv$ Longest path from input to output

Naive Matrix Multiply in the Circuit Model

$$C_{ij} = \sum_{k=1}^n A_{ik} B_{kj} \quad C = A \cdot B$$



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Totals: Work: $O(n^3)$
Time: $O(\log n)$

Naive MM on PRAM

$P \equiv \# \text{ processor}$ $T \equiv \text{Parallel time}$

1) CREW $P \equiv O(n^3)$ one processor / node
 $T \equiv O(\log n)$

Note: fan out $\equiv$ reads
fan in $\equiv$ writes

2) CREW $P \equiv O(n^3 / \log n)$
 $T \equiv O(\log n)$

each process does the work of
 $\log n$ virtual process.

3) EREW $P = n^3 / \log n$ $T \equiv O(\log n)$

Def $Work \equiv P \cdot T$

One must pay for each process
for life of the run.

Naive MM: $O(n^3)$ work $O(\log n)$ time

Claim: If we have $P < n^3 / \log n$ processors
then the Time $\equiv W/P$.

Strassen's Alg

Recall: recurrence

$$MM(n) = 7 MM(n/a) + Cn^2$$

↑
7 recursive
calls

↑
matrix
additions

$A+B$

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$$O(\log n)$$

we must pay for each call!

$$O(n^{2.81} \dots)$$

All-Prefix-Sums / Pre scan

Let $\oplus$ be an associative binary op
eg $+$, $\cdot$

Def: All-Prefix-Sums

input: $[a_0, \dots, a_{n-1}]$

output: $[a_0, a_0 \oplus a_1, a_0 \oplus a_1 \oplus a_2, \dots, a_0 \oplus \dots \oplus a_{n-1}]$

Pre scan

output $[I, a_0, a_0 \oplus a_1, \dots, a_0 \oplus \dots \oplus a_{n-2}]$

Application

Packing Memory

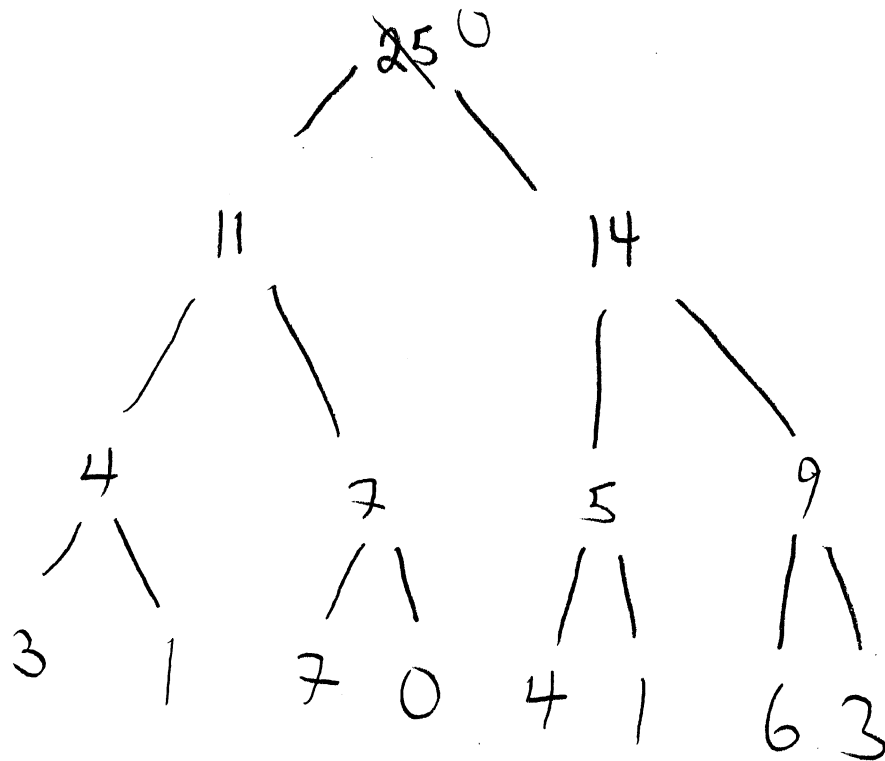
location	0	1	2	3	4	5	6	7
contents	∅	*	∅	∅	*	*	∅	*

0	1	2	3				
*	*	*	*	∅	∅		

Diagram illustrating memory packing. The first table shows the original memory layout with locations 0-7 and their contents. The second table shows the packed memory layout, where only the non-empty contents (locations 1, 2, 3, 4) are stored in a new sequence, starting from location 0. Arrows indicate the mapping from the original locations to the packed locations: location 1 maps to packed location 0, location 2 maps to packed location 1, location 3 maps to packed location 2, and location 4 maps to packed location 3.

input: (3, 1, 7, 0, 4, 1, 6, 3) (+) addition

- Alg 1) Compute Tree of partial sums
 2) set root to zero
 3) DOWN!



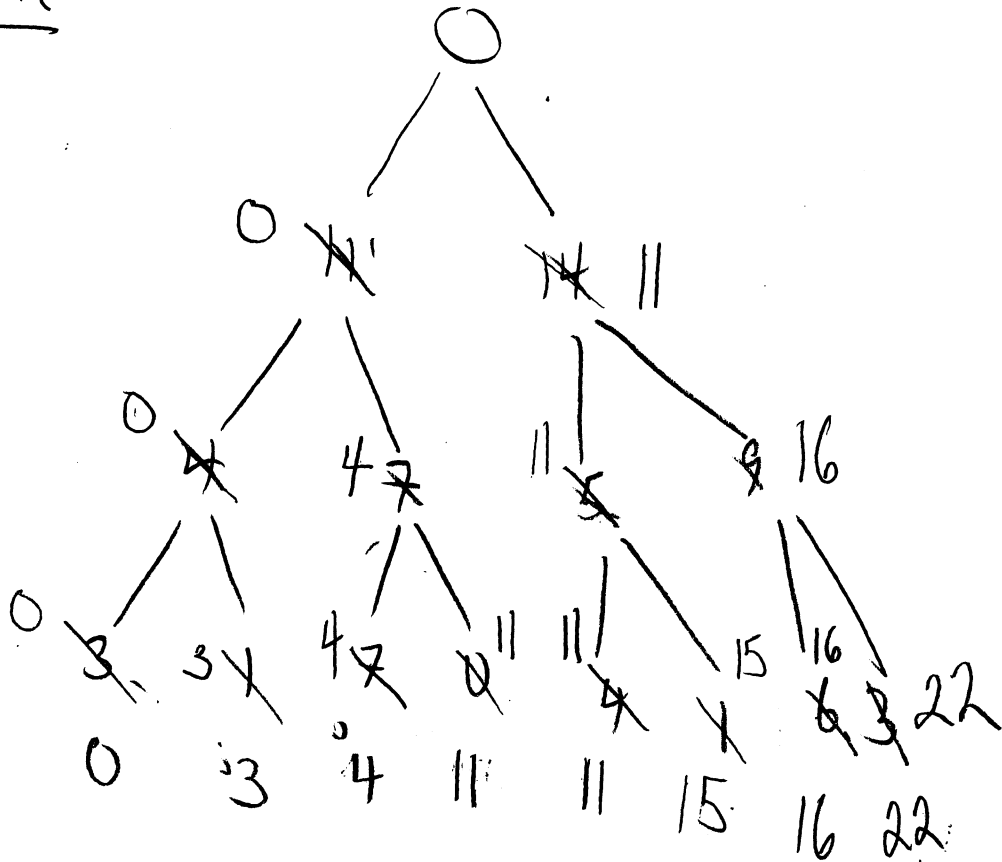
3) Down!

a) Right-Child $\leftarrow$ Parent $\oplus$ Left-Child

b) Left-Child $\leftarrow$ Parent

Down

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$$T(n) = O(\log n)$$

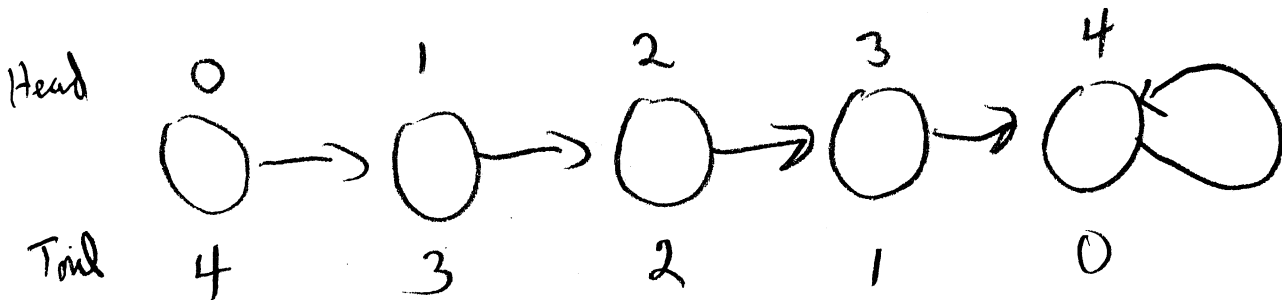
$$W(n) = O(n)?$$

List Ranking

input: linked-list

output: A mark on each node s.t.

mark = distance from head or
= distance to tail



Assume!

- 1) pointers are in consecutive memory
- 2) we know location of head & tail
- 3) pointers in arbitrary order.

Wyllie's Alg

Init rank(!) := 1 ; rank(tail) = 0

Init while succ(head) ≠ nil do

if succ(!) ≠ nil do

rank(!) := rank(!) ⊕ rank(succ(!))

succ(!) := succ(succ(!))

fi

processors = n

Goal

Time = $O(\lg n)$

$O(\lg n)$

Work = $O(n \lg n)$

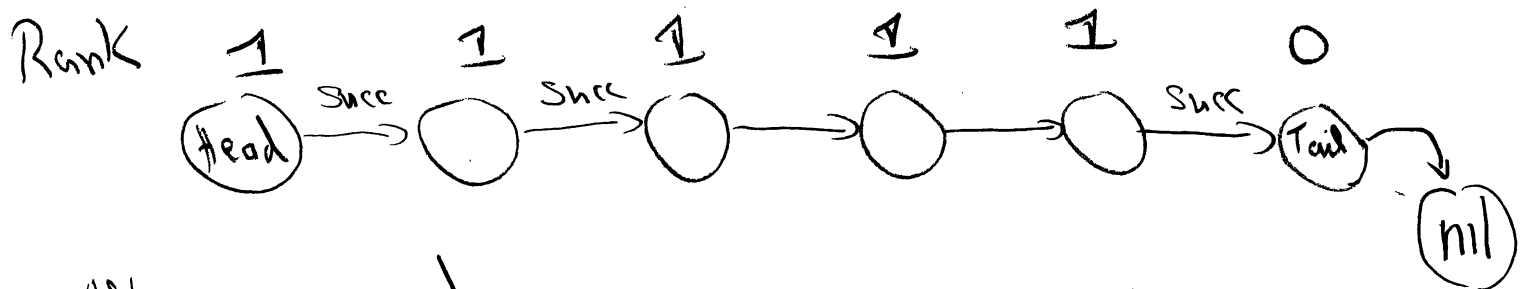
$O(n)$

CREW model

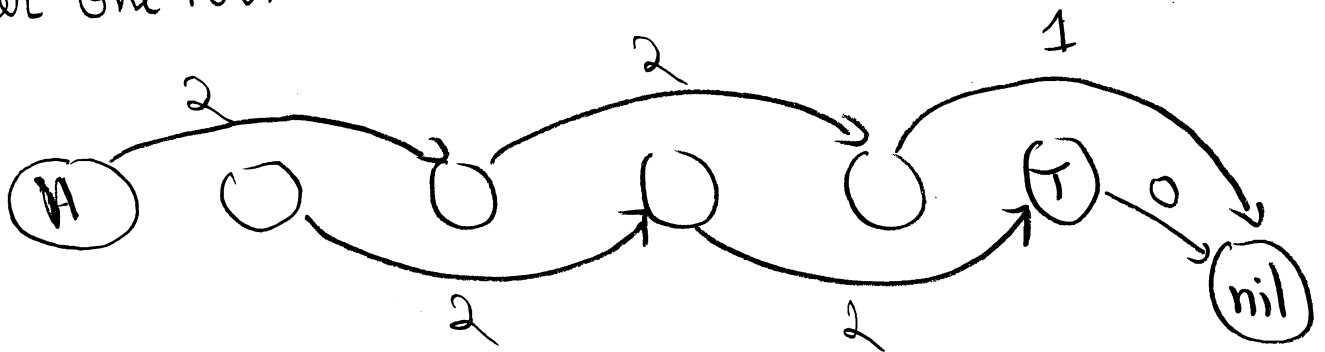
Wyllie's Alg

16A

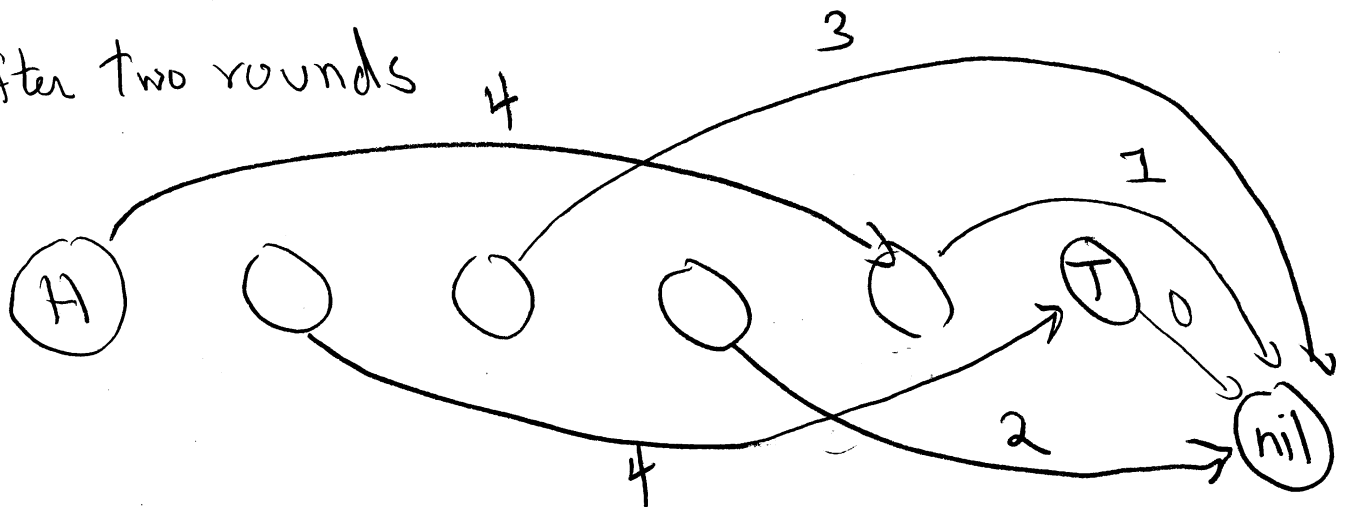
Initial



After one round



After two rounds

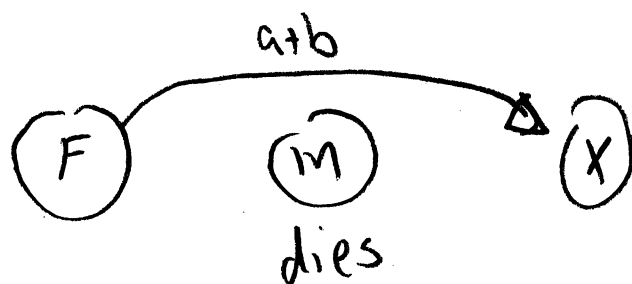


Random-Mate

1) Contraction Phase

1) Each live node randomly picks a sex

2) If $(F) \xrightarrow{a} (M) \xrightarrow{b} (X)$ then



3) Stop when head points to nil.
(only head is live)

Thm The contraction phase stops in $c \log n$ rounds with high prob.

pt Let $P_i \equiv$ Event that node i is still live after one round

Note node i not head then $\text{Prob}[P_i] = 3/4$

Let $P_i^K \equiv$ Event. that node i still live after K rounds.

Note: $\text{Prob}[P_i^K] = (3/4)^K$ i not head.

$$\text{Set } K = \lfloor c \log_{4/3} n \rfloor$$

$$\text{Prob}[P_i^K] = \frac{1}{(4/3)^K} \leq \frac{1}{(4/3)^{c \log_{4/3} n}} = \frac{1}{n^c}$$

Let $P^K \equiv$ Event that some non-head node
is still live.

Assume that $node_0$ is the head.

$$P^K = P_1^K \cup P_2^K \cup \dots \cup P_n^K$$

$$\text{Prob}[P^K] = \text{Prob}[P_1^K \cup \dots \cup P_n^K]$$

$$\leq \text{Prob}[P_1^K] + \dots + \text{Prob}[P_n^K]$$

$$\leq n \cdot \frac{1}{nc} = \frac{1}{n^{c-1}}$$

If we set $c=2$ then the contraction phase
stops with $\text{prob} \leq 1/n$.

In the expansion phase we run contraction phase "backwards".

