### Lectures 25-26 Memory Hierarchy Optimizations & Locality Analysis

CS745: Memory Hierarchy Optimizations

Todd C. Mowry

### Optimizing Cache Performance

- Things to enhance:
  - · temporal locality
  - · spatial locality
- Things to minimize:
  - · conflicts (i.e. bad replacement decisions)

What can the *compiler* do to help?

C5745: Memory Hierarchy Optimizations

-3-

Carnegie Mellon
Todd C. Mowry

### Caches: A Quick Review

- How do they work?
- Why do we care about them?
- What are typical configurations today?
- What are some important cache parameters that will affect performance?

CS745: Memory Hierarchy Optimizations

2

rnegie Mellon

Todd C. Mowry

### Two Things We Can Manipulate

- Time
  - · When is an object accessed?
- Space
  - · Where does an object exist in the address space?

How do we exploit these two levers?

CS745: Memory Hierarchy Optimizations

-4-

Carnegie Mellon
Todd C. Mowry

### Time: Reordering Computation

- What makes it difficult to know when an object is accessed?
- How can we predict a better time to access it?
  - · What information is needed?
- How do we know that this would be safe?

C5745: Memory Hierarchy Optimizations

-5-

Todd C. Mowry

### Types of Objects to Consider

- Scalars
- Structures & Pointers
- Arrays

CS745: Memory Hierarchy Optimizations

-

Carnegie Mellon
Todd C. Mowry

### Space: Changing Data Layout

- What do we know about an object's location?
  - scalars, structures, pointer-based data structures, arrays, code, etc.
- How can we tell what a better layout would be?
  - · how many can we create?
- To what extent can we safely alter the layout?

CS745: Memory Hierarchy Optimizations

Todd C. Mowry

int x:

double y;

foo(int a){

int i;

### Scalars

-8-

- Locals
- Globals
- Procedure arguments
- Is cache performance a concern here?
- If so, what can be done?

CS745: Memory Hierarchy Optimizations

Carnegie Mellon
Todd C. Mowry

```
Structures and Pointers

struct {

• What can we do here?

• within a node

• across nodes

• across nodes

• What limits the compiler's ability to optimize here?

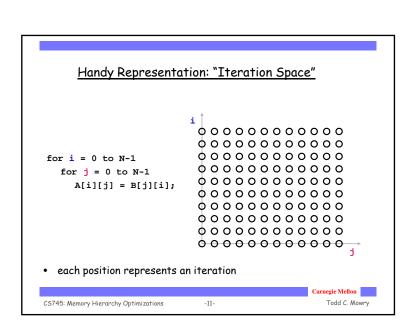
Carnegie Mellon

CS745: Memory Hierarchy Optimizations

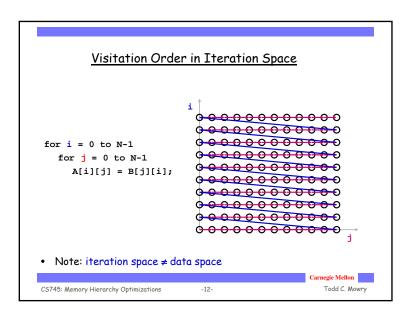
-9-

Carnegie Mellon

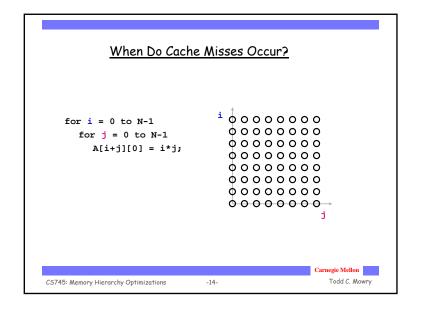
Todd C. Mowry
```



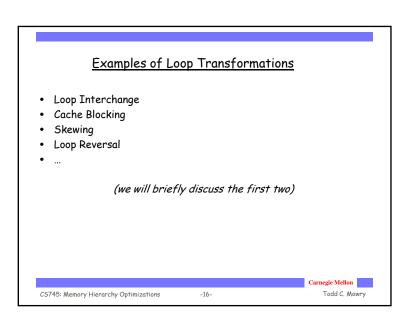
# Arrays double A[N][N], B[N][N]; for i = 0 to N-1 for j = 0 to N-1 A[i][j] = B[j][i]; • usually accessed within loops nests makes it easy to understand "time" • what we know about array element addresses: start of array? relative position within array Carregle Mellon Cod C. Mowry

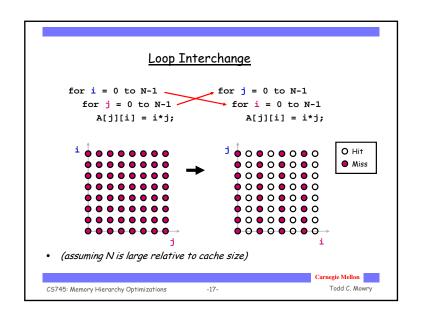


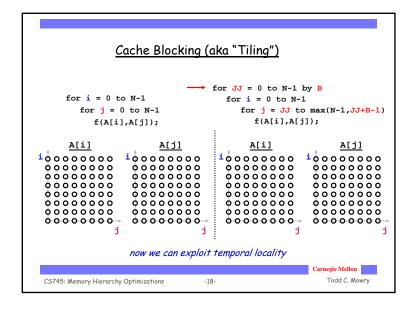
```
When Do Cache Misses Occur?
           for i = 0 to N-1
             for j = 0 to N-1
               A[i][j] = B[j][i];
                      i 0000000
    0000000
     0000000
                        0000000
     0000000
                        0000000
     0000000
                        0000000
     0000000
                        0000000
     0000000
                        0000000
     0000000
                        0000000
     0000000
                        0000000
                                 Carnegie Mellon
                                  Todd C. Mowry
CS745: Memory Hierarchy Optimizations
```

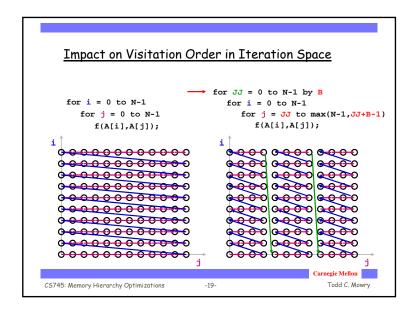


# Optimizing the Cache Behavior of Array Accesses • We need to answer the following questions: • when do cache misses occur? • use "locality analysis" • can we change the order of the iterations (or possibly data layout) to produce better behavior? • evaluate the cost of various alternatives • does the new ordering/layout still produce correct results? • use "dependence analysis" Carnegle Mellon Corrected Mellon Todd C. Mowry









```
Cache Blocking in Two Dimensions
                               for JJ = 0 to N-1 by B
                                 for KK = 0 to N-1 by B
                                   for i = 0 to N-1
 for i = 0 to N-1
                                     for j = JJ to max(N-1,JJ+B-1)
  for j = 0 to N-1
                                       for k = KK to max(N-1,KK+B-1)
    for k = 0 to N-1
                                         c[i,k] += a[i,j]*b[j,k];
      c[i,k] += a[i,j]*b[j,k];

    brings square sub-blocks of matrix "b" into the cache

• completely uses them up before moving on
                                                        Carnegie Mellon
CS745: Memory Hierarchy Optimizations
                                                           Todd C. Mowry
```

### Predicting Cache Behavior through "Locality Analysis"

- · Definitions:
  - Reuse
    - · accessing a location that has been accessed in the past
  - Locality
    - · accessing a location that is now found in the cache
- Key Insights
  - · Locality only occurs when there is reuse!
  - · BUT, reuse does not necessarily result in locality.
    - why not?

CS745: Memory Hierarchy Optimizations

CS745: Memory Hierarchy Optimizations

-21-

Carnegie Mellon
Todd C. Mowry

Carnegie Mellon

Todd C. Mowry

### Steps in Locality Analysis

- 1. Find data reuse
  - · if caches were infinitely large, we would be finished
- 2. Determine "localized iteration space"
  - set of inner loops where the data accessed by an iteration is expected to fit within the cache
- 3. Find data locality:

CS745: Memory Hierarchy Optimizations

• reuse  $\cap$  localized iteration space  $\Rightarrow$  locality

Carnegie Mello

,

Todd C. Mowry

### Types of Data Reuse/Locality for i = 0 to 2 for j = 0 to 100 O Hit A[i][j] = B[j][0] + B[j+1][0];Miss A[i][j] B[j+1][0] B[j][0] i • 0 • 0 • 0 • 0 i 0 0 0 0 0 0 0 0 i 00000000 0000000 0000000 • 0 • 0 • 0 • 0 → **•••••** •000000 Temporal Spatial Group

-23-

### Reuse Analysis: Representation

• Map *n* loop indices into *d* array indices via array indexing function:

$$\begin{split} \vec{f}(\vec{\imath}) &= H\vec{\imath} + \vec{c} \\ \text{A[i][j]} &= \text{A}\left(\left[ \begin{array}{cc} 1 & 0 \\ 0 & 1 \end{array} \right] \left[ \begin{array}{c} i \\ j \end{array} \right] + \left[ \begin{array}{c} 0 \\ 0 \end{array} \right] \right) \\ \text{B[j][0]} &= \text{B}\left(\left[ \begin{array}{cc} 0 & 1 \\ 0 & 0 \end{array} \right] \left[ \begin{array}{c} i \\ j \end{array} \right] + \left[ \begin{array}{c} 0 \\ 0 \end{array} \right] \right) \\ \text{B[j+1][0]} &= \text{B}\left(\left[ \begin{array}{cc} 0 & 1 \\ 0 & 0 \end{array} \right] \left[ \begin{array}{c} i \\ j \end{array} \right] + \left[ \begin{array}{c} 1 \\ 0 \end{array} \right] \right) \end{split}$$

CS745: Memory Hierarchy Optimizations

Carnegie Mellon

 $\mathsf{Todd}\; \mathit{C}.\; \mathsf{Mowry}$ 

### Finding Temporal Reuse

• Temporal reuse occurs between iterations  $\vec{\imath}_1$  and  $\vec{\imath}_2$  whenever:

$$H\vec{i}_1 + \vec{c} = H\vec{i}_2 + \vec{c}$$
  
 $H(\vec{i}_1 - \vec{i}_2) = \vec{0}$ 

• Rather than worrying about individual values of  $\vec{i}_1$  and  $\vec{i}_2$  we say that reuse occurs along direction vector  $\vec{r}$  when:

$$H(\vec{r}) = \vec{0}$$

• Solution: compute the *nullspace* of *H* 

CS745: Memory Hierarchy Optimizations

-25

Carnegie Mellon
Todd C. Mowry

### Temporal Reuse Example

• Reuse between iterations  $(i_1, j_1)$  and  $(i_2, j_2)$  whenever:

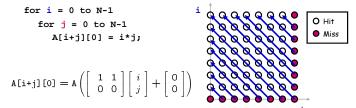
$$\begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} i_1 \\ j_1 \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} i_2 \\ j_2 \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$
$$\begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} i_1 - i_2 \\ j_1 - j_2 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \end{bmatrix}$$

- True whenever j<sub>1</sub> = j<sub>2</sub>, and regardless of the difference between i<sub>1</sub> and i<sub>2</sub>.
  - i.e. whenever the difference lies along the nullspace of  $\begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix}$ , which is span{(1,0)} (i.e. the outer loop).

,

Todd C. Mowry

### More Complicated Example



• Nullspace of  $\begin{bmatrix} 1 & 1 \\ 0 & 0 \end{bmatrix}$  = span{(1,-1)}.

C5745: Memory Hierarchy Optimizations

-27-

Carnegie Mellon
Todd C. Mowry

### Computing Spatial Reuse

- Replace last row of Hwith zeros, creating Hs
- Find the nullspace of  $H_s$

CS745: Memory Hierarchy Optimizations

• Result: vector along which we access the same row

CS745: Memory Hierarchy Optimizations

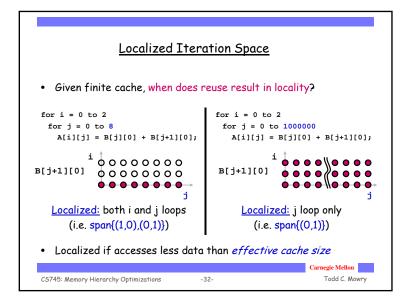
-28-

Carnegie Mellon
Todd C. Mowry

### Computing Spatial Reuse: Example for i = 0 to 2for j = 0 to 100 A[i][j] = B[j][0] + B[j+1][0]; $A[i][j] = A\left(\begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}\begin{bmatrix} i \\ j \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \end{bmatrix}\right)$ • $H_s = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix}$ • Nullspace of $H_s = \text{span}\{(0,1)\}$ • i.e. access same row of A[i][j] along inner loop Carnegie Mellon Todd C. Mowry

# Group Reuse for i = 0 to 2 for j = 0 to 100 A[i][j] = B[j][0] + B[j+1][0]; • Only consider "uniformly generated sets" • index expressions differ only by constant terms • Check whether they actually do access the same cache line • Only the "leading reference" suffers the bulk of the cache misses Carnegie Mellon CS745: Memory Hierarchy Optimizations -31Todd C. Mowry

# Computing Spatial Reuse: More Complicated Example for i = 0 to N-1 for j = 0 to N-1 A[i+j] = i\*j; $A[i+j] = A\left(\begin{bmatrix} 1 & 1 \end{bmatrix}\begin{bmatrix} i \\ j \end{bmatrix} + \begin{bmatrix} 0 \end{bmatrix}\right)$ • Nullspace of $H = \text{span}\{(1,0),(0,1)\}$ • Nullspace of $H_s = \text{span}\{(1,0),(0,1)\}$ • Carnegie Mellon Todd C. Mowry



### Computing Locality

- Reuse Vector Space ∩ Localized Vector Space ⇒ Locality Vector Space
- Example: for i = 0 to 2 for j = 0 to 100 A[i][j] = B[j][0] + B[j+1][0];
- If both loops are localized:
  - $span\{(1,0)\} \cap span\{(1,0),(0,1)\} \Rightarrow span\{(1,0)\}$
  - i.e. temporal reuse does result in temporal locality
- If only the innermost loop is localized:
  - $span\{(1,0)\} \cap span\{(0,1)\} \Rightarrow span\{\}$
  - · i.e. no temporal locality

CS745: Memory Hierarchy Optimizations

Carnegie Mellon

Todd C. Mowry