Parallel Programming Models and Languages


Exploiting Parallelism on GPUs


Scheduling for Parallelism


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Exploiting Heterogeneous Architectures


Architectural Support for Security


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Warehouse-Scale Computing


Optimizing Power and Energy


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Caches and Memory Hierarchies


Cache Coherence


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Memory Ordering


• Abhayendra Singh, Daniel Marino, Satish Narayanasamy, Todd Millstein, and Madan Musuvathi. “Efficient processor support for DRFy, a memory model with exceptions,” in Proceedings of the sixteenth international conference on Architectural support for programming languages and operating systems (ASPLOS ’11).


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