

15-740 Computer Architecture

Fall 2007 Schedule

Table 1: CS 740, Fall 2007, Tentative Schedule.

Class	Date	Day	Topic	Reading	Assignments
1	9/11	Tue	Performance & Technology	H&P Ch. 1	
2	9/12	Wed	x86 Programming	H&P App.. J.3	
3	9/13	Thu	RISC vs. CISC Instruction Sets	H&P App. B & J	#1 Out
4	9/18	Tue	The Memory Hierarchy	H&P Ch. 5	
5	9/19	Wed	Intro to Parallel Architecture	H&P Ch. 4.1, CSG Ch. 1	
6	9/20	Thu	Parallel Programming I	CSG Ch. 2	
7	9/25	Tue	Parallel Programming II	CSG Ch. 3 & 4	
8	9/26	Wed	Cache Coherence I	H&P Ch. 4.2-4.3, CSG Ch. 5	#1 Due, #2 Out
9	9/27	Thu	Cache Coherence II	H&P Ch. 4.4, CSG Ch. 5	
10	10/2	Tue	Memory Consistency	H&P Ch. 4.6, CSG Ch. 6	
11	10/3	Wed	Thread-Level Speculation	Handouts	
12	10/4	Thu	Transactional Memory	Handouts	
13	10/9	Tue	<i>Recent Research on Architecture I</i>	Handouts	
	10/10	Wed	Exam I		
14	10/11	Thu	<i>Recent Research on Architecture II</i>	Handouts	
15	10/16	Tue	<i>Recent Research on Architecture III</i>	Handouts	#2 Due
16	10/17	Wed	Synchronization	H&P Ch. 4.5, CSG Ch. 5	
17	10/18	Thu	Interconnection Networks	H&P App. E, CSG Ch. 10	
	10/23	Tue	<i>Class Cancelled</i>		
18	10/24	Wed	Virtual Memory	H&P Ch. 5.4, App. C.4-C.5	Project Proposal
19	10/25	Thu	Basic Pipelining	H&P Ch. 2.1, App. A.1	
20	10/30	Tue	Pipelining Hazards	H&P App. A.2-A.6	
21	10/31	Wed	Instruction-Level Parallelism	H&P Ch. 2.2-2.10	
	11/8	Thu	Exam II		
	11/20	Tue			Project Milestone
	12/4	Tue			Project Due
	12/6	Thu	<i>Project Poster Session</i>		