

Final Exam Review

15/18-213: Introduction to Computer Systems

Recitation 1111

Dec. 15, 2011

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Section J, Mon 11:30-12:20, WEH 4709

Outline

- Proxy aftermath
- Final Exam
 - Logistics
 - Review
 - Topics

Proxy

- You've completed the final lab of 213. Congratulations!
- Demos (Meet with a TA for a half hour)
 - Make sure you are signed up.
 - Know how you will prove the correctness of the (4) required elements. (See writeup)

Final Exam - Logistics

- Fri Dec 16, 2011
 - 8:30am-11:30am
- 30% of your course grade
- Closed book
- You may bring 2 double-sided sheets for the final

Final Exam - Review

- You should know:

Everything from Chapters 1-3 and Chapters 6-12 in the textbook

- What to do:
 - Review the lecture slides
 - Read the book
 - Do the past exams
 - **Understand** the labs

[Subset of] Final Exam Topics

- Bitops, Floating point
- Machine code, x86/x86-64 Assembly, gdb
- Stack discipline
- Memory Hierarchy
- Threads vs. Processes, I/O, Signals
- Virtual Memory, TLB
 - Know how address translation works
- Dynamic Memory
- Networks
- Synchronization & Concurrency

[Subset of] Final Exam Topics

- Can you:
 - Simulate a cache?
 - Simulate malloc/free and coalescing?
 - Draw a process tree?
 - Manually translate a virtual address?

Questions?