Carnegie Mellon University
School of Computer Science
Reconfigurable Computing Seminar

15-828/18-847, Spring 1998 | Questionnaire | January 12

To assist us in planning the course please answer the following questions and return questionnaire outside Seth's office (7122 WeH) by 1/14.

1. Personal

(1) Name:

(2) Email:

(3) Year (E.g., Senior, Grad-2):

(4) Department:

(5) Degree Goal:

(6) Research advisor (if you have one):

(7) Current research project (if there is one):

(8) Research interests:

2. Course Information

(9) Taking this class for: **credit** **audit**

(10) Other courses you are taking this semester?

(11) Why do you want to take this course?

(12) What do you want to get out of this course?

3. General Background

(13) Please list the *compiler* courses you have taken:

(14) Please list the *architecture* courses you have taken:

(15) Please list the *VLSI* courses you have taken:

(16) Please list the *CAD* courses you have taken:

(17) Please list the *DSP* courses you have taken:

(18) Please list the *Parallel Computing* courses you have taken:

(19) What is the largest software project you have worked on?

(20) What is the largest hardware/VLSI project you have worked on?

(21) Please describe any application areas in which you are interested, e.g., computer arithmetic, cryptography, image processing, genetic algorithms.

4. FPGA Background

For each class of tools, please list your level of experience.

(22) Hardware Description Languages (E.g., Verilog, VHDL) **alot** **some** **none**

(23) Synthesis (E.g., Synopsys, Synplicity) **alot** **some** **none**

(24) Physical Design (E.g., XDM, XACT) **alot** **some** **none**

(25) Are there other tools relating to FPGA/RC design that you have used?

(26) What is the largest FPGA project you have worked on?

5. Equipment

(27) Do you have access to a PC running: **Windows-95** **Windows-NT**

(28) Do you have access to a workstation: **Sparc-Solaris** **PowerPC-AIX**